


Undecidability of Semi-Unification on a Napkin

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Abstract

Semi-unification (unification combined with matching) has been proven undecidable by Kfoury, Tiurnyn, and Urzyczyn in the 1990s. The original argument reduces Turing machine immortality via Turing machine boundedness to semi-unification. The latter part is technically most challenging, involving several intermediate models of computation.

This work presents a novel, simpler reduction from Turing machine boundedness to semi-unification. In contrast to the original argument, we directly translate boundedness to solutions of semi-unification and vice versa. In addition, the reduction is mechanized in the Coq proof assistant, relying on a mechanization-friendly stack machine model that corresponds to space-bounded Turing machines. Taking advantage of the simpler proof, the mechanization is comparatively short and fully constructive.

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Supplementary Material <https://github.com/uds-psl/2020-fscd-semi-unification>

1 Introduction

In the 1980s it was an actively studied, long-standing open problem whether the combination of first-order unification and matching, both of which are decidable problems, is decidable. This problem, called *semi-unification*, is: given a finite set of pairs (σ, τ) of first-order terms, is there a valuation φ of term variables such that for each pair (σ, τ) we have $\psi(\varphi(\sigma)) = \varphi(\tau)$ for some valuation ψ of term variables?

Semi-unification is directly related [10, 16] to type inference in an extension of the Hindley–Milner type system [11, 19] (cf. the standard **ML** [20] programming language), which allows for polymorphic recursion [21]. Therefore, computational properties of semi-unification translate to type inference capabilities for polymorphic functional programming languages, affecting programming language design. For a broad overview over properties of semi-unification the reader is referred to [17, 13].

In the 1990s Kfoury, Tiurnyn, and Urzyczyn have shown that semi-unification is undecidable [15, 17]. This negative result motivated exploration of decidable fragments of semi-unification (for an overview see [18]). The original undecidability proof is quite sophisticated, reflecting the inherent intricacy of the semi-unification problem. It involves Turing machine immortality, symmetric intercell Turing machine boundedness, path equation derivability, and termination of a redex contraction procedure for semi-unification. Therefore, it is challenging to verify the original proof down to the last detail, let alone mechanize it in a proof assistant. Additionally, the original argument uses König’s lemma and it is not obvious whether it can be presented constructively.

This work contributes to a better understanding of semi-unification in three aspects. First, we present a simpler proof for the undecidability of semi-unification. The presented technical argument connects an undecidable machine property (in immediate correspondence with Turing machine boundedness) to solutions of semi-unification in a direct way. The key contribution regarding this aspect is the function ζ (Definition 41) that constructs solutions



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for semi-unification instances. Second, we mechanize [2] (ca. 1500 lines of code in the Coq [5] proof assistant) the presented argument, leaving little room for doubt regarding its correctness. Third, König’s lemma in the original argument is replaced by the fan theorem. The provided mechanization reveals full constructivity of the remaining reasoning.

Proof Synopsis

First, we reduce Turing machine immortality [12] (is there a diverging configuration?) to a uniform boundedness problem for stack machines (is there a uniform bound on the number of reachable configurations?). The considered, restricted class of stack machines, which we call *simple*, is a mechanization-friendly presentation of space-bounded Turing machines.

Second, given a simple stack machine \mathcal{M} , we encode each instruction of \mathcal{M} as a semi-unification constraint, thereby constructing a finite set of constraints \mathcal{C} . Each state of \mathcal{M} is a variable in \mathcal{C} . The resulting constraints are of restricted shape, which we also call *simple*.

Third, if \mathcal{M} is uniformly bounded, then we interpret configurations of \mathcal{M} as first-order terms using an uncomplicated, computable function ζ . Most importantly, the interpretation of an empty stack configuration in each state of \mathcal{M} is a solution for \mathcal{C} .

Fourth, if \mathcal{C} has a solution φ , then we construct a uniform bound for \mathcal{M} from the maximal depth of the syntax trees in the range of φ .

Fifth, the above constitutes an undecidability proof of semi-unification for simple constraints and immediately implies undecidability of semi-unification.

Key aspects of all of the above points, except the third, also appear in [17]. However, the technically most challenging aspect of [17], which we are able to simplify, is to show that a solution for a constructed semi-unification instance exists. Specifically, the function ζ is the main contribution of this work towards a better understanding of semi-unification.

Organization of the Paper

Section 2 contains preliminary properties of simple semi-unification (Problem 15), which is a restriction of semi-unification that transports undecidability (Theorem 1).

Section 3 contains preliminary properties of simple stack machines (Definition 16), which are equivalent to space-bounded Turing machines. Additionally, uniform boundedness of deterministic simple stack machines (Problem 26) is shown undecidable (Theorem 2).

Section 4 contains a reduction from uniform boundedness of deterministic simple stack machines to simple semi-unification. Correctness of the reduction (Lemma 48 and Lemma 45) results in undecidability of semi-unification (Theorem 4).

Section 5 provides an overview over the mechanization [2] of the presented reduction.

Section 6 concludes and lists potential future work.

2 Semi-unification Preliminaries

This section, following [17], recollects the basic definition and properties of semi-unification (Problem 3).

► **Definition 1 (Terms \mathbb{T}).** Let \mathbb{V} be a countably infinite set of *variables* ranged over by α, β, γ . The set of *terms* \mathbb{T} , ranged over by σ, τ , is given by the grammar

$$\sigma, \tau \in \mathbb{T} ::= \alpha \mid \sigma \rightarrow \tau$$

► **Definition 2 (Valuation $(\varphi), (\psi)$).** A *valuation* $\varphi : \mathbb{V} \rightarrow \mathbb{T}$ assigns terms to variables, and is tacitly lifted to terms.

► **Problem 3** (Semi-unification (SU)). Given a finite set $\{s_1 \leq_1 t_1, \dots, s_n \leq_n t_n\}$ of *indexed inequalities*, do there exist valuations $\varphi, \psi_1, \dots, \psi_n : \mathbb{V} \rightarrow \mathbb{T}$ such that $\psi_i(\varphi(s_i)) = \varphi(t_i)$ holds for $i = 1 \dots n$?

Compared to first-order unification, semi-unification is non-structural. In a solvable instance, the left-hand side of an indexed inequality may even appear as subterm of the right-hand side (Example 4).

► **Example 4.** The indexed inequalities $\{\alpha \leq_1 \alpha \rightarrow \beta, \alpha \rightarrow \alpha \leq_2 \beta\}$ are solved by the valuations $\varphi = \{\alpha \mapsto \alpha, \beta \mapsto \alpha \rightarrow \alpha\}$, $\psi_1 = \{\alpha \mapsto \alpha \rightarrow (\alpha \rightarrow \alpha)\}$, and $\psi_2 = \{\alpha \mapsto \alpha\}$ because

$$\begin{aligned}\psi_1(\varphi(\alpha)) &= \alpha \rightarrow (\alpha \rightarrow \alpha) = \varphi(\alpha \rightarrow \beta) \\ \psi_2(\varphi(\alpha \rightarrow \alpha)) &= \alpha \rightarrow \alpha = \varphi(\beta)\end{aligned}$$

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Next, we introduce the notion of constraints (Definition 6) (called *path equations* in [17]). Constraints play a key role connecting (constraint-based) semi-unification to the execution of a stack machine. Intuitively, a constraint $X \doteq Y$ reflects joinability of configurations X and Y in a stack machine (cf. Section 4).

► **Definition 5** (Binary Words (\mathbb{B}^*)). Let $\mathbb{B} = \{0, 1\}$ be ranged over by a, b . The set \mathbb{B}^* of *words* is ranged over by s, t, v, w .

► **Definition 6** (Constraint ($s_1\alpha t \doteq v_1\beta_1w$)). A *constraint* has the shape $s_1\alpha t \doteq v_1\beta_1w$, where $\alpha, \beta \in \mathbb{V}$ and $s, t, v, w \in \mathbb{B}^*$.

A constraint is *simple* if it has the shape $a_1\alpha_1\epsilon \doteq \epsilon_1\beta_1b$, where $\alpha, \beta \in \mathbb{V}$, $a, b \in \mathbb{B}$, and ϵ is the empty word.

In order to connect words with valuations, we define valuation compositions (Definition 7) and path functions on terms (Definition 8).

► **Definition 7** (Valuation Composition (ψ_v)). Let $\psi_0, \psi_1 : \mathbb{V} \rightarrow \mathbb{T}$ be valuations. For a word $v \in \mathbb{B}^*$, the *composed valuation* $\psi_v : \mathbb{T} \rightarrow \mathbb{T}$ is such that

$$\psi_\epsilon(\sigma) = \sigma \quad \psi_{wa}(\sigma) = \psi_w(\psi_a(\sigma))$$

► **Definition 8** (Path Function (π_v)). For a word $v \in \mathbb{B}^*$, the partial *path function* $\pi_v : \mathbb{T} \dashrightarrow \mathbb{T}$ is such that

$$\pi_\epsilon(\sigma) = \sigma \quad \pi_{0w}(\sigma \rightarrow \tau) = \pi_w(\sigma) \quad \pi_{1w}(\sigma \rightarrow \tau) = \pi_w(\tau) \quad (\text{otherwise } \pi_v(\sigma) \text{ is undefined})$$

Intuitively, a simple constraint $a_1\alpha_1\epsilon \doteq \epsilon_1\beta_1b$ is satisfied by a valuation triple $(\varphi, \psi_0, \psi_1)$, if $\psi_a(\varphi(\alpha)) = \pi_b(\psi_0(\beta))$. The absence of ψ_0 and ψ_1 on the right-hand side captures matching as part of semi-unification. Similarly to [17], the respective side $s_1\alpha t$ of a constraint is interpreted wrt. a valuation triple $(\varphi, \psi_0, \psi_1)$ by the term which arises when we apply ψ_s to $\varphi(\alpha)$ and then select a subterm via π_t . This interpretation is captured by the following model relation (\models).

► **Definition 9** (Model Relation (\models)). A valuation triple $(\varphi, \psi_0, \psi_1)$ *models* a constraint $s_1\alpha t \doteq v_1\beta_1w$, written $(\varphi, \psi_0, \psi_1) \models s_1\alpha t \doteq v_1\beta_1w$, if $\pi_t(\psi_s(\varphi(\alpha))) = \pi_w(\psi_v(\varphi(\beta)))$.

For a set \mathcal{C} of constraints, we write $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$ if $(\varphi, \psi_0, \psi_1) \models C$ for all $C \in \mathcal{C}$.

For a set \mathcal{C} of constraints and a constraint C , we write $\mathcal{C} \models C$ if for all valuation triples $(\varphi, \psi_0, \psi_1)$ such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$ we have $(\varphi, \psi_0, \psi_1) \models C$.

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As a side note, path equation derivability of [17] is sound for (\models) . The following Example 10, Example 11, and Example 13 illustrate positive and negative cases for models.

► **Example 10.** Let $\mathcal{C} = \{0_1\alpha_1\epsilon \doteq \epsilon_1\beta_11, 1_1\gamma_1\epsilon \doteq \epsilon_1\beta_11, 1_1\alpha_1\epsilon \doteq \epsilon_1\gamma_10\}$ be a set of simple constraints. We have $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$, where

$$\begin{aligned}\varphi &= \{\alpha \mapsto \alpha, \beta \mapsto \beta_0 \rightarrow (\beta_{10} \rightarrow \beta_{11}), \gamma \mapsto \gamma_0 \rightarrow \gamma_1\} \\ \psi_0 &= \{\alpha \mapsto \beta_{10} \rightarrow \beta_{11}\} \\ \psi_1 &= \{\alpha \mapsto \gamma_0, \gamma_0 \mapsto \beta_{10}, \gamma_1 \mapsto \beta_{11}\}\end{aligned}$$

► **Example 11.** Let $\mathcal{C} = \{0_1\alpha_1\epsilon \doteq \epsilon_1\beta_11, 1_1\gamma_1\epsilon \doteq \epsilon_1\beta_11, 1_1\alpha_1\epsilon \doteq \epsilon_1\gamma_10\}$ be a set of simple constraints. We have $\mathcal{C} \models 0_1\alpha_10 \doteq 1_1\alpha_1\epsilon$, because for any valuations φ, ψ_0, ψ_1 such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$ we have

$$\pi_0(\psi_0(\varphi(\alpha))) = \pi_0(\pi_1(\varphi(\beta))) = \pi_0(\psi_1(\varphi(\gamma))) = \psi_1(\pi_0(\varphi(\gamma))) = \psi_1(\psi_1(\varphi(\alpha)))$$

The *depth* of a term is the maximal depth of its syntax tree, and is non-decreasing under substitution.

► **Definition 12** (Term Depth (depth)). The function $\text{depth} : \mathbb{T} \rightarrow \mathbb{N}$ is such that

$$\text{depth}(\alpha) = 0 \quad \text{depth}(\sigma \rightarrow \tau) = 1 + \max\{\text{depth}(\sigma), \text{depth}(\tau)\}$$

► **Example 13.** There is no valuation triple $(\varphi, \psi_0, \psi_1)$ that models the simple constraint $1_1\alpha_1\epsilon \doteq \epsilon_1\alpha_10$. Otherwise, we would have

$$\begin{aligned}\pi_\epsilon(\psi_1(\varphi(\alpha))) &= \pi_0(\psi_\epsilon(\varphi(\alpha))) \\ \implies \psi_1(\varphi(\alpha)) &= \pi_0(\varphi(\alpha)) \\ \implies \psi_1(\sigma \rightarrow \tau) &= \sigma \quad \text{where } \varphi(\alpha) = \sigma \rightarrow \tau \\ \implies \text{depth}(\psi_1(\sigma \rightarrow \tau)) &= \text{depth}(\sigma) \\ \implies \text{depth}(\psi_1(\sigma)) &< \text{depth}(\sigma) \quad \text{which is a contradiction}\end{aligned}$$

Intuitively, the simple constraint $1_1\alpha_1\epsilon \doteq \epsilon_1\alpha_10$ corresponds to an unbounded computation that transforms arbitrary many 1s on the left stack to 0s on the right stack (cf. Section 4).

The following Lemma 14 describes in which cases a simple constraint is modeled.

► **Lemma 14.** We have $(\varphi, \psi_0, \psi_1) \models a_1\alpha_1\epsilon \doteq \epsilon_1\beta_1b$ iff one of the following conditions holds

- $b = 0$ and $\psi_a(\varphi(\alpha)) \rightarrow \tau = \varphi(\beta)$ for some term $\tau \in \mathbb{T}$
- $b = 1$ and $\sigma \rightarrow \psi_a(\varphi(\alpha)) = \varphi(\beta)$ for some term $\sigma \in \mathbb{T}$

Finally, we identify the following semi-unification problem based on simple constraints. The importance of this restriction is pointed out in [17, Sec. 4], and its undecidability implies the undecidability of semi-unification (Theorem 1). Intuitively, we will use a simple constraint $a_1\alpha_1\epsilon \doteq \epsilon_1\beta_1b$ to represent a stack machine transition from state α to state β , removing the symbol a from the left stack and adding the symbol b to the right stack.

► **Problem 15** (Simple Semi-unification (SSU)). Given a finite set \mathcal{C} of simple constraints, do there exist valuations $\varphi, \psi_0, \psi_1 : \mathbb{V} \rightarrow \mathbb{T}$ such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$?

► **Theorem 1.** If simple semi-unification (Problem 15) is undecidable, then so is semi-unification (Problem 3).

Proof. Let $\mathcal{C} = \{0|\alpha_i|\epsilon \doteq \epsilon|\beta_i|b_i \mid i = 1 \dots n\} \cup \{1|\alpha_i|\epsilon \doteq \epsilon|\beta_i|b_i \mid i = n + 1 \dots m\}$ be a set of simple constraints. We define an instance \mathcal{D} of semi-unification that reflects solvability of \mathcal{C} as follows.

Define $\sigma_i = \begin{cases} \alpha_i \rightarrow \gamma_i & \text{if } b_i = 0 \\ \gamma_i \rightarrow \alpha_i & \text{if } b_i = 1 \end{cases}$, where γ_i is fresh for $i = 1 \dots m$. Define \mathcal{D} as (for convenience, we start indexing inequalities from 0)

$$\begin{aligned} \sigma_1 \rightarrow \dots \rightarrow \sigma_n \leq_0 \beta_1 \rightarrow \dots \rightarrow \beta_n \\ \sigma_{n+1} \rightarrow \dots \rightarrow \sigma_m \leq_1 \beta_{n+1} \rightarrow \dots \rightarrow \beta_m \end{aligned}$$

First, by Lemma 14, if \mathcal{D} has a solution φ, ψ_0, ψ_1 , then $\psi_0(\varphi(\sigma_i)) = \varphi(\beta_i)$ for $i = 1 \dots n$, and $\psi_1(\varphi(\sigma_i)) = \varphi(\beta_i)$ for $i = n + 1 \dots m$. Therefore, $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$.

Second, assume $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$. Define $\varphi' : \mathbb{V} \rightarrow \mathbb{T}$ such that $\varphi'(\gamma_i) = \gamma_i$ for $i = 1 \dots m$, and otherwise $\varphi'(\alpha) = \varphi(\alpha)$. For $a \in \mathbb{B}$, define $\psi'_a : \mathbb{V} \rightarrow \mathbb{T}$ such that $\psi'_a(\gamma_i) = \pi_{(1-b_i)}(\varphi(\beta_i))$ for $i = 1 \dots m$, and otherwise $\psi'_a(\alpha) = \psi_a(\alpha)$. By Lemma 14, $\varphi', \psi'_0, \psi'_1$ solve \mathcal{D} . ◀

3 Stack Machine Preliminaries

Instead of working with Turing machines (or symmetric intercell Turing machines of [17]), we use a more convenient computational model of simple stack machines (Definition 16). Intuitively, simple stack machines are a mechanization-friendly presentation of space-bounded Turing machines (cf. proof of Theorem 2).

► **Definition 16** (Simple Stack Machine (\mathcal{M})). Let p, q range over a countably infinite set \mathbb{S} of states. A simple stack machine \mathcal{M} is a finite set of instructions of shape either $ap \rightarrow qb$ or $pa \rightarrow bq$, where $p, q \in \mathbb{S}$ and $a, b \in \mathbb{B}$.

A configuration is a triple $s|p|t$, where $p \in \mathbb{S}$ is a state, $s \in \mathbb{B}^*$ is the left stack, and $t \in \mathbb{B}^*$ is the right stack. The set of all configurations is denoted by \mathbb{C} .

The step relation $(\rightarrow_{\mathcal{M}}) \subseteq \mathbb{C} \times \mathbb{C}$ on configurations is given by

- $s|ap|t \rightarrow_{\mathcal{M}} s|q|bt$ if $(ap \rightarrow qb) \in \mathcal{M}$
- $s|p|at \rightarrow_{\mathcal{M}} sb|q|t$ if $(pa \rightarrow bq) \in \mathcal{M}$

The reachability relation $(\rightarrow_{\mathcal{M}}^*) \subseteq \mathbb{C} \times \mathbb{C}$ on configurations is the reflexive, transitive closure of $(\rightarrow_{\mathcal{M}})$. For brevity, we say *machine* for simple stack machine.

► **Example 17.** Consider the machine $\mathcal{M} = \{(1p \rightarrow p0)\}$, which pops 1s from the left stack and pushes 0s onto the right stack.

We have that from the configuration $X = 1^n|p|\epsilon$ the configurations $Y_m = 1^m|p|0^{n-m}$ such that $m \leq n$ are reachable, i.e. $X \rightarrow_{\mathcal{M}}^* Y_m$ for $m = 0 \dots n$.

► **Definition 18** (Deterministic). A machine \mathcal{M} is *deterministic* if for all configurations $X, Y, Z \in \mathbb{C}$ such that $X \rightarrow_{\mathcal{M}} Y$ and $X \rightarrow_{\mathcal{M}} Z$ we have $Y = Z$.

► **Remark 19.** The step relation for Turing machines is naturally connected to the step relation for simple stack machines as follows. Say a Turing machine reading a symbol a in state x writes a symbol b , transitions into a state y , and moves right. This local behavior is described by the instructions $((x, a)0 \rightarrow b(y, 0))$ and $((x, a)1 \rightarrow b(y, 1))$, where $(x, a), (y, 0), (y, 1) \in \mathbb{S}$. The left (resp. right) stack describes the Turing machine tape left (resp. right) of the current head position.

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A distinctive machine feature is preservation of total available space under reachability (Lemma 21).

► **Definition 20** (Word Length (length)). The function $\text{length} : \mathbb{B}^* \rightarrow \mathbb{N}$ is such that

$$\text{length}(\epsilon) = 0 \quad \text{length}(av) = 1 + \text{length}(v)$$

► **Lemma 21.** If $s_1p_1t \xrightarrow{*}_{\mathcal{M}} v_1q_1w$, then $\text{length}(s) + \text{length}(t) = \text{length}(v) + \text{length}(w)$.

Proof. Instructions preserve the sum of stack lengths. ◀

Since machines operate in bounded space (as opposed to Turing machines that operate on infinite tape), most machine properties, such as reachability (Lemma 22), are decidable. This is most useful for a fully constructive mechanization.

► **Lemma 22.** It is decidable, whether for a machine \mathcal{M} and configurations $X, Y \in \mathbb{C}$, we have $X \xrightarrow{*}_{\mathcal{M}} Y$.

Proof. By Lemma 21, the number of configurations reachable from X is finite and can be searched exhaustively. ◀

Although boundedness (is for any configuration X the number of configurations reachable from X finite?) is a trivially true machine property, uniform boundedness (Problem 26) is undecidable (Theorem 2).

► **Definition 23** (Uniformly Bounded). A machine \mathcal{M} is *uniformly bounded* by a natural number $n \in \mathbb{N}$ if for all configurations $X \in \mathbb{C}$ we have

$$|\{Y \in \mathbb{C} \mid X \xrightarrow{*}_{\mathcal{M}} Y\}| \leq n$$

For brevity, we say that \mathcal{M} is uniformly bounded if \mathcal{M} is uniformly bounded by some $n \in \mathbb{N}$.

The following Example 24 illustrates a uniformly bounded machine.

► **Example 24.** The machine $\mathcal{M} = \{(0p \rightarrow q1), (q1 \rightarrow 1p), (1p \rightarrow q0), (q0 \rightarrow 0p)\}$ is (by case analysis) uniformly bounded by $n = 4$. For instance, in case of a configuration $X = sa_1p_1t$, where $a \in \mathbb{B}$ and $s, t \in \mathbb{B}^*$, we have

$$|\{Y \in \mathbb{C} \mid X \xrightarrow{*}_{\mathcal{M}} Y\}| = |\{sa_1p_1t, s_1q_1(1-a)t, s(1-a)_1p_1t, s_1q_1at\}| = 4 \leq n \quad \lrcorner$$

Complementarily, the following Example 25 illustrates a machine that is not uniformly bounded. As will be shown in Section 4, this is because the simple constraint $1_1a_1\epsilon \doteq \epsilon_1a_10$ in Example 13 has no model.

► **Example 25.** The machine $\mathcal{M} = \{(1p \rightarrow p0)\}$ from Example 17 is not uniformly bounded, because for any $n \in \mathbb{N}$ and the configuration $X = 1^n_1p_1\epsilon$ we have

$$|\{Y \in \mathbb{C} \mid X \xrightarrow{*}_{\mathcal{M}} Y\}| = |\{1^m_1p_10^{n-m} \mid 0 \leq m \leq n\}| = n + 1 > n \quad \lrcorner$$

► **Problem 26** (Uniform Boundedness of Deterministic Simple Stack Machines (UBDSSM)). Given a deterministic machine \mathcal{M} , is \mathcal{M} uniformly bounded?

The intuition in the above Remark 19 is used in the following Theorem 2 to connect unbounded simple stack machines to immortal Turing machines.

► **Theorem 2.** Uniform boundedness of deterministic simple stack machines (Problem 26) is undecidable.

Proof. Weak truth-table reduction from Turing machine mortality [12]. Let \mathcal{T} be a Turing machine with moving tape over the alphabet \mathbb{B} having states Q and transition function $\delta : Q \times \mathbb{B} \rightarrow Q \times \mathbb{B} \times \{L, R\}$. A generalized instantaneous description (GID)¹ of \mathcal{T} is a pair $(x, T) \in Q \times \mathbb{B}^{\mathbb{Z}}$, where x is the current state and T is the current tape content with the currently scanned symbol $T(0)$.

Let $(Q \times \mathbb{B}) \subseteq \mathbb{S}$. Define a simple stack machine \mathcal{M} having as instructions

- $(0(x, a) \rightarrow (y, 0)b)$ and $(1(x, a) \rightarrow (y, 1)b)$ if $\delta(x, a) = (y, b, L)$
- $((x, a)0 \rightarrow b(y, 0))$ and $((x, a)1 \rightarrow b(y, 1))$ if $\delta(x, a) = (y, b, R)$

If \mathcal{T} is deterministic, then so is \mathcal{M} . Clearly, any finite number of \mathcal{T} -transitions corresponds to \mathcal{M} -steps for a large enough starting configuration.

We now show that if we can decide whether \mathcal{M} is uniformly bounded, then we can decide whether \mathcal{T} is immortal, i.e. that \mathcal{T} has a GID which has no terminal successor.

First, assume that \mathcal{M} is uniformly bounded by n . From a GID (x, T) we have that \mathcal{T} cannot scan symbols initially positioned at i such that $i < -n$ or $i > n$. Therefore, \mathcal{T} is immortal iff it loops in space $2n + 1$, which is decidable by exhaustive search.

Second, assume that every GID in \mathcal{T} has a terminal successor. We use the fan theorem (as formulated by [4]) to show that \mathcal{M} is uniformly bounded. Let $B = B_{\top} \cup B_{\perp}$, where B_{\top} is the set of binary words that encode terminating computational histories (finite sequences of GIDs in bounded space) in \mathcal{T} , and let B_{\perp} be the set of binary words that cannot be extended to encode a terminating computational history. Since every GID in \mathcal{T} has a terminal successor, membership in B is decidable and B is a *bar*, i.e. every infinite binary sequence has a finite prefix in B . By the fan theorem, B is a uniform bar, i.e. there exists an $n \in \mathbb{N}$ such that any word in B has a prefix of length at most n that is in B . As a result, encoded terminating computational histories are of length at most n . Therefore, \mathcal{M} is uniformly bounded by n . ◀

► **Remark 27.** In the above proof of Theorem 2, we deliberately use the fan theorem instead of König's lemma (used in [17, Corollary 5]). In constructive mathematics, the fan theorem, which is valid in Brouwer's intuitionism, is weaker than König's lemma (cf. [22]), which is valid classically.

► **Remark 28.** Peculiarly, for counter machines, as another model of computation, uniform boundedness is decidable (similarly to [14, Thm. 2]), whereas boundedness is not (similarly to [14, Thm. 1]). For simple stack machines it is vice versa.

3.1 Narrow Configurations

Clearly, a configuration from which no configuration with an empty left or right stack is reachable does not fully utilize the space it is provided. Therefore, key to boundedness are configurations that have an empty left or right stack, as such configurations may require additional space to reach further configurations. Extending this thought, in this section we identify a property of configurations, which we call *narrowness* (Definition 34) which plays a pivotal role in the overall argument and is part of the main contribution.

¹ An instantaneous description (ID) requires the tape content to be 0 except for finitely many positions.

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We can view machine instructions as a restricted rewriting system. Such a view induces the notion of joinable configurations (Definition 29). For deterministic machines, configuration joinability is an equivalence relation (Lemma 30) with a system of representatives (Definition 31).

► **Definition 29** (Joinable $(\sim_{\mathcal{M}})$). Two configurations $X, Y \in \mathbb{C}$ are *joinable* in a machine \mathcal{M} , written $X \sim_{\mathcal{M}} Y$, if there exists a configuration Z such that $X \rightarrow_{\mathcal{M}}^* Z$ and $Y \rightarrow_{\mathcal{M}}^* Z$.

► **Lemma 30.** If a machine \mathcal{M} is deterministic, then $(\sim_{\mathcal{M}})$ is an equivalence (reflexive, symmetric, transitive) relation on configurations.

Proof. Clearly, $(\sim_{\mathcal{M}})$ is reflexive and symmetric. Since \mathcal{M} is deterministic, we have that $(\rightarrow_{\mathcal{M}})$ is confluent. Therefore, for any configurations X_1, X_2, X_3, Y_1, Y_2 such that $X_1 \rightarrow_{\mathcal{M}}^* Y_1$, $X_2 \rightarrow_{\mathcal{M}}^* Y_1$, $X_2 \rightarrow_{\mathcal{M}}^* Y_2$, and $X_3 \rightarrow_{\mathcal{M}}^* Y_2$ there exists a configuration Z such that $X_1 \rightarrow_{\mathcal{M}}^* Y_1 \rightarrow_{\mathcal{M}}^* Z$ and $X_3 \rightarrow_{\mathcal{M}}^* Y_2 \rightarrow_{\mathcal{M}}^* Z$. Therefore, $(\sim_{\mathcal{M}})$ is transitive. ◀

► **Definition 31** (Representative $([X]_{\mathcal{M}})$). The *representative* of a configuration $X \in \mathbb{C}$ in a deterministic machine \mathcal{M} , written $[X]_{\mathcal{M}}$, is the lexicographically smallest configuration Y such that $X \sim_{\mathcal{M}} Y$.

► **Lemma 32.** For configurations $X, Y \in \mathbb{C}$, we have $[X]_{\mathcal{M}} = [Y]_{\mathcal{M}}$ iff $X \sim_{\mathcal{M}} Y$.

► **Remark 33.** By Lemma 21 and Lemma 22 the representative $[X]_{\mathcal{M}}$ of a configuration X in \mathcal{M} is computable, and joinability $(\sim_{\mathcal{M}})$ is decidable.

Next, we identify a key property (Definition 34) of configurations, that connects machine computation with semi-unification (cf. Section 4).

► **Definition 34** (Narrow). A configuration X is *narrow* in a machine \mathcal{M} , if there exists a state $p \in \mathbb{S}$ and a word $s \in \mathbb{B}^*$ such that $X \sim_{\mathcal{M}} s\uparrow p\downarrow\epsilon$.

► **Remark 35.** For a state $p \in \mathbb{S}$, the configuration $\epsilon\uparrow p\downarrow\epsilon$ is narrow in any machine \mathcal{M} .

► **Remark 36.** Similarly to Lemma 22, it is decidable, whether for a machine \mathcal{M} and configuration $X \in \mathbb{C}$, we have that X is narrow in \mathcal{M} .

► **Example 37.** In the machine $\mathcal{M} = \{(p1 \rightarrow 0r), (1q \rightarrow r1)\}$ the configuration $0\uparrow p\downarrow 11$ is narrow because $0\uparrow p\downarrow 11 \rightarrow_{\mathcal{M}}^* 00\uparrow r\downarrow 11 \leftarrow_{\mathcal{M}}^* 001\uparrow q\downarrow \epsilon$, that is we have $0\uparrow p\downarrow 11 \sim_{\mathcal{M}} 001\uparrow q\downarrow \epsilon$.

Narrow configurations play a pivotal role for uniform boundedness (Lemma 38 and Lemma 39). Additionally, narrowness is *the* decisive property which we use to construct solutions for semi-unification instances (Definition 41 and Definition 42).

► **Lemma 38.** If a machine \mathcal{M} is uniformly bounded, then there exists $m \in \mathbb{N}$ such that for all narrow in \mathcal{M} configurations $s\uparrow p\downarrow t \in \mathbb{C}$ we have $\text{length}(t) \leq m$.

Proof. If $s\uparrow p\downarrow t$ is narrow in \mathcal{M} , then there are configurations $s'\uparrow p'\downarrow \epsilon$ and $v\uparrow q\downarrow w$ such that $s\uparrow p\downarrow t \rightarrow_{\mathcal{M}}^* v\uparrow q\downarrow w$ and $s'\uparrow p'\downarrow \epsilon \rightarrow_{\mathcal{M}}^* v\uparrow q\downarrow w$. If \mathcal{M} is uniformly bounded by n , then we have $|\text{length}(t) - \text{length}(w)| \leq n$ and $|\text{length}(\epsilon) - \text{length}(w)| \leq n$. Therefore, $\text{length}(t) \leq 2n$. ◀

► **Lemma 39.** Let \mathcal{M} be a deterministic machine. If there exists $m \in \mathbb{N}$ such that for all narrow in \mathcal{M} configurations $\epsilon|p|t \in \mathbb{C}$ we have $\text{length}(t) \leq m$, then \mathcal{M} is uniformly bounded.

Proof. Let $m \in \mathbb{N}$ be such that for all narrow in \mathcal{M} configurations $\epsilon|p|t \in \mathbb{C}$ we have $\text{length}(t) \leq m$. Let $n \in \mathbb{N}$ and let $X = s|p|t$ reach at least n configurations such that $\text{length}(s) + \text{length}(t)$ is minimal. We show that \mathcal{M} is uniformly bounded by showing

$$n \leq 1 + |\{s'|p'|t' \in \mathbb{C} \mid \text{length}(s') + \text{length}(t') \leq m \text{ and } p' \text{ occurs in } \mathcal{M}\}| \quad (\star)$$

We have $X \xrightarrow{*}_{\mathcal{M}} \epsilon|q|w$ for some state $q \in \mathbb{S}$ and word $w \in \mathbb{B}^*$. Otherwise, left stacks of all configurations reachable from X would have the same prefix, which could be removed. Similarly, we have $X \xrightarrow{*}_{\mathcal{M}} v|r|\epsilon$ for some state $r \in \mathbb{S}$ and word $v \in \mathbb{B}^*$. Since \mathcal{M} is deterministic, $(\xrightarrow{*}_{\mathcal{M}})$ is confluent. Therefore, the configuration $\epsilon|q|w$ is narrow in \mathcal{M} .

Finally, by Lemma 21, for any configuration $s'|p'|t'$ such that $X \xrightarrow{*}_{\mathcal{M}} s'|p'|t'$ we have $\text{length}(s') + \text{length}(t') = \text{length}(s) + \text{length}(t) = \text{length}(w) \leq m$, showing (\star) . ◀

4 Undecidability of Semi-unification

In this section we fix a deterministic machine \mathcal{M} . Our goal is to construct a *specific instance* $\mathcal{C}_{\mathcal{M}}$ (Definition 40) of simple semi-unification such that the machine \mathcal{M} is uniformly bounded if (Lemma 48) and only if (Lemma 45) $\mathcal{C}_{\mathcal{M}}$ is solvable.

For brevity, we omit \mathcal{M} in notations in this section, i.e. we write (\sim) for $(\sim_{\mathcal{M}})$, write \mathcal{C} for $\mathcal{C}_{\mathcal{M}}$, say narrow for narrow in \mathcal{M} , etc. All definitions in this section tacitly depend on \mathcal{M} .

Let us tacitly inject \mathbb{S} into \mathbb{V} , i.e. $\mathbb{S} \subseteq \mathbb{V}$. Additionally, for each configuration $X \in \mathbb{C}$ we fix a distinct variable $\alpha_X \in \mathbb{V}$.

► **Definition 40** (Specific instance \mathcal{C}). The set \mathcal{C} of simple constraints is given by

$$\mathcal{C} = \{a|p|\epsilon \doteq \epsilon|q|b \mid (ap \longrightarrow qb) \in \mathcal{M}\} \cup \{b|q|\epsilon \doteq \epsilon|p|a \mid (pa \longrightarrow bq) \in \mathcal{M}\}$$

4.1 Uniform Boundedness of \mathcal{M} to Solvability of \mathcal{C}

In this subsection we assume that \mathcal{M} is uniformly bounded and construct a solution φ, ψ_0, ψ_1 (Definition 42) for \mathcal{C} . Surprisingly, this can be done directly via the following function ζ (Definition 41), based on the notion of narrow configurations (Definition 34).

► **Definition 41** (ζ). If \mathcal{M} is uniformly bounded, then the function $\zeta : \mathbb{C} \rightarrow \mathbb{T}$ is given by

$$\zeta(s|p|t) = \begin{cases} \zeta(s|p|t0) \rightarrow \zeta(s|p|t1) & \text{if } s|p|t \text{ is narrow} \\ \alpha_{[s|p|t]} & \text{otherwise} \end{cases}$$

By Lemma 38, ζ is well-defined and computable (cf. Remark 36 and Remark 33). Computability of ζ is essential for a fully constructive argument.

► **Definition 42** (Valuations φ, ψ_0, ψ_1). The valuation $\varphi : \mathbb{V} \rightarrow \mathbb{T}$ is such that

$$\varphi(p) = \zeta(\epsilon|p|\epsilon) \quad (\text{otherwise } \varphi(\alpha) = \alpha)$$

For $a \in \mathbb{B}$, the valuation $\psi_a : \mathbb{V} \rightarrow \mathbb{T}$ is such that

$$\psi_a(\alpha_{s|p|t}) = \zeta(as|p|t) \quad (\text{otherwise } \psi_a(\alpha) = \alpha)$$

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The function ζ respects joinability (Lemma 43), i.e. it can be lifted to (\sim) equivalence classes.

► **Lemma 43.** For configurations $X, Y \in \mathbb{C}$ such that $X \sim Y$ we have $\zeta(X) = \zeta(Y)$.

Proof. We show $\zeta(s\!|p\!|t) = \zeta(v\!|q\!|w)$ by induction on $\text{depth}(\zeta(s\!|p\!|t))$.

Case $s\!|p\!|t$ is narrow: By Lemma 30, the configuration $v\!|q\!|w$ is narrow. Therefore,

$$\zeta(s\!|p\!|t) = \zeta(s\!|p\!|t0) \rightarrow \zeta(s\!|p\!|t1) \stackrel{\text{(IH)}}{=} \zeta(v\!|q\!|w0) \rightarrow \zeta(v\!|q\!|w1) = \zeta(v\!|q\!|w)$$

Case $s\!|p\!|t$ is not narrow: By Lemma 30, the configuration $v\!|q\!|w$ is not narrow. Therefore,

$$\zeta(s\!|p\!|t) = \alpha_{[s\!|p\!|t]} \stackrel{\text{Lem. 32}}{=} \alpha_{[v\!|q\!|w]} = \zeta(v\!|q\!|w) \quad \blacktriangleleft$$

Since the function ζ respects joinability, it absorbs ψ_0 and ψ_1 (Lemma 44).

► **Lemma 44.** For $a \in \mathbb{B}$ and configuration $s\!|p\!|t \in \mathbb{C}$, we have $\psi_a(\zeta(s\!|p\!|t)) = \zeta(as\!|p\!|t)$.

Proof. We show $\psi_a(\zeta(s\!|p\!|t)) = \zeta(as\!|p\!|t)$ by induction on $\text{depth}(\zeta(s\!|p\!|t))$.

Case $s\!|p\!|t$ is narrow: We have that $as\!|p\!|t$ is narrow, and

$$\begin{aligned} \psi_a(\zeta(s\!|p\!|t)) &= \psi_a(\zeta(s\!|p\!|t0) \rightarrow \zeta(s\!|p\!|t1)) = \psi_a(\zeta(s\!|p\!|t0)) \rightarrow \psi_a(\zeta(s\!|p\!|t1)) \\ &\stackrel{\text{(IH)}}{=} \zeta(as\!|p\!|t0) \rightarrow \zeta(as\!|p\!|t1) = \zeta(as\!|p\!|t) \end{aligned}$$

Case $s\!|p\!|t$ is not narrow: Let $v\!|q\!|w = [s\!|p\!|t]$. We have

$$\psi_a(\zeta(s\!|p\!|t)) = \psi_a(\alpha_{[s\!|p\!|t]}) = \zeta(av\!|q\!|w) \stackrel{\text{Lem. 43}}{=} \zeta(as\!|p\!|t) \quad \blacktriangleleft$$

As a result, the valuations φ, ψ_0, ψ_1 solve \mathcal{C} (Lemma 45).

► **Lemma 45.** If \mathcal{M} is uniformly bounded, then $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$.

Proof. Configuration where both stacks are empty are trivially narrow (Remark 35).

Case $a\!|p\!|\epsilon \doteq \epsilon\!|q\!|b \in \mathcal{C}$: We have $(ap \rightarrow qb) \in \mathcal{M}$, therefore $a\!|p\!|\epsilon \sim \epsilon\!|q\!|b$. We have

$$\psi_a(\varphi(p)) = \psi_a(\zeta(\epsilon\!|p\!|\epsilon)) \stackrel{\text{Lem. 44}}{=} \zeta(a\!|p\!|\epsilon) \stackrel{\text{Lem. 43}}{=} \zeta(\epsilon\!|q\!|b) = \pi_b(\zeta(\epsilon\!|q\!|\epsilon)) = \pi_b(\varphi(q))$$

Case $b\!|q\!|\epsilon \doteq \epsilon\!|p\!|a \in \mathcal{C}$: We have $(pa \rightarrow bq) \in \mathcal{M}$, therefore $b\!|q\!|\epsilon \sim \epsilon\!|p\!|a$. We have

$$\psi_b(\varphi(q)) = \psi_b(\zeta(\epsilon\!|q\!|\epsilon)) \stackrel{\text{Lem. 44}}{=} \zeta(b\!|q\!|\epsilon) \stackrel{\text{Lem. 43}}{=} \zeta(\epsilon\!|p\!|a) = \pi_a(\zeta(\epsilon\!|p\!|\epsilon)) = \pi_a(\varphi(p)) \quad \blacktriangleleft$$

Essentially, the function ζ interprets machine configurations as terms from which the solution $(\varphi, \psi_0, \psi_1)$ of \mathcal{C} is constructed. Traditionally, this step in the overall argument [17] relies on on a more complicated path equation derivability and termination of a redex contraction procedure for semi-unification. Arguably, the function ζ is the main insight of this work, as it contributes to a simpler, fully constructive translation of machine boundedness to solvability of semi-unification.

4.2 Solvability of \mathcal{C} to Uniform Boundedness of \mathcal{M}

In this subsection we assume that there exist valuations φ, ψ_0, ψ_1 such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$, and we show that \mathcal{M} is uniformly bounded.

Intuitively, we show that joinability is sound for constraint semantics (Corollary 47) based on soundness of the step relation for constraint semantics (Lemma 46).

► **Lemma 46.** For configurations $X, Y \in \mathbb{C}$ such that $X \longrightarrow Y$ we have $\mathcal{C} \models X \doteq Y$.

Proof. Let φ, ψ_0, ψ_1 be valuations such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$.

Case $sa|p|t \longrightarrow s|q|bt$: We have $a|p|\epsilon \doteq \epsilon|q|b \in \mathcal{C}$. Therefore, $\psi_a(\varphi(p)) = \pi_b(\varphi(q))$ and

$$\pi_t(\psi_{sa}(\varphi(p))) = \pi_t(\psi_s(\psi_a(\varphi(p)))) = \pi_t(\psi_s(\pi_b(\varphi(q)))) = \pi_{bt}(\psi_s(\varphi(q)))$$

Case $s|p|at \longrightarrow_{\mathcal{M}} sb|q|t$: We have $b|q|\epsilon \doteq \epsilon|p|a \in \mathcal{C}$. Therefore, $\psi_b(\varphi(q)) = \pi_a(\varphi(p))$ and

$$\pi_{at}(\psi_s(\varphi(p))) = \pi_t(\psi_s(\pi_a(\varphi(p)))) = \pi_t(\psi_s(\psi_b(\varphi(q)))) = \pi_t(\psi_{sb}(\varphi(q))) \quad \blacktriangleleft$$

► **Corollary 47.** For configurations $X, Y \in \mathbb{C}$ such that $X \sim Y$ we have $\mathcal{C} \models X \doteq Y$.

As a result, narrow configurations $\epsilon|p|t$ do not admit arbitrary long right stacks t , because $\pi_t(\varphi(p))$ is undefined if $\text{length}(t)$ exceeds $\text{depth}(\varphi(p))$. The bound on depth for the range of φ immediately induces a uniform bound for \mathcal{M} (Lemma 48).

► **Lemma 48.** If there exist valuations φ, ψ_0, ψ_1 such that $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$, then \mathcal{M} is uniformly bounded.

Proof. Let $\epsilon|p|t \in \mathbb{C}$ be narrow, i.e. $\epsilon|p|t \sim s|q|\epsilon$ for some state $q \in \mathbb{S}$ and word $s \in \mathbb{B}^*$. By Corollary 47, we have $\pi_t(\varphi(p)) = \psi_s(\varphi(q)) \in \mathbb{T}$. Therefore,

$$\text{length}(t) \leq \max\{\text{depth}(\varphi(r)) \mid r \in \mathbb{S} \text{ and } r \text{ occurs in } \mathcal{M}\}$$

By Lemma 39, \mathcal{M} is uniformly bounded. ◀

Key to the construction of a uniform bound in the above proof is the characterization of uniform boundedness via narrow configurations (Lemma 39).

4.3 Main Result

Overall, we obtain undecidability of semi-unification (Theorem 4) via undecidability of simple semi-unification (Theorem 3).

► **Theorem 3.** Simple semi-unification (Problem 15) is undecidable.

Proof. By Theorem 2, uniform boundedness of deterministic machines (UBDSSM) is undecidable. Section 4 gives a reduction from UBDSSM to simple semi-unification, for which correctness is shown by Lemma 45 and Lemma 48. ◀

► **Theorem 4.** Semi-unification is undecidable.

Proof. By Theorem 3 and Theorem 1. ◀

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Let us illustrate the construction, revisiting the uniformly bounded machine of Example 24.

► **Example 49.** Let $\mathcal{M} = \{(0p \rightarrow q1), (q1 \rightarrow 1p), (1p \rightarrow q0), (q0 \rightarrow 0p)\}$. Then, $\mathcal{C} = \{0|p|\epsilon \doteq \epsilon|q|1, 1|p|\epsilon \doteq \epsilon|q|1, 1|p|\epsilon \doteq \epsilon|q|0, 0|p|\epsilon \doteq \epsilon|q|0\}$. Narrow in \mathcal{M} configurations are $s|r|\epsilon$ for words $s \in \mathbb{B}^*$ and states $r \in \mathbb{S}$, and $s|q|a$ for words $s \in \mathbb{B}^*$ and symbols $a \in \mathbb{B}$. Therefore (not writing out representatives), we have

$$\begin{aligned} \varphi(p) &= \zeta(\epsilon|p|\epsilon) = \zeta(\epsilon|p|0) \rightarrow \zeta(\epsilon|p|1) = \alpha_{[\epsilon|p|0]} \rightarrow \alpha_{[\epsilon|p|1]} \\ \varphi(q) &= \zeta(\epsilon|q|\epsilon) = \zeta(\epsilon|q|0) \rightarrow \zeta(\epsilon|q|1) \\ &= (\zeta(\epsilon|q|00) \rightarrow \zeta(\epsilon|q|01)) \rightarrow (\zeta(\epsilon|q|10) \rightarrow \zeta(\epsilon|q|11)) \\ &= (\alpha_{[\epsilon|q|00]} \rightarrow \alpha_{[\epsilon|q|01]}) \rightarrow (\alpha_{[\epsilon|q|10]} \rightarrow \alpha_{[\epsilon|q|11]}) \\ \psi_a(\alpha_{\epsilon|p|b}) &= \zeta(a|p|b) = \alpha_{[a|p|b]} \quad \text{for } a, b \in \mathbb{B} \end{aligned}$$

Overall, the valuations φ, ψ_0, ψ_1 model \mathcal{C} , i.e. $(\varphi, \psi_0, \psi_1) \models \mathcal{C}$. For example, we have $(\varphi, \psi_0, \psi_1) \models 0|p|\epsilon \doteq \epsilon|q|1$ because $0|p|1 \sim \epsilon|q|11$ and $0|p|1 \sim \epsilon|q|11$ imply

$$\begin{aligned} \psi_0(\varphi(p)) &= \psi_0(\alpha_{[\epsilon|p|0]} \rightarrow \alpha_{[\epsilon|p|1]}) = \psi_0(\alpha_{\epsilon|p|0} \rightarrow \alpha_{\epsilon|p|1}) = \alpha_{[0|p|0]} \rightarrow \alpha_{[0|p|1]} \\ &= \alpha_{[\epsilon|q|10]} \rightarrow \alpha_{[\epsilon|q|11]} = \pi_1(\varphi(q)) \end{aligned}$$

5 Mechanization

This section provides an overview over the mechanization [2] in the Coq proof assistant of the reduction presented in Section 4.

The mechanization can be considered self-contained code supporting the mathematical argument and its constructivity. In addition, it is compatible with the framework of *synthetic undecidability results* [9, 8, 7] in *synthetic computability theory* [3].

5.1 Semi-unification

Terms (Definition 1) are mechanized in `SemiU/SemiU_prelim.v` as the inductive type

```
Inductive term : Set :=
| atom : nat -> term
| arr : term -> term -> term.
```

Correspondingly, application of valuations is mechanized as

```
Definition valuation : Set := nat -> term.

Fixpoint substitute (f: valuation) (t: term) : term :=
match t with
| atom n => f n
| arr s t => arr (substitute f s) (substitute f t)
end.
```

Solvability of semi-unification inequalities is mechanized as

```
Definition inequality : Set := (term * term).

Definition solution (φ : valuation) : inequality -> Prop :=
fun '(s, t) => exists (ψ : valuation),
substitute ψ (substitute φ s) = substitute φ t.
```

Correspondingly, semi-unification is mechanized in `SemiU/SemiU.v` as the predicate

```
Definition SemiU (p: list inequality) := exists (φ: valuation),
forall (c: inequality), In c p -> solution φ c.
```

5.2 Simple Stack Machines

Machines (`ssm`) are mechanized in `SM/SSM_prelim.v` as lists of instructions.

```

Definition stack : Set := list bool.
Definition state : Set := nat.
Definition config : Set := stack * state * stack.
Definition dir : Set := bool.
Definition symbol : Set := bool.
Definition instruction : Set := state * state * symbol * symbol * dir.
Definition ssm : Set := list instruction.

```

For example, $(p, q, a, b, \text{true}) : \text{instruction}$ corresponds to the instruction $(ap \rightarrow qb)$, and $(p, q, b, a, \text{false}) : \text{instruction}$ corresponds to the instruction $(pb \rightarrow aq)$. This is captured by the inductive predicate `Inductive step (M : ssm) : config -> config -> Prop`, that mechanizes the step relation.

Deterministic machines (`dssm`) admit only functional step predicates and reachability (`reachable`) is the reflexive, transitive closure of `step`.

```

Definition deterministic (M: ssm) := forall (X Y Z: config),
  step M X Y -> step M X Z -> Y = Z.

Definition dssm := { M : ssm | deterministic M }.

Definition reachable (M: ssm) : config -> config -> Prop :=
  clos_refl_trans config (step M).

```

Uniform boundedness (`bounded`) of deterministic machines (`dssm`) is mechanized in `SM/DSSM_UB.v` as the predicate `DSSM_UB`.

```

Definition bounded (M: ssm) (n: nat) : Prop :=
  forall (X: config), exists (L: list config),
    (forall (Y: config), reachable M X Y -> In Y L) /\ length L <= n.

Definition DSSM_UB (M: dssm) := exists (n: nat), bounded (proj1_sig M) n.

```

5.3 Main Result

Many-one reducibility (\preceq) of a predicate $p : X \rightarrow \text{Prop}$ to a predicate $q : Y \rightarrow \text{Prop}$ is mechanized in `Reduction.v` as

```

Definition reduces X Y (p : X -> Prop) (q : Y -> Prop) :=
  exists f : X -> Y, forall x, p x <-> q (f x).
Notation "p  $\preceq$  q" := (reduces p q) (at level 50).

```

The main result is mechanized in `SemiU/DSSM_UB_to_SemiU.v` as

```

Theorem DSSM_UB_to_SemiU : DSSM_UB  $\preceq$  SemiU.
Proof.
  apply (reduces_transitive DSSM_UB_to_SSemiU).
  exact SSemiU_to_SemiU.
Qed.

```

The above shows that we first reduce `DSSM_UB` to simple semi-unification (mechanized in `SemiU/SSemiU.v` as the predicate `SSemiU`) and then reduce `SSemiU` to `SemiU`. Mechanization details of `DSSM_UB_to_SSemiU` are found in `SemiU/SSemiU/DSSM_UB_to_SSemiU_argument.v`.

Informative decidability of narrowness is mechanized in `DSM/DSSM/DSSM_facts.v` as

```

Lemma narrow_dec (X: config) : decidable (narrow X).

```

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Based on decidability of narrowness, the key function ζ (Definition 41) is mechanized as

```
Fixpoint  $\zeta$  (n: nat) (X: config) : term :=
  match n with
  | 0 => atom (embed (nf X))
  | S n =>
    match X with
    | (A, x, B) =>
      if narrow_dec (A, x, B) then
        arr ( $\zeta$  n (A, x, B++[false])) ( $\zeta$  n (A, x, B++[true]))
      else atom (embed (nf X))
    end
  end.
end.
```

where `nf X` mechanizes the representative of the mechanized configuration `x` (Definition 31). The parameter `n` is initialized with a uniform bound of the underlying machine.

Finally, Lemma 45 and Lemma 48 are mechanized as

```
Lemma soundness {M: dssm} :
  DSSM_UB M -> SSemiU (SM_to_SUcs (proj1_sig M)).

Lemma completeness {M: dssm}:
  SSemiU (SM_to_SUcs (proj1_sig M)) -> DSSM_UB M.
```

Overall, the mechanization encompasses 1500 lines of code, where two thirds show machine properties (such as decidability of narrowness) and one third is dedicated to the main argument of Section 4.

6 Conclusion

Traditionally, the association of an undecidable property for Turing machines with solvability of semi-unification is, arguably, opaque. It is established via the symmetric closure of intercell Turing machines, path equation derivability, and termination of a redex contraction procedure for semi-unification [17]. The main novelty of the presented approach is the direct association of an undecidable boundedness property with solutions of semi-unification via certain (narrow) machine configurations. As a consequence, we obtain a simpler argument for the undecidability of semi-unification. Additionally, this allows for a fully constructive mechanization of a reduction from uniform boundedness of deterministic simple stack machines (Problem 26) to semi-unification (Problem 3).

There are at least two reasonable goals to pursue next.

First, there exists a larger Coq framework [9] containing various undecidability results. The mechanization presented in Section 5 is a significant part of the ongoing effort to mechanize a reduction from the Turing machine halting problem to semi-unification. It is unclear whether a comprehensive reduction can be given fully constructively, as the presented mechanization starts with uniform boundedness. The reduction from the Turing machine halting problem (as of now) requires the fan theorem (which is part of Brouwer's constructivism, but is not considered fully constructive by Bishop). Nevertheless, it is an improvement over König's lemma used in [17]. There is reason to believe, that eliminating immortality as an intermediate step may allow for a fully constructive reduction. This is why the mechanization in Section 5 starts with boundedness as opposed to immortality.

Second, related work on semi-unification mostly follows the original approach (e.g. [1, 6]). We anticipate that the more direct argument, presented in this work, can be adapted to the related scenarios. Specifically, the presented approach seems promising to realize a fully constructive mechanization of the undecidability of unification modulo synchronous distributivity [1].

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