Beyond Natural Proofs: Hardness Magnification and Locality

Lijie Chen
Massachusetts Institute of Technology, Cambridge, MA, USA
lijieche@mit.edu

Shuichi Hirahara
National Institute of Informatics, Tokyo, Japan
s_hirahara@nii.ac.jp

Igor C. Oliveira
University of Warwick, United Kingdom
igor.oliveira@warwick.ac.uk

Ján Pich
University of Oxford, United Kingdom
jan.pich@cs.ox.ac.uk

Ninad Rajgopal
University of Oxford, United Kingdom
ninad.rajgopal@cs.ox.ac.uk

Rahul Santhanam
University of Oxford, United Kingdom
rahul.santhanam@cs.ox.ac.uk

—— **Abstract** ——

Hardness magnification reduces major complexity separations (such as $\mathsf{EXP} \nsubseteq \mathsf{NC}^1$) to proving lower bounds for some natural problem Q against weak circuit models. Several recent works [42, 36, 13, 39, 12, 38, 10] have established results of this form. In the most intriguing cases, the required lower bound is known for problems that appear to be significantly easier than Q, while Q itself is susceptible to lower bounds but these are not yet sufficient for magnification.

In this work, we provide more examples of this phenomenon, and investigate the prospects of proving new lower bounds using this approach. In particular, we consider the following essential questions associated with the hardness magnification program:

- *Does hardness magnification avoid the natural proofs barrier of Razborov and Rudich [46]?*
- *Can we adapt known lower bound techniques to establish the desired lower bound for Q?*

We establish that some instantiations of hardness magnification overcome the natural proofs barrier in the following sense: slightly superlinear-size circuit lower bounds for certain versions of the minimum circuit size problem MCSP imply the non-existence of natural proofs. As a corollary of our result, we show that certain magnification theorems not only imply strong worst-case circuit lower bounds but also rule out the existence of efficient learning algorithms.

Hardness magnification might sidestep natural proofs, but we identify a source of difficulty when trying to adapt existing lower bound techniques to prove strong lower bounds via magnification. This is captured by a *locality barrier*: existing magnification theorems *unconditionally* show that the problems Q considered above admit highly efficient circuits extended with small fan-in oracle gates, while lower bound techniques against weak circuit models quite often easily extend to circuits containing such oracles. This explains why direct adaptations of certain lower bounds are unlikely to yield strong complexity separations via hardness magnification.

1 Introduction

Proving circuit size lower bounds for explicit Boolean functions is a central problem in Complexity Theory. Unfortunately, it is also notoriously hard, and arguments ruling out a wide range of approaches have been discovered. The most prominent of them is the *natural proofs barrier* of Razborov and Rudich [46].

A candidate approach for overcoming this barrier was investigated recently by Oliveira and Santhanam [42]. *Hardness Magnification* identifies situations where strong circuit lower bounds for explicit Boolean functions (e.g. $\mathsf{NP} \not\subseteq \mathsf{P/poly}$) follow from much weaker (e.g. slightly superlinear) lower bounds for specific natural problems. As discussed in [42], in some cases the lower bounds required for magnification are already known for explicit problems, but not yet for the problem for which the magnification theorem holds. This approach to lower bounds has attracted the interest of several researchers, and a number of recent works have proved magnification results [36, 13, 39, 12, 38, 10] (see also [50, 3, 35, 37] for related previous work). We provide a concise review of existing results in Appendix A.1.

In this work, we are interested in understanding the prospects of proving new lower bounds using hardness magnification, including potential barriers.

1.1 Hardness Magnification Frontiers

While hardness magnification is a broad phenomenon, its most promising instantiations seem to occur in the setting of circuit classes such as NC^1. The potential of hardness magnification stems from establishing the following scenario.

> **HM Frontier:** There is a natural problem Q and a computational model $\mathcal{C}$ such that:
> 1. (*Magnification*) $Q \notin \mathcal{C}$ implies $\mathsf{NP} \not\subseteq \mathsf{NC}^1$ or a similar breakthrough.
> 2. (*Evidence of Hardness*) $Q \notin \mathcal{C}$ under a standard conjecture.
> 3. (*Lower Bound against $\mathcal{C}$*) $L \notin \mathcal{C}$, where L is a simple function like PARITY.
> 4. (*Lower Bound for Q*) $Q \notin \mathcal{C}^-$, where $\mathcal{C}^-$ is slightly weaker than $\mathcal{C}$.

A frontier of this form provides hope that the required lower bound in Item 1 is true (thanks to Item 2), and that it might be within the reach of known techniques (thanks to Items 3 and 4, which provide evidence that we can analyse the circuit model and the problem). HM frontiers have been already achieved in earlier works with a striking example appearing in [39] (see also [10]). Despite the number of works in this area, we note that the HM frontier is achieved only by some magnification theorems (Item 3 is often unknown; e.g. in the case of results in [3, 13]).

---

[1] Most of this work was completed while Igor C. Oliveira was affiliated with the University of Oxford.

In order to make our subsequent discussion more concrete, we provide five examples of HM frontiers. Some of these results are new or require an extension of previous work, and the relevant statements will be explained in more detail in Section 3. The list of frontiers is not meant to be exhaustive, but we have tried to cover different computational models.

(A) HM Frontier for $\mathsf{MKtP}[n^c, 2n^c]$ and $\mathsf{AC}^0\text{-XOR}$:

A1. If $\mathsf{MKtP}[n^c, 2n^c] \notin \mathsf{AC}^0\text{-XOR}[N^{1.01}]$ for large $c > 1$ then $\mathsf{EXP} \not\subseteq \mathsf{NC}^1$ (Section 3.1).

A2. $\mathsf{MKtP}[n^c, 2n^c] \notin \mathsf{P/poly}$ for large enough c under exponentially secure PRFs [46].

A3. $\mathsf{Majority} \notin \mathsf{AC}^0\text{-XOR}[2^{N^{o(1)}}]$ (immediate from [44, 49]).

A4. $\mathsf{MKtP}[n^c, 2n^c] \notin \mathsf{AC}^0$ for any sufficiently large constant c (Section 3.1).

A. $\mathsf{MKtP}[s, t]$ refers to the promise problem of determining if an N-bit input has Levin Kolmogorov complexity at most s versus at least t (cf. [39]). Here $N = 2^n$. The $\mathsf{AC}^0\text{-XOR}$ model is the extension of AC^0 where gates at the bottom layer of the circuit can compute arbitrary parity functions. $\mathsf{AC}^0\text{-XOR}[s]$ denotes $\mathsf{AC}^0\text{-XOR}$ circuits of size s where the size is measured as the number of gates. This circuit class has received some attention in recent years (cf. [15]), and a few basic questions about AC^0 circuits with parity gates (such as constructing PRGs of seed length $o(n)$ and learnability using random examples) remain open for $\mathsf{AC}^0\text{-XOR}$ as well.

(B) HM Frontier for $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ and $\mathsf{Formula\text{-}XOR}$:

B1. $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \notin \mathsf{Formula\text{-}XOR}[N^{1.01}]$ implies $\mathsf{NQP} \not\subseteq \mathsf{NC}^1$ (Section 3.2).

B2. $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \notin \mathsf{P/poly}$ under standard cryptographic assumptions [46].

B3. $\mathsf{InnerProduct} \notin \mathsf{Formula\text{-}XOR}[N^{1.99}]$ (immediate consequence of [52]).

B4. $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \notin \mathsf{Formula}[N^{1.99}]$ ([23]; see also [39]).

B.   Here, NQP is nondeterministic quasi-polynomial time, $\mathsf{InnerProduct}$ is the Boolean function defined as $\mathsf{InnerProduct}(x, y) = \sum_i x_i \cdot y_i \pmod 2$, where $x, y \in \{0, 1\}^N$, $\mathsf{Formula\text{-}XOR}[s]$ refers to the class of Boolean formulas over the De Morgan basis with at most s leaves, where each leaf is an XOR of arbitrary arity over the inputs[2], and $\mathsf{MCSP}[s, t]$ denotes a promise problem over $N = 2^n$ input bits with YES inputs being truth-tables of Boolean functions on n inputs which are computable by circuits of size s, and NO instances being truth-tables of Boolean functions which are hard for circuits of size t.

(C) HM Frontier for $\mathsf{MCSP}[2^{n^{1/2}}/10n, 2^{n^{1/2}}]$ and Almost-Formulas:

C1. $\mathsf{MCSP}[\frac{2^{n^{1/2}}}{10n}, 2^{n^{1/2}}] \notin N^{0.01}\text{-Almost-Formula}[N^{1.01}]$ implies $\mathsf{NP} \not\subseteq \mathsf{NC}^1$ (Section 3.3).

C2. $\mathsf{MCSP}[\frac{2^{n^{1/2}}}{10n}, 2^{n^{1/2}}] \notin \mathsf{P/poly}$ under standard cryptographic assumptions [46].

C3. $\mathsf{PARITY} \notin N^{0.01}\text{-Almost-Formula}[N^{1.01}]$ (Section 3.3).

C4. $\mathsf{MCSP}[2^{n^{1/2}}/10n, 2^{n^{1/2}}] \notin \mathsf{Formula}[N^{1.99}]$ ([23]; see also [39]).

---

[2] Note that $\mathsf{Formula\text{-}XOR}[N^{1.01}] \subseteq \mathsf{Formula}[N^{3.01}]$. A better understanding of the former class is therefore necessary before we can understand the power and limitations of super-cubic formulas, which is a major open question in circuit complexity.

C.   An almost-formula is a circuit with a bounded number of gates of fan-out larger than 1. More precisely, a γ-Almost-Formula$[s]$ is a circuit containing at most s AND, OR, NOT gates of fan-in at most 2, and among such gates, at most γ of them have fan-out larger than 1. Consequently, this class naturally interpolates between formulas and circuits. This magnification frontier can be seen as progress towards establishing magnification theorems for worst-case variants of MCSP in the regime of sub-quadratic formulas (see the discussion in [39]).

(D) HM Frontier for MCSP$[2^{\sqrt{n}}]$ and one-sided error randomized formulas:

D1. MCSP$[2^{\sqrt{n}}] \notin$ GapAND$_{O(N)}$-Formula$[N^{2.01}] \Rightarrow$ NQP $\notin$ NC^1 (Section 3.2).
D2. MCSP$[2^{\sqrt{n}}] \notin$ P/poly under standard cryptographic assumptions [46].
D3.1. Andreev$_N \notin$ GapAND$_{O(N)}$-Formula$[N^{2.99}]$ (implicit in [21]).
D3.2. MCSP$[2^n/n^4] \notin$ GapAND$_{O(N)}$-Formula$[N^{2.99}]$ (implicit in [16]).
D4. MCSP$[2^{\sqrt{n}}] \notin$ GapAND$_{O(N)}$-Formula$[N^{1.99}]$ ([11], building on [23, 39]).

D.   GapAND$_N$ is the promise function on N bits such that it outputs 1 when all input bits are 1, and outputs 0 when at most $1/10$ of the input bits are 1. GapAND$_{O(N)}$-Formula$[s]$ denotes circuits with GapAND$_{O(N)}$ gate at the top with formulas of size s being inputs of the top gate. Therefore, GapAND$_{O(N)}$-Formula can be seen as *randomized formulas with one-sided error*.[3] The most interesting aspect of this magnification frontier is that the gap between the known hardness result and the magnification threshold is *nearly-tight* ($N^{2-\varepsilon}$ versus $N^{2+\varepsilon}$).[4]

(E) HM Frontier for $(n-k)$-Clique and AC^0:

E1. If $(n-k)$-Clique $\notin$ AC$^0[m^{1.01}]$ for some $k = (\log n)^C$, then NP $\not\subseteq$ NC^1 (Section 3.4).
E2. (Non-uniform) ETH $\Rightarrow (n-k)$-Clique $\notin$ P/poly for some $k = (\log n)^C$ (Section 3.4).
E3. Parity $\notin$ AC^0 [1, 19].
E4. $(n-k)$-Clique $\notin$ mP/poly for some $k = (\log n)^C$ ([4]; see Section 3.4).

E.   The ℓ-Clique problem is defined on graphs on n vertices in the adjacency matrix representation of size $m = \Theta(n^2)$. (The statements above refers to the regime of very large clique detection.) The class mP/poly refers to monotone circuits of polynomial size. In this frontier we are modifying Item 4 from HM frontier so that instead of slightly weaker $\mathcal{C}^-$ we consider an incomparable $\mathcal{C}^-$. This frontier is however particularly interesting, as items E1 and E4 connect hardness magnification to a basic question about the power of *non-monotone*

---

[3] Suppose there is a GapAND$_{O(N)}$-Formula circuit computing a function $f\colon \{0,1\}^N \to \{0,1\}$. Consider a uniform distribution of all sub-formulas below the top GapAND$_{O(N)}$ gate. Then for any input x, if $f(x) = 1$ then a sample formula from that distribution always outputs 1 on x, otherwise it outputs 0 with probability at least 0.9 on x. On the other hand, it is possible to derandomize a distribution of formulas computing f with one-sided error using a top GapAND$_{O(N)}$ gate.

[4] This tight threshold is first observed in [11], we include it here to show that the barrier discussed in this paper also applies to this particular setting.

circuits when computing *monotone* functions (see [14, 20] and references therein): Is every monotone function in AC^0 computable by a monotone (unbounded depth) boolean circuit of polynomial size? If this is the case, $\mathsf{NP} \not\subseteq \mathsf{NC}^1$ would follow.

Note that these hardness magnification frontiers offer different approaches to proving lower bounds against NC^1.

Essential Questions.   Do magnification theorems bring us closer to strong circuit lower bounds? In order to understand the limits and prospects of hardness magnification, the following questions are relevant.

Q1. *Naturalization.* Is hardness magnification a *non-naturalizing* approach to circuit lower bounds? If we accept standard cryptographic assumptions, non-naturalizability is a necessary property of any successful approach to strong circuit lower bounds.[5]

Q2. *Extending known lower bounds.* Can we adapt an existing lower bound proof from Items 3 and 4 in some HM frontier to show the lower bound required from Item 1 in that HM frontier? Is it possible to establish the required lower bounds via a reduction from L to Q?

Q3. *Improving existing magnification theorems.* Can we close the gap between Items 1 and 4 in HM frontier by establishing a magnification theorem that meets *known* lower bounds, such as the ones appearing in Item 4?

In the next sections, we present results that shed light into all these questions.

1.2   Hardness Magnification and Natural Proofs

The very existence of the natural proofs barrier provides a direction for proving strong circuit lower bounds: one can proceed by *refuting the existence of natural properties*.[6] In other words, a way to avoid natural proofs is to prove that there are no natural proofs. It is also easy to see that $\mathsf{P/poly}$-natural properties useful against $\mathsf{P/poly}$ can be turned into natural properties with much higher constructivity, e.g. into linear-size natural properties useful against circuits of polynomial-size. If read contrapositively, this gives a form of hardness magnification.

The initial hardness magnification theorem of Oliveira and Santhanam [42] proceeds in a similar fashion. It proposes to approach $\mathsf{NP} \not\subseteq \mathsf{P/poly}$ by deriving slightly superlinear circuit lower bounds for specific problems such as an *approximate version* of MCSP, which asks to distinguish truth-tables of Boolean functions computable by small circuits from truth-tables of Boolean functions which are hard to approximate by small circuits. Interestingly, this approach does not seem to naturalize, as it appears to yield strong lower bounds only for certain problems, and not for most of them. (The same heuristic argument appears in [3].) However, this is only an informal argument, and we would like to get stronger evidence that the natural proofs barrier does not apply here.

We show that hardness magnification for approximate MCSP can be used to conclude the *non-existence* of natural proofs against polynomial-size circuits. More precisely, we prove that if approximate MCSP requires slightly superlinear-size circuits, then there are no $\mathsf{P/poly}$-natural properties against $\mathsf{P/poly}$. This strongly suggests that the natural proofs

---

[5] We assume familiarity of the reader with the natural proofs framework of [46].
[6] A similar perspective has been employed in proof complexity in attempts to approach strong proof complexity lower bounds by extending the natural proofs barrier (see [34, 45]).

barrier isn't relevant to the magnification approach. Indeed, there remains the possibility that the weak circuit lower bound for MCSP in the hypothesis of the result can be shown using naturalizing techniques (as there aren't any strong enough plausible cryptographic conjectures known that rule this out), and yet by using magnification to "break" naturalness, we could get strong circuit lower bounds and even conclude the non-existence of natural proofs!

The core of our proof is the following new hardness magnification theorem: if approximate MCSP requires slightly superlinear-size circuits, then not only NP $\not\subseteq$ P/poly but *it is impossible even to learn efficiently*. We can then refute the existence of natural proofs by applying the known translation of natural properties to learning algorithms [8]. Similar implications hold with a *worst-case gap version* of MCSP (in the sense of HM Frontiers B and C but with different parameters) instead of approximate MCSP, following an idea from [22].

Interestingly, all the implications above are actually *equivalences*. In particular, the existence of natural properties is equivalent to the existence of highly efficient circuits for computing approximate MCSP and worst-case gap MCSP with certain parameters (cf. Theorem 1). This extends a known characterization of natural properties: Carmosino et al. [8] showed that P/poly natural proofs against P/poly are equivalent to learning P/poly by subexponential-size circuits, which was in turn shown to be equivalent by Oliveira and Santhanam [41] to the non-existence of non-uniform pseudorandom function families of sub-exponential security. The connection of hardness magnification to learning and pseudorandom function generators might be of independent interest, since it extends the consequences of magnification into two central areas in Complexity Theory.

▶ **Theorem 1** (Equivalences for Hardness Magnification)**.** *The following statements are equivalent:*[7]

(a) *Hardness of approximate* **MCSP** *against almost-linear size circuits.*
   There exist $c \geq 1$, $0 < \gamma < 1$, and $\varepsilon > 0$ such that $\mathsf{MCSP}[(n^c, 0), (2^{n^\gamma}, n^{-c})] \notin Circuit[N^{1+\varepsilon}]$.

(b) *Hardness of worst-case* **MCSP** *against almost-linear size circuits.*
   There exists $c \geq 1$ and $\varepsilon > 0$ such that $\mathsf{MCSP}[n^c, 2^n/n^c] \notin Circuit[N^{1+\varepsilon}]$.

(c) *Hardness of sub-exponential size learning using non-adaptive queries.*
   There exist $\ell \geq 1$ and $0 < \gamma < 1$ such that $Circuit[n^\ell]$ cannot be learned up to error $O(1/n^\ell)$ under the uniform distribution by circuits of size $2^{O(n^\gamma)}$ using non-adaptive membership queries.

(d) *Non-existence of natural properties against polynomial size circuits.*
   For some $d \geq 1$ there is no $Circuit[\mathsf{poly}(N)]$-natural property useful against $Circuit[n^d]$.

(e) *Existence of non-uniform* **PRFs** *secure against sub-exponential size circuits.*
   For every constant $a \geq 0$, there exists $d \geq 1$, a sequence $\mathfrak{F} = \{\mathcal{F}_n\}_{n \geq 1}$ of families $\mathcal{F}_n$ of n-bit boolean functions $f_n \in Circuit[n^d]$, and a sequence of probability distributions $\mathfrak{D} = \{\mathcal{D}_n\}_{n \geq 1}$ supported over $\mathcal{F}_n$ such that, for infinitely many values of n, $(\mathcal{F}_n, \mathcal{D}_n)$ is pseudo-random function family that $(1/N^{\omega(1)})$-fools (oracle) circuits of size $2^{a \cdot n}$.

The proof of this result appears in Section 4.1. We highlight below the most interesting implications of Theorem 1. (Note that some of them have appeared in other works in similar or related forms.)

▬ $(a) \rightarrow (d)$: The initial hardness magnification result from [42, Theorem 1] (stated for circuits) implies the *non-existence* of natural proofs useful against polynomial-size circuits, indicating that the natural proofs barrier might not be relevant to the magnification approach.

---

[7] See Preliminaries (Section 2) for definitions.

- (a), $(b) \leftrightarrow (d)$: *Any* P/poly natural property useful against P/poly can be transformed into an almost-linear size natural property that is simply the approximate MCSP$[(n^c, 0), (2^{n^\gamma}, n^{-c})]$ or worst-case gap MCSP$[n^c, 2^n/n^c]$. (Note the different regime of circuit size parameters for these problems.)
- (a), $(b) \leftrightarrow (c)$: A weak-seeming hardness assumption for worst-case gap and approximate versions of MCSP implies a strong non-learnability result: polynomial-size circuits cannot be learned over the uniform distribution even non-uniformly in sub-exponential time.
- (a), $(b) \leftrightarrow (e)$: Hardness magnification for MCSP also yields cryptographic hardness in a certain regime.

We note that the use of non-adaptive membership queries in Theorem 4.1 Item (c) is not essential. It follows from [8] that, in the context of learnability of polynomial size circuits under the uniform distribution in sub-exponential time, adaptive queries are not significantly more powerful than non-adaptive queries.[8]

Towards a more robust theory.   While Theorem 1 formally connects hardness magnification and natural properties, it would be very interesting to understand to which extent different hardness magnification theorems are provably non-naturalizable. This would provide a more complete answer to Question Q1 asked above. For instance, Theorem 1 leaves open whether hardness magnification for worst-case versions of MCSP such as MCSP$[n^c, 2^{n^\varepsilon}]$ refutes natural proofs as well. Note that one way of approaching this question would be to study reductions from MCSP$[n^c, 2^{n^\gamma}]$ to its approximate version MCSP$[(n^{c'}, 0), (2^{n^{\gamma'}}, n^{-c'})]$.[9] In Section 4.2, we observe that this question is related to the problem of basing *hardness of learning* on *worst-case assumptions* such as P $\neq$ NP (cf. [5]). We refer to the discussion in Section 4.2 for more details.

1.3   The Locality Barrier

The results from the preceding section show that hardness magnification can go beyond natural proofs. Is there another barrier that makes it difficult to establish lower bounds via magnification? In this section, we present a general argument to explain why the lower bound techniques behind A3-E3, A4-D4 in the magnification frontiers from Section 1.1 cannot be adapted (without significantly new ideas) to establish the required lower bounds in Items A1-E1, respectively. We refer to it as the *locality barrier*. While we will focus on these particular examples to make the discussion concrete, we believe that this barrier applies more broadly.

In order to explain the locality barrier, let's consider the argument behind the proof of B1 presented in Section 3.2. Recall that this result shows that if MCSP$[2^{n^{1/3}}, 2^{n^{2/3}}] \notin$ Formula-XOR$[N^{1.01}]$ then NQP $\not\subseteq$ NC^1. This and other known hardness magnification theorems are established in the contrapositive. The core of the argument is to prove that there are highly efficient Formula-XOR circuits that reduce an input to MCSP$[2^{n^{1/3}}, 2^{n^{2/3}}]$ of length $N = 2^n$ to deciding whether certain strings of length N' (much smaller than N) belong to

---

[8] In a bit more detail, one can easily extract a natural property from a learner that uses adaptive queries. In turn, closer inspection of the technique of [8] shows that a non-adaptive learner can be obtained from a natural property.

[9] More precisely, the existence of a reduction from MCSP$[n^c, 2^{n^\gamma}]$ to MCSP$[(n^{c'}, 0), (2^{n^{\gamma'}}, n^{-c'})]$ shows that lower bounds for the former problem yield lower bounds for the latter. Since any such lower bound must be non-naturalizable by Theorem 1, we obtain the same consequence for MCSP$[n^c, 2^{n^\gamma}]$. (Note that in the context of hardness magnification it is also important to have highly efficient reductions.)

a certain language L'. Then, under the assumption that $\mathsf{NQP} \subseteq \mathsf{NC}^1$, one argues that L' has polynomial size formulas. Finally, since $N' \ll N$, we can employ such formulas and still conclude that $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ is in $\mathsf{Formula\text{-}XOR}[N^{1.01}]$, which completes the proof.

Note that the argument above provides a *conditional* construction of highly efficient formulas for the original problem. Crucially, however, we can derive an *unconditional* circuit upper bound from this argument: If we stop right before we replace the calls to L' by an algorithm for L' (this is what makes the reduction conditional), it unconditionally follows that $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ can be computed by highly efficient $\mathsf{Formula\text{-}XOR}$ circuits containing oracle gates of small fan-in, for some oracle. Similarly, one can argue that the problems in Items A1-E1 can be computed in the respective models by highly efficient Boolean devices containing oracles of small fan-in.

We stress that, as opposed to a magnification theorem, where one cares about the complexity of the oracle gates, in our discussion of the locality barrier we only need the fact that there is *some way* of setting these oracles gates so that the resulting circuit or formula solves the original problem. (A definition of this model appears in Section 2.5.) A more exhaustive interpretation of magnification theorems as construction of circuits with small fan-in oracles can be found in Appendix A.2.

On the other hand, we argue that the lower bound arguments from Items A3-E3 of the hardness magnification frontiers quite easily handle (in the respective models) the presence of oracles of small fan-in, *regardless of the function* computed by these oracles. Using a more involved argument we can localize also lower bounds from items A4-D4. Consequently, these methods do not seem to be refined enough to prove the lower bounds required by A1-D1 without excluding oracle circuits that are unconditionally known to exist for the corresponding problems.

Following the example above, we state our results for the Magnification Frontier B.

▶ **Theorem 2** (Locality Barrier for HM Frontier B). *The following results hold.*

- (B1$^{\mathcal{O}}$) (Oracle Circuits from Magnification) :   *For any $\varepsilon > 0$, $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \in \mathsf{Formula\text{-}\mathcal{O}\text{-}XOR}[N^{1.01}]$ for some oracle $\mathcal{O}$, where every oracle gate has fan-in at most N^ε and appears in the layer right above the XOR leaves.*

- (B3$^{\mathcal{O}}$) (Extension of Lower Bound Techniques Above Magnification Threshold) :   *For any $\delta > 0$, $\mathsf{InnerProduct}$ over N input bits cannot be computed by $N^{2-3\delta}$-size $\mathsf{Formula\text{-}\mathcal{O}\text{-}XOR}$ circuits with at most $N^{2-3\delta}$ oracle gates of fan-in N^δ in the layer right above the XOR leaves, for any oracle $\mathcal{O}$.*

- (B4$^{\mathcal{O}}$) (Extension of Lower Bound Techniques Below Magnification Threshold) :   *There is a universal constant c such that for all constants $\varepsilon > 0$ and $\alpha > 2$, $\mathsf{MCSP}[n^c, 2^{\varepsilon/\alpha \cdot n}]$ cannot be computed by oracle formulas F with $\mathsf{SIZE}_3(F) \leq N^{2-\varepsilon}$ and adaptivity $o(\log N/ \log \log N)$.*[10]

Here, $\mathsf{Size}_t(F)$ denotes the size of the formula, if we replace every oracle $\mathcal{O}$ with fan-in β in F by a formula of size β^t which reads all its inputs exactly β^{t-1} times (see Section 5.2.2 for the motivation of this definition).

The first two items of Theorem 2 are proved in Section 5.1.2. The third item is proved in Section 5.2.2. While Theorem 2 does not specify that, we actually localize all proofs of the lower bounds from B3 and B4 we are aware of. Interestingly, the localization of B4 allows us to refute the Anti-Checker Hypothesis from [39] (and a family of potential hardness magnification theorems), cf. Section 5.2.2. We refer to Section 5 for analogous statements describing the locality barrier in frontiers A, C, D, and E.

---

[10] That is, on any path from root to a leaf, there are at most $o(\log N/ \log \log N)$ oracles.

Locality of Computations and Lower Bound Techniques.   The fact that many lower bound techniques extend to computational devices with oracles of small fan-in was observed already by Yao in 1989 on a paper on local computations [56]. According to Yao, a local function is one that can be efficiently computed using only localized processing elements. In our terminology, this corresponds to circuits with oracles of small fan-in. Among other results, [56] argues that Razborov's monotone circuit size lower bound for k-Clique [43] and Karchmer and Wigderson's monotone formula size lower bound for ST-CONN [31] extend to boolean devices with monotone oracles of bounded fan-in. Compared to Yao's work, our motivation and perspective are different. While Yao is particularly interested in lower bounds that can be extended in this sense (see e.g. Sections 2 and 6 in [56]), here we view such extensions as a *limitation* of the corresponding arguments, meaning that they are not refined enough to address the locality barrier.[11]

We note, however, that not every lower bound technique extends to circuits with small fan-in oracles.[12] For instance, by the work of Allender and Koucký [3] (also a more recent work by Chen and Tell [13]), the parity function Parity_n over n input bits can be computed by a TC^0 circuit of size $O(n)$ (number of wires) containing $\leq n^{1-\varepsilon}$ oracle gates of fan-in $\leq n^\varepsilon$, provided that its depth $d = O(1/\varepsilon)$. On the other hand, it is known that $\mathsf{Parity}_n \notin \mathsf{TC}^0_d[n^{1+c^{-d}}]$ for a constant $c > 0$ [25] (again, the complexity measure is the number of wires). Since the latter lower bound is super-linear for every choice of d, it follows by the result of [3, 13] that it cannot be extended to circuits containing a certain number of oracles of fan-in n^ε, for a large enough depth d that depends on ε. Incidentally, the hardness magnification theorems of [3, 13] do not achieve a magnification frontier.

In Section 3.2 we identify one specific lower bound related to HM frontier D which is both above the magnification threshold and provably non-localizable, cf. Theorem 49. In principle, there might be ways to overcome the locality barrier and match the lower bound with the magnification threshold. We refer to Section 1.4 below for additional discussion.

On Lower Bounds Through Reductions.   The discussion above has focused on the possibility of *directly* adapting existing lower bounds from Item 3 in HM frontier to establish the desired lower bound in Item 1. There is however an *indirect* approach that one might hope to use: *reductions*. For instance, in the context of the HM Frontier B discussed above, can we have a reduction from InnerProduct to $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ that would allow us to show that $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \notin \mathsf{Formula}\text{-}\mathsf{XOR}[N^{1.01}]$? The first thing to notice is that, for this approach to make sense, the reduction needs to have a specific form so that composing the reduction with a candidate Formula-XOR circuit for $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ violates the hardness of InnerProduct. Is there any hope to design a reduction of this form?

The locality barrier presents a *definitive answer* in this case. Indeed, it is immediate from the first two items of Theorem 2 that such a reduction *does not exist*. For the same reason, it is not possible to use reductions to establish the required lower bounds in some other magnification frontiers, cf. Section 5.1.6.

---

[11] On a more technical level, we are interested in the regime of barely super-linear size circuits and formulas, and our results do not impose a monotonicity constraint on the oracle.

[12] Of course any such discussion depends on parameters such as number of oracles and their fan-ins, so whether a technique avoids or not the locality barrier is relative to a particular magnification theorem.

1.4   Concluding Remarks and Open Problems

Hardness magnification shows that obtaining a refined understanding of weak computational models is an approach to major complexity lower bounds, such as separating EXP from NC^1. As discussed in Sections 1.1 and 1.2 above, its different instantiations are connected to a few basic questions in Complexity Theory, including the power of non-monotone operations, learnability of circuit classes, and pseudorandomness.

One of our main conceptual contributions in this work is to identify a challenge when implementing this strategy for lower bounds. Quoting the influential article [46] that introduced the natural proofs barrier,

> *"We do not conclude that researchers should give up on proving serious lower bounds. Quite the contrary, by classifying a large number of techniques that are unable to do the job we hope to focus research in a more fruitful direction."*

> Razborov and Rudich [46, Section 6]

We share a similar opinion with respect to hardness magnification and the obstruction identified in Section 1.3. While locality provides a unified explanation for the difficulty of adapting combinatorial lower bound techniques to exploit most (if not all) known magnification frontiers, it might be possible to discover new HM frontiers whose associated lower bound techniques in Item 3 are sensitive to the presence of small fan-in oracles. For instance, in the case of *uniform* complexity lower bounds, this has been achieved in [38] via an indirect diagonalization that explores the theory of pseudorandomness.[13] Alternatively, it might be possible to establish magnification theorems using a technique that does not produce circuits with small fan-in oracles. Even if one is pessimistic about these possibilities, we believe that an important contribution of the theory of hardness magnification is to break the divide between "weak" and "strong" circuit classes advocated by the natural proofs barrier, and that it deserves further investigation.

We finish with a couple of technical questions related to our contributions. First, we would like to understand if it is possible to strengthen items (a) and (b) in Theorem 1 to a wider range of parameters. For example, is hardness magnification for worst-case $\mathsf{MCSP}[n^c, 2^{n^\gamma}]$ with $\gamma < 1$ non-naturalizable? The core of this question seems to be the problem of reducing worst-case MCSP from item (a) to approximate MCSP from item (b).

Another important direction is to show that hardness magnification avoids natural proofs also in the context of *non*-meta-computational problems. Interestingly, many magnification theorems from [39] established for MCSP and variants were subsequently shown to hold for any sparse language in NP [10]. Could it be the case that hardness magnification overcomes natural proofs in a much broader sense?

Finally, it would be useful to investigate the locality of additional lower bound techniques. Can we, for example, come up with non-localizable lower bounds similar to Theorem 49 which would be above the magnification threshold and work for a problem more closely related to the one from the corresponding HM frontier?

---

[13] In other words, the magnification theorem discussed in [38] admits a formulation for uniform randomized algorithms, and its proof provides an algorithm with oracle gates of small fan-in in the spirit of the oracle circuits discussed here. Nevertheless, the unconditional lower bound established in the same paper does not extend to algorithms with such oracle gates.

2   Preliminaries

2.1   Notation

Given a Boolean function $f : \{0,1\}^n \to \{0,1\}$, $\mathsf{tt}(f)$ denotes the 2^n-bit string representing the truth table of f. On the other hand, for any string $y \in \{0,1\}^{2^n}$, define f_y as the function on n inputs such that $\mathsf{tt}(f_y) = y$.

$\mathsf{Circuit}[s]$ denotes fan-in two Boolean circuits of size at most s where we count the number of gates. $\mathsf{Formula}[s]$ denotes formulas over the basis U_2 (fan-in two ANDs and ORs) of size at most s (counting the number of leaves) with input leaves labelled by literals or constants.

For a circuit class $\mathcal{C}$, $\mathcal{C}[s]$ denotes circuits from $\mathcal{C}$ of size at most s.

A function $f : \{0,1\}^n \to \{0,1\}$ is γ-approximated by a circuit C, if $\Pr_x[C(x) = f(x)] \geq \gamma$.

2.2   Complexity of Learning

▶ **Definition 3** (Learning). *A circuit class $\mathcal{C}$ is learnable over the uniform disribution by circuits in $\mathcal{D}$ up to error ε with confidence δ if there are randomized oracle $\mathcal{D}$-circuits L^f such that for every boolean function $f : \{0,1\}^n \mapsto \{0,1\}$ computable by a circuit from $\mathcal{C}$, when given oracle access to f, input 1^n and the internal randomness $w \in \{0,1\}^*$, L^f outputs the description of a circuit satisfying*

$$\Pr_w\{L^f(1^n, w) \ (1-\varepsilon)\text{-approximates } f\} \geq \delta.$$

L^f uses non-adaptive membership queries if the set of queries which L^f makes to the oracle does not depend on the answers to previous queries. If $\delta = 1$, we omit mentioning the confidence parameter.

2.3   Natural Properties, MCSP, and its Variants

Let $\mathcal{F}_n$ be the set of all functions on n variables. $\mathfrak{R} = \{\mathcal{R}_n \subseteq \mathcal{F}_n\}_{n \in \mathbb{N}}$ is a combinatorial property of Boolean functions.

▶ **Definition 4** (Natural property [46]). *Let $\mathfrak{R} = \{\mathcal{R}_n\}$ be a combinatorial property, $\mathcal{C}$ be a circuit class and Γ be a complexity class. Then, $\mathfrak{R}$ is a Γ-natural property useful against $\mathcal{C}[s(n)]$, if there exists an $n_0 \in \mathbb{N}$ such that the following hold:*

- ***Constructivity*** : *For any function $f_n \in \mathcal{F}_n$, the predicate $f_n \overset{?}{\in} \mathcal{R}_n$ is computable in Γ in the size of the truth table of f_n.*
- ***Largeness*** : *For every $n \geq n_0$, $\Pr_{f_n \sim \mathcal{F}_n}\{f_n \in \mathcal{R}_n\} \geq \frac{1}{2^{O(n)}}$.*
- ***Usefulness*** : *For every $n \geq n_0$, $\mathcal{R}_n \cap \mathcal{C}[s(n)] = \emptyset$.*

The following result which follows from [8] connects the existence of natural properties useful against a class $\mathcal{C}$ to designing learning algorithms for $\mathcal{C}$.

▶ **Theorem 5** (From Theorem 5.1 of [8] and Lemma 14 of [27]). *Let R be a P/poly-natural property useful against $\mathsf{Circuit}[n^d]$ for some $d \geq 1$. Then, for each $\gamma \in (0,1)$, there are randomized, oracle circuits $\{D_n\}_{n \geq 1} \in \mathsf{Circuit}[2^{O(n^\gamma)}]$ that learn $\mathsf{Circuit}[n^k]$ up to error $\frac{1}{n^k}$ using non-adaptive oracle queries to f_n, where $k = \frac{d\gamma}{a}$ and a is a universal constant that does not depend on d and γ.*

▶ **Definition 6** (Gap MCSP). *Let $s, t : \mathbb{N} \to \mathbb{N}$, where $s(n) \leq t(n)$ and $0 \leq \varepsilon, \sigma < 1/2$. Define MCSP$[(s, \sigma), (t, \varepsilon)]$ on inputs of length $N = 2^n$, as the following promise problem :*

- *YES instances: $y \in \{0, 1\}^N$ such that there exists a circuit of size $s(n)$ that $(1 - \sigma)$-approximates f_y.*
- *NO instances: $y \in \{0, 1\}^N$ such that no circuit of size $t(n)$ $(1 - \varepsilon)$-approximates f_y.*

We refer to MCSP$[(s, 0), (t, 0)]$ as MCSP$[s, t]$. Informally speaking, if $\varepsilon > 0$, we say that MCSP$[(s, 0), (t, \varepsilon)]$ is an *approximate* version of MCSP. Otherwise, it is a *worst-case* version of MCSP.

▶ **Remark 7.** In Definition 6, if $s(n) = t(n)$, we also require that $\sigma < \varepsilon$ for the yes and no instances to be disjoint.

▶ **Definition 8** (Succinct MCSP). *For functions $s, t : \mathbb{N} \mapsto \mathbb{N}$, Succinct-MCSP$[s(n), t(n)]$ is the following problem. Given an input $\langle 1^n, 1^s, (x_1, b_1), \ldots, (x_t, b_t)\rangle$ where $x_i \in \{0, 1\}^n, b_i \in \{0, 1\}$, decide if there is a circuit C of size s such that $\bigwedge_{i=1,\ldots,t} C(x_i) = b_i$.*

2.4 Pseudorandom Generators

▶ **Definition 9** (Pseudorandom function families). *For any circuit class $\mathcal{C}$, size functions $s(n), t(n) \geq n$, family $\mathcal{G}_n$ of n-bit Boolean functions and distribution $\mathcal{D}_n$ over $\mathcal{G}_n$, we say that a pair $(\mathcal{G}_n, \mathcal{D}_n)$ is a $(t(n), \varepsilon(n))$-pseudorandom function family (PRF) in $\mathcal{C}[s(n)]$, if each function in $\mathcal{G}_n$ is in $\mathcal{C}[s(n)]$ and for every randomized circuit $A^O \in \text{Circuit}^O[t(n)]$, where O denotes oracle access to a fixed Boolean function over n inputs, we have*

$$\left| \Pr_{g \sim \mathcal{D}_n, w} \{ A^g(w) = 1 \} - \Pr_{f \sim \mathcal{F}_n, w} \{ A^f(w) = 1 \} \right| \leq \varepsilon(n)$$

[41] state an equivalence between the non-existence of PRFs in a circuit class $\mathcal{C}$ and learning algorithms for $\mathcal{C}$. In particular, we care about the following direction which they prove using a small-support version of Von-Neumann's Min-max Theorem.

▶ **Theorem 10** (No PRFs in $\mathcal{C}$ implies Learning Algorithm for $\mathcal{C}$ [41]). *Let $t(n) \leq 2^{O(n)}$. Suppose that for every $k \geq 1$ and large enough n, there exists no $(\text{poly}(t(n)), 1/10)$-pseudorandom function families in $\mathcal{C}[n^k]$. Then, for every $\varepsilon > 0, k \geq 1$ and large enough n, there is a randomized oracle circuit in $\text{Circuit}^O[2^{n^\varepsilon}]$ that learns every function $f_n \in \mathcal{C}[n^k]$ up to error $1/n^k$ with confidence $1 - 1/n$, where O denotes membership query access to f_n.*

2.5 Local Circuit Classes

Our definition of local computation is somewhat similar to some definitions appearing in [56].

▶ **Definition 11** (Local circuit classes). *Let $\mathcal{C}$ be a circuit class (such as $\text{AC}^0[s]$, $\text{TC}^0_d[s]$, Circuit$[s]$, etc). For functions $q, \ell, a \colon \mathbb{N} \to \mathbb{N}$, we say that a language L is in $[q, \ell, a]-\mathcal{C}$ if there exists a sequence $\{E_n\}$ of oracle circuits for which the following holds:*

(i) *Each oracle circuit E_n is a circuit from $\mathcal{C}$.*
(ii) *There are at most $q(n)$ oracle gates in E_n, each of fan-in at most $\ell(n)$, and any path from an input gate to an output gate encounters at most $a(n)$ oracle gates.*
(iii) *There exists a language $\mathcal{O} \subseteq \{0, 1\}^*$ such that the sequence $\{E_n^{\mathcal{O}}\}$ (E_n with its oracle gates set to $\mathcal{O}$) computes L.*

In the definition above, q stands for *quantity*, ℓ for *locality*, and a for *adaptivity* of the corresponding oracle gates.

2.6   Random Restrictions

Let $\rho : [N] \to \{0, 1, *\}$ be a *restriction*, and $\boldsymbol{\rho}$ be a *random restriction*, i.e., a distribution of restrictions. We say that $\boldsymbol{\rho}$ is p-regular if $\Pr[\boldsymbol{\rho}(i) = *] = p$ and $\Pr[\boldsymbol{\rho}(i) = 0] = \Pr[\boldsymbol{\rho}(i) = 1] = (1 - p)/2$ for every $i \in [N]$. We also say $\boldsymbol{\rho}$ is k-wise independent if any k coordinates of $\boldsymbol{\rho}$ are independent. For a function $f : \{0, 1\}^N \to \{0, 1\}$, we use $f{\restriction}_\rho$ to denote the function $\{0, 1\}^{|\rho^{-1}(*)|} \to \{0, 1\}$ obtained by restricting f according to ρ in the natural way.

We need the following lemma stating that one can sample from a k-wise independent random restriction with a short seed, and moreover all restrictions have a small circuit description.

▶ **Lemma 12** ([24, 55]). *There exists a q-regular k-wise independent random restriction $\boldsymbol{\rho}$ distributed over $\rho : [N] \to \{0, 1, *\}$ samplable with $O(k \cdot \log(N) \log(1/q))$ bits. Furthermore, each output coordinate of the random restriction can be computed in time polynomial in the number of random bits.*

2.7   Technical Results

▶ **Lemma 13** (Hoeffding's inequality). *Let $X_1, \ldots, X_n$ be independent random variables such that $0 \le X_i \le 1$ for every $i \in [n]$. Let $X = \sum_{i=1}^{n} X_i$. Then, for any $\varepsilon > 0$, we have*

$$\Pr\{|X - \boldsymbol{E}X| \ge \varepsilon n\} \le 2 \exp(-2\varepsilon^2 n)$$

3   Magnification Frontiers

3.1 $\mathsf{EXP} \not\subseteq \mathsf{NC}^1$ and AC^0-XOR Lower Bounds for MKtP

In this Section, we present the proofs of the new results stated in HM Frontier A. Recall that $\mathsf{Kt}(x)$ is defined as the minimum over $|M| + \log t$ such that a program M outputs x in t steps. For thresholds $\theta, \theta' : \mathbb{N} \to \mathbb{N}$, we denote by $\mathsf{MKtP}[\theta(N), \theta'(N)]$ the promise problem whose YES instances consist of the strings $x \in \{0, 1\}^N$ such that $\mathsf{Kt}(x) \le \theta(N)$ and NO instances consist of the strings such that $\mathsf{Kt}(x) > \theta'(N)$.

We start with the hardness magnification theorem of HM Frontier A1.

▶ **Theorem 14.** *There exists a constant c such that, for every large enough constant $d > 1$,*

$$\mathsf{MKtP}[(\log N)^d, (\log N)^d + c \log N] \notin \mathsf{AC}^0\text{-XOR}[N^{1.01}] \quad \textit{implies} \quad \mathsf{EXP} \not\subseteq \mathsf{NC}^1.$$

Proof. We prove the contrapositive. Assume that $\mathsf{EXP} \subseteq \mathsf{NC}^1$. First, recall that any N-bit-input polynomial-size NC^1 circuit can be converted into a depth-d' AC^0 circuit of size $2^{N^{O(1/d')}}$ for every positive integer constant d' (see, e.g., [2, Lemma 8.1]).

Oliveira, Pich, and Santhanam [39] showed that there exists a problem $L \in \mathsf{EXP}$ such that $\mathsf{MKtP}[\theta(N), \theta(N) + c \log N] \in \mathsf{AND}_{O(N)}\text{-}L_{O(\theta(N))}\text{-XOR}$ for $\theta(N) \ge \log N$. (Here the subscript denotes the fan-in of a gate.) That is, the promise problem $\mathsf{MKtP}[\theta(N), \theta(N) + c \log N]$ can be computed by the following form of an L-oracle circuit: The output gate is an AND gate of fan-in $O(N)$, at the middle layer are L-oracle gates of fan-in $O(\theta(N))$, and at the bottom layer are XOR gates. Under the assumption that $\mathsf{EXP} \subseteq \mathsf{NC}^1$, we can replace L-oracle circuits with depth-d' AC^0 circuits of size $2^{(\log N)^{O(d/d')}}$, which is smaller than $N^{0.01}$ by choosing a constant d' large enough. In particular, we obtain a depth-$(d' + O(1))$ almost linear size AC^0 circuit with bottom XOR gates that computes $\mathsf{MKtP}[\theta(N), \theta(N) + c \log N]$. ◀

The rest of this section is devoted to proving the following AC^0 lower bound for MKtP, which establishes HM Frontier A4.

▶ **Theorem 15.** *For any $d = d(N)$, for some $\theta(N) = d \cdot \widetilde{O}(\log N)^3$ and any $\theta'(N) = N/\omega(\log N)^d$, it holds that $\mathsf{MKtP}[\theta(N), \theta'(N)] \notin \mathsf{AC}^0_d$.*

Note that Theorem 15 is only meaningful if $d = o(\log N / \log \log N)$, because otherwise the promise problem is not well-defined.

The idea of the proof is as follows: Trevisan and Xue [54] showed that there exists a pseudorandom restriction ρ of seed length $\mathsf{polylog}(N)$ that shrinks every polynomial-size depth-2 circuit into shallow decision trees. Moreover, the expected fraction of unrestricted variables $\rho^{-1}(*)$ is at least $p = \Omega(1/\log N)$. In particular, by composing d independent pseudorandom restrictions $\rho_1, \cdots, \rho_d$, every depth-d circuit can be turned into a constant function, while still leaving at least p^d-fraction of inputs unrestricted. The seed length required to sample d independent pseudorandom restrictions is at most $d \times \mathsf{polylog}(N)$, and thus $\mathsf{Kt}(0^N \circ \rho) \leq \mathsf{polylog}(N)$. We stress that the exponent of the seed length does not depend on d. Since the circuit hit with the pseudorandom restriction becomes a constant function, it cannot distinguish $0^N \circ \rho$ with $U_N \circ \rho$, i.e., the distribution where the unrestricted variables of ρ are replaced with the uniform distribution U_N. Assuming that there remain sufficiently many unrestricted inputs (e.g., $N/O(\log N)^d \gg \mathsf{polylog}(N)$), the latter distribution has a large Kt complexity, which is a contradiction to the fact that the circuit computes a gap version of MKtP.

We note that Cheraghchi, Kabanets, Lu, and Myrisiotis [16] used the pseudorandom restriction method in order to obtain an exponential-size AC^0 lower bound. A crucial difference in this work is that instead of optimizing the size of AC^0 circuits, we aim at minimizing the threshold θ of $\mathsf{MKtP}[\theta]$.

Following [54], in order to generate a random restriction $\rho \in \{0, 1, *\}^N$ that leaves a variable unrestricted with probability 2^{-q}, we regard a binary string $w \in \{0, 1\}^{(q+1)N}$ as a random restriction ρ_w. Specifically:

▶ **Definition 16.** *For a string $w \in \{0, 1\}^{(q+1)N}$, we define a restriction $\rho_w \in \{0, 1, *\}^N$ as follows: Write w as $(w_1, b_1) \cdots (w_N, b_N)$, where $w_i \in \{0, 1\}^q$ and $b_i \in \{0, 1\}$. For each $i \in [N]$, if $w_i = 1^q$ then set $\rho_w(i) := *$; otherwise, set $\rho_w(i) := b_i$.*

Note that this is defined so that $\Pr_w[\rho_w(i) = *] = 2^{-q}$ for every $i \in [N]$, when w is distributed uniformly at random.

Trevisan and Xue [54] showed that Håstad's switching lemma can be derandomized by using a distribution that fools CNFs. To state this formally, we need the following definitions. Define a t-width CNF as one which has at most t literals in each clause. We say that a distribution $\mathcal{D}$ over $\{0, 1\}^n$ ε-fools a set of functions $\mathcal{S}_n$ over n variables if for every $f \in \mathcal{S}_n$, $|\Pr_{x \sim D}\{f(x) = 1\} - \Pr_{x \sim U_n}\{f(x) = 1\}| \leq \varepsilon$. Finally, define $\mathsf{DT}(f)$ as the depth of the smallest decision tree computing f.

▶ **Lemma 17** (Derandomized Switching Lemma [54, Lemma 7]). *Let φ be a t-width M-clause CNF formula over N inputs. Let $p = 2^{-q}$ for some $q \in \mathbb{N}$. Assume that a distribution $\mathcal{D}$ over $\{0, 1\}^{(q+1)N}$ ε_0-fools $M \cdot 2^{t(q+1)}$-clause CNFs. Then,*

$$\Pr_{w \sim \mathcal{D}}[\mathsf{DT}(\varphi\!\restriction_{\rho_w}) > s] \leq 2^{s+t+1}(5pt)^s + \varepsilon_0 \cdot 2^{(s+1)(2t + \log M)}.$$

▶ **Theorem 18** (Based on [54] and [53, Theorem 56]). *Let $s, M, d, N \in \mathbb{N}$ be positive integers. Let $p = 2^{-q}$ for some $q \in \mathbb{N}$ so that $1/128s \leq p < 1/64s$. Assume that there is a pseudorandom generator $G \colon \{0, 1\}^r \to \{0, 1\}^{(q+1)N}$ that ε_0-fools CNFs of size $M \cdot 2^s \cdot 2^{s(q+1)}$. Then, there exists a distribution $\mathcal{R}$ of random restrictions that satisfies the following:*

1. *For every circuit C of size M and depth d over N inputs,*

$$\Pr_{\rho \sim \mathcal{R}}[\mathsf{DT}(C\restriction_\rho) > s] \le M \cdot \left(2^{-s+1} + \varepsilon_0 \cdot 2^{(s+1)(3s+\log M)} \right).$$

2. *For any parameter $\delta < 1$, with probability at least $1 - N(\delta + d\varepsilon_0)$, the number of unrestricted variables in $[N]$ is at least $\lfloor N \cdot p^{d-1}/64 \log(1/\delta) \rfloor$.*
3. *$\mathcal{R}$ can be generated by a seed of length dr in polynomial time.*

Proof. We apply the derandomized switching lemma (Lemma 17) d times. In the first iteration, we set $p := 1/64$ (and $q := 6$) and generate $\rho_{G(z)[1,\cdots,(6+1)N]}$. (Here we use the first $(6+1)N$ bits of $G(z)$ to generate $\rho_{G(z)}$.) This turns a circuit C of size M into a circuit whose bottom fan-in is at most s. For every other iteration i (where $i = 2, \cdots, d$), we set $p := 2^{-q}$ and turn a circuit C of depth $d - i + 2$ into a circuit of depth $d - i + 1$. Our final pseudorandom restriction $\rho \sim \mathcal{R}$ is defined by the composition of the d independent pseudorandom restrictions $\rho_{G(z_1)[1,\cdots,(6+1)N]}, \rho_{G(z_2)}, \cdots, \rho_{G(z_d)}$.

Our proof is essentially the same with [53], except that (1) we apply the switching lemma d times (instead of $d - 1$) in order to turn depth-d circuits into shallow decision trees, and (2) in [54, 53], for the application of constructing a pseudorandom generator for AC^0, fixed bits of pseudorandom restrictions must be generated by using truly random bits, whereas in our case we generate all the bits by using G.

In more detail, for each $i \in [d]$, let M_i be the number of the gates at level i in C (i.e., the gates whose distance from the input gates is i). At the first iteration, we set $p := 1/64 = 2^{-6}$ and $q := 6$. We then generate $\rho^1 := \rho_{G(z_1)[1,\cdots,(6+1)N]}$ by choosing a seed $z_1 \sim \{0,1\}^r$ uniformly at random. We regard C as a depth-$(d+1)$ circuit of bottom fan-in 1, and apply Lemma 17 to each gate at level 1 (in the original circuit C). The probability that there exists a gate at level 1 in $C\restriction_{\rho^1}$ that cannot be computed by a decision tree of depth s is bounded above by

$$M_1 \cdot \left(2^{s+1+1}(5/64)^s + \varepsilon_0 \cdot 2^{(s+1)(2+\log M)} \right).$$

In the complement event, each gate at level 1 can be written as DNFs and CNFs of width s, and hence can be merged into some gate at level 2. Thus a circuit $C\restriction_{\rho^1}$ can be turned into a circuit of depth d and bottom fan-in s. Moreover, the number of gates at level 1 is bounded by $M \cdot 2^s$, which is an invariant preserved during the iterations.

For every other iteration i ($i = 2, \cdots, d$), we generate $\rho^i := \rho_{G(z_i)}$ by choosing a seed $z_i \sim \{0,1\}^r$ uniformly at random. Using the invariant that the number of gates at level $i - 1$ is at most $M \cdot 2^s$, the probability that some gate at level i in $C\restriction_{\rho^1 \cdots \rho^i}$ cannot be computed by a decision tree of depth s is bounded above by

$$M_i \cdot \left(2^{s+s+1}(5ps)^s + \varepsilon_0 \cdot 2^{(s+1)(2s+\log(M2^s))} \right).$$

In the complement event, every gate at level i can be written as width-s CNFs or DNFs of size 2^s, and hence can be merged into some gate at level $i+1$ (for $i < d$). At the last iteration (i.e., $i = d$), the circuit $C\restriction_{\rho^1 \cdots \rho^d}$ can be written as a decision tree of depth s. We define the pseudorandom restriction ρ as $\rho^d \circ \cdots \circ \rho^1$. Item 3 is obvious from this construction.

Overall, the probability that $\mathsf{DT}(C\restriction_\rho) > s$ is at most $M \cdot (2^{-s+1} + \varepsilon_0 \cdot 2^{(s+1)(3s+\log M)})$. This completes the proof of Item 1.

To see Item 2, we divide N input bits into k disjoint blocks $T_1, \cdots, T_k$ of size at least t (and hence $k = \lfloor N/t \rfloor$), where t is a parameter chosen later. We claim that each block must contain at least one unrestricted variable in $\rho \sim \mathcal{R}$ with high probability (and hence $|\rho^{-1}(*)| \ge \lfloor N/t \rfloor$).

Fix any block $T = T_i$ for some $i \in [k]$. As in [53], one can easily observe that the condition that every variable in T is restricted can be checked by a CNF of size at most $|T| (\leq N)$. By a simple hybrid argument, the concatenation of d independent pseudorandom distributions $G(z_1), \cdots, G(z_d)$ $d\varepsilon_0$-fools CNFs (cf. [53, Corollary 55]). Therefore, the probability that every variable in T is restricted by $\rho \sim \mathcal{R}$ is bounded by $(1 - p^{d-1}/64)^t + d\varepsilon_0$, where the first term is an upper bound for the probability that every variable in T is restricted by a truly random restriction. Choosing $t = 64 \log(1/\delta)/p^{d-1}$ and using a union bound, the probability that some block T_i is completely fixed can be bounded above by $\lfloor N/t \rfloor \cdot (\delta + d\varepsilon_0)$, which completes the proof of Item 2. ◀

▶ **Corollary 19.** *For every circuit C of size M ($\geq N$) and depth d over N inputs, there exists a restriction ρ such that*
1. *$C{\upharpoonright}_\rho$ is a decision tree of depth at most $s := 2 \log 8M$,*
2. *$|\rho^{-1}(*)| \geq N/O(\log M)^d$, and*
3. *$\mathsf{Kt}(\rho) \leq d \cdot \widetilde{O}((\log M)^3)$.*

Proof. Tal [53, Theorem 52] showed that there exists a polynomial-time pseudorandom generator G of seed length $r := \widetilde{O}(\log M_0 \cdot \log(M_0/\varepsilon_0))$ that ε_0-fools CNFs of size M_0. We set $M_0 := M \cdot 2^s \cdot 2^{s(q+1)}$, $s := 2 \log 8M$, and $\varepsilon_0 := 2^{-9s^2}$. Then the seed length r of G is at most $r = \widetilde{O}(\log M_0 \cdot \log(M_0/\varepsilon_0)) = \widetilde{O}(\log M \cdot (\log M)^2)$. Applying Theorem 18, the probability that $\mathsf{DT}(C{\upharpoonright}_\rho) > s$ is bounded by $\frac{1}{2}$. Choosing $\delta = 1/8N$, we also have that the probability that $|\rho^{-1}(*)| < \lfloor N \cdot p^{d-1}/64 \log(1/\delta) \rfloor$ is at most $\frac{1}{4}$. Thus there exists some restriction ρ in the support of $\mathcal{R}$ such that $\mathsf{DT}(C{\upharpoonright}_\rho) \leq s$ and $|\rho^{-1}(*)| \geq \Omega(N \cdot p^{d-1}/\log N) \geq N/O(\log M)^d$. ◀

Using the assumption that a circuit computes MKtP, we show that shallow decision trees must be a constant function.

▶ **Lemma 20.** *Let C be a circuit and ρ be a restriction such that $C{\upharpoonright}_\rho$ is a decision tree of depth s. If $\mathsf{MKtP}[O(s \log N) + \mathsf{Kt}(\rho)] \subseteq C^{-1}(1)$, then $C{\upharpoonright}_\rho \equiv 1$.*

Proof. We prove the contrapositive. Assume that $C{\upharpoonright}_\rho \not\equiv 1$, which means that there is a path $\pi \colon [N] \to \{0, 1, *\}$ of a decision tree $C{\upharpoonright}_\rho$ that assigns at most s variables so that $C{\upharpoonright}_{\rho\pi} \equiv 0$. Note that $\mathsf{Kt}(\pi) \leq O(s \log N)$, because one can specify each restricted variable of π by using $O(\log N)$ bits. Thus we have $\mathsf{Kt}(0^N \circ \pi \circ \rho) \leq O(s \log N) + \mathsf{Kt}(\rho)$. On the other hand, $C(0^N \circ \pi \circ \rho) = C{\upharpoonright}_{\rho\pi}(0^N) = 0$. Therefore, we obtain $\mathsf{MKtP}[O(s \log N) + \mathsf{Kt}(\rho)] \not\subseteq C^{-1}(1)$. ◀

Now we are ready to prove the main result of this section.

Proof of Theorem 15. Assume, by way of contradiction, that there is a circuit C of size $M := N^{O(1)}$ and depth d that computes $\mathsf{MKtP}[d \cdot \widetilde{O}(\log N)^3, N/\omega(\log N)^d]$. Using Corollary 19, we take a restriction ρ such that $C{\upharpoonright}_\rho$ is a decision tree of depth $s = O(\log N)$. By Lemma 20, we have $C{\upharpoonright}_\rho \equiv 1$, under the assumption that $O(s \log N) + \mathsf{Kt}(\rho) \leq \theta(N)$, which is satisfied by choosing $\theta(N)$ large enough. Now, by counting the number of inputs accepted by $C{\upharpoonright}_\rho$, we obtain

$$2^{N/O(\log N)^d} \leq 2^{|\rho^{-1}(*)|} = |(C{\upharpoonright}_\rho)^{-1}(1)| \leq 2^{\theta'(N)+1},$$

where, in the last inequality, we used the fact that the number of strings whose Kt complexity is at most $\theta'(N)$ is at most $2^{\theta'(N)+1}$. However, the inequality contradicts the choice of $\theta'(N)$. ◀

3.2   NQP $\not\subseteq$ NC^1 and Formula-XOR or GapAND-Formula for MCSP

This section is devoted to proving HM Frontier B1 and HM Frontier D1. In fact, we provide two different proofs of HM Frontier B1, one based on [42], another one based on [10].

In both proofs, the hardness magnification is achieved by constructing an oracle circuit for MCSP. The most interesting part of the first proof is that it gives a *conditional* construction assuming QP $\subseteq$ P/poly. While the oracle circuit construction can be made *unconditional* (as in the second proof), it illustrates a potentially more applicable approach: proving the hardness magnification theorem while assuming the target circuit lower bound is false (i.e., NQP $\subseteq$ NC^1).

3.2.1   Reduction Based Approach from [42]

In the initial magnification theorem [42, Theorem 1], approximate MCSP was shown to admit hardness magnification phenomena. Here we present a similar hardness magnification theorem for a worst-case version of MCSP.

A natural way of reducing worst-case MCSP to approximate MCSP is to apply error-correcting codes. Error-correcting codes map a hard Boolean function to a Boolean function which is hard on average. A problem with this approach is that error-correcting codes do not guarantee that an easy Boolean function will be mapped to an easy Boolean function. Our main idea is to enforce the latter property with an extra assumption QP $\subseteq$ P/poly. Here, QP denotes TIME$[n^{\log^{O(1)} n}]$. Similarly, NQP will stand for NTIME$[n^{\log^{O(1)} n}]$.

We will use the following explicit error-correcting code.

▶ **Theorem 21** (Explicit linear error-correcting codes [30, 48])**.** *There exists a sequence* $\{E_N\}_{N \in \mathbb{N}}$ *of error-correcting codes* $E_N \colon \{0,1\}^N \to \{0,1\}^{M(N)}$ *with the following properties:*
- $E_N(x)$ *can be computed by a uniform deterministic algorithm running in time* poly(N).
- $M(N) = b \cdot N$ *for a fixed* $b \geq 1$.
- *There exists a constant* $\delta > 0$ *such that any codeword* $E_N(x) \in \{0,1\}^{M(N)}$ *that is corrupted on at most a* δ-*fraction of coordinates can be uniquely decoded to* x *by a uniform deterministic algorithm* D *running in time* poly$(M(N))$.
- *Each output bit is computed by a parity function: for each input length* $N \geq 1$ *and for each coordinate* $i \in [M(N)]$, *there exists a set* $S_{N,i} \subseteq [N]$ *such that for every* $x \in \{0,1\}^N$,

$$E_N(x)_i = \bigoplus_{j \in S_{N,i}} x_j.$$

Under the assumption that QP $\subseteq$ P/poly, we present an efficient reduction from worst-case MCSP to approximate MCSP: Given the truth table of a function f, we simply map it to $E_N(\mathsf{tt}(f))$. The following lemma establishes the correctness of this reduction.

▶ **Lemma 22** (Reducing worst-case MCSP to approximate MCSP)**.** *Assume QP $\subseteq$ P/poly. Then the error-correcting code* E_N *from Theorem 21 satisfies the following:*

1. $f_n \in Circuit[2^{n^{1/3}}] \Rightarrow E_N(tt(f_n)) \in Circuit[2^{\sqrt{m}}]$,[14]
2. $f_n \notin Circuit[2^{n^{2/3}}] \Rightarrow E_N(tt(f_n))$ *is hard to* $(1 - \delta)$-*approximate by* $2^{\sqrt{m}}$-*size circuits, where* $m = \Theta(n)$.

---

[14] Here we identify $E_N(\mathsf{tt}(f_n))$ with the function whose truth table is $E_N(\mathsf{tt}(f_n))$.

Proof. For the first implication we consider the map

$$C, i \mapsto E_N(\mathsf{tt}(C))_i$$

where C is a circuit with n inputs and size $2^{n^{1/3}}$, $i \in \{0,1\}^m$, and $m = \log|E_N|$. The map takes an input of length $2^{O(n^{1/3})}$, and is computable in time $2^{O(n)}$; hence the map is in $\mathsf{QP} \subseteq \mathsf{P/poly}$. Thus, there exists a circuit F of size $2^{O(n^{1/3})}$ that, taking the description of a circuit C of size $2^{n^{1/3}}$ and $i \in \{0,1\}^m$ as input, outputs the ith bit of $E_N(\mathsf{tt}(C))$. Therefore if f_n is computed by a circuit C of size $2^{n^{1/3}}$, the function $i \mapsto E_N(\mathsf{tt}(f_n))_i$ is computable by a circuit $F(C, \text{-})$ of size $2^{O(n^{1/3})} < 2^{\sqrt{m}}$.

The second implication is obtained in a similar way by considering the map

$$C, i \mapsto D_N(\mathsf{tt}(C))_i$$

where C is a circuit with $m = \log|E_N|$ inputs and size $2^{\sqrt{m}}$, $i \in \{0,1\}^n$ and D_N is an efficient decoder of E_N. The new map is computable in time $2^{O(m)}$ and again is in $\mathsf{QP} \subseteq \mathsf{P/poly}$. Therefore if $E_N(\mathsf{tt}(f_n))$ is $(1-\delta)$-approximated by a circuit C of size $2^{\sqrt{m}}$, f_n is computable by a circuit of size $2^{O(\sqrt{m})} < 2^{n^{2/3}}$. ◄

Since the error-correcting code of Theorem 21 can be computed by using one layer of XOR gates, we obtain the following corollary.

▶ **Corollary 23.** *If* $\mathsf{QP} \subseteq \mathsf{P/poly}$, *then* $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ *is reducible to* $\mathsf{MCSP}[(2^{\sqrt{n}}, 0), (2^{\sqrt{n}}, \delta)]$ *by using a many-one reduction computed by a linear-size circuit of* XOR *gates.*

We are ready to prove the main result of this section:

▶ **Theorem 24** (Magnification for worst-case MCSP via error-correcting codes)**.**
Assume that $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \notin \mathsf{Formula}\text{-}\mathsf{XOR}[N^{1+\varepsilon}]$ *for some constant* $\varepsilon > 0$. *Then either* $\mathsf{QP} \not\subseteq \mathsf{P/poly}$ *or* $\mathsf{NP} \not\subseteq \mathsf{NC}^1$. *In particular,* $\mathsf{NQP} \not\subseteq \mathsf{NC}^1$.

Proof. We prove the contrapositive. Assume that $\mathsf{QP} \subseteq \mathsf{P/poly}$ and $\mathsf{NP} \subseteq \mathsf{NC}^1$. [42, Lemma 16] shows that $\mathsf{NP} \subseteq \mathsf{NC}^1$ implies $\mathsf{MCSP}[(2^{\sqrt{n}}, 0), (2^{\sqrt{n}}, \delta)] \in \mathsf{Formula}[N^{1+\varepsilon}]$ for any constant $\varepsilon > 0$. By combining this with Corollary 23, we obtain that $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \in \mathsf{Formula}\text{-}\mathsf{XOR}[O(N^{1+\varepsilon})]$. ◄

3.2.2   Kernelization Based Approach from [10]

Now we give another proof of HM Frontier B1 by adapting techniques from [10]. In fact, the following proof implies (under a straightforward adjustment of parameters) both HM Frontier B1 and HM Frontier D1.

▶ **Theorem 25** (Magnification for worst-case MCSP via kernelization for GapAND-Formula-XOR)**.** *Assume that* $\mathsf{MCSP}[2^{n^{1/3}}] \notin \mathsf{GapAND}_{O(N)}\text{-}\mathsf{Formula}\text{-}\mathsf{XOR}[N^{\varepsilon}]$ *for some constant* $\varepsilon > 0$. *Then* $\mathsf{NQP} \not\subseteq \mathsf{NC}^1$.

Proof Sketch. The following proof is just an adaption of Theorem 3.4 of [10].
Let $N = 2^n$ and $s = 2^{n^{1/3}} = 2^{(\log N)^{1/3}}$. Let $S = \mathsf{MCSP}[2^{n^{1/3}}]^{-1}(1)$ (that is, all yes instances of $\mathsf{MCSP}[2^{n^{1/3}}]$ on inputs of length N), and $m = |S|$. We have that $m \leq s^{O(s)}$. Let E_N be the error correcting code from Theorem 21. Recall that E_N maps from $\{0,1\}^N$ to $\{0,1\}^{b \cdot N}$ for a constant b.

Let $T = c_1 \cdot \log m$ for a large enough constant c_1. Suppose we pick T random indexes $I = (i_1, i_2, \ldots, i_T)$ from $[b \cdot N]$ independently and uniformly at random. Given $x \in \{0,1\}^N$, let $H_I(x) := (E_N(x)_{i_1}, E_N(x)_{i_2}, \ldots, E_N(x)_{i_T})$.

By a Chernoff bound and a union bound, we can see that with high probability over random choices of I, all inputs from S are mapped into distinct strings in $\{0,1\}^T$ by H_I. We fix such a good collection of indexes I_{good}.

Now, consider the following language

$$L_{\mathsf{check}} : [b \cdot N]^T \times \{0,1\}^T \times [b \cdot N] \times \{0,1\} \to \{0,1\},$$

which takes as inputs I (hash function coordinates), w (hash value), i (index), and z (check-bit). $L_{\mathsf{check}}(I, w, i, z)$ guesses an input $y \in \{0,1\}^N$, and accepts if $H_I(y) = w$, $\mathsf{MCSP}[2^{n^{1/3}}](y) = 1$, and $E_N(y)_i = z$. It is easy to see that L_{check} is in NQP.

Given $x \in \{0,1\}^N$, we claim that $\mathsf{MCSP}[2^{n^{1/3}}](x) = 1$ iff $L_{\mathsf{check}}(I_{\mathsf{good}}, H_{I_{\mathsf{good}}}(x), i, E_N(x)_i) = 1$ for all $i \in [b \cdot N]$.

1. When $\mathsf{MCSP}[2^{n^{1/3}}](x) = 1$, on the particular guess $y = x$, $L_{\mathsf{check}}(I_{\mathsf{good}}, H_{I_{\mathsf{good}}}(x), i, E_N(x)_i)$ accepts for all $i \in [b \cdot N]$.
2. When $\mathsf{MCSP}[2^{n^{1/3}}](x) = 0$, we set $z = H_{I_{\mathsf{good}}}(x)$. By our choice of I_{good}, there is at most one x' satisfying both $\mathsf{MCSP}[2^{n^{1/3}}](x') = 1$ and $H_{I_{\mathsf{good}}}(x') = z$. If there is no such x', then all $L_{\mathsf{check}}(I_{\mathsf{good}}, H_{I_{\mathsf{good}}}(x), i, x_i)$ reject. Otherwise, we have $x \neq x'$. Let i be an index such that $E_N(x)_i \neq E_N(x')_i$. Then $L_{\mathsf{check}}(I_{\mathsf{good}}, H_{I_{\mathsf{good}}}(x), i, E_N(x)_i)$ rejects.

Moreover, in the second case, $L_{\mathsf{check}}(I_{\mathsf{good}}, H_{I_{\mathsf{good}}}(x), i, E_N(x)_i)$ indeed rejects at least for a constant fraction of $i \in [b \cdot N]$, since $E_N(x)$ is an error correcting code,

Now suppose $\mathsf{NQP} \subseteq \mathsf{NC}^1$ for the sake of contradiction. Since $H_{I_{\mathsf{good}}}(x)$ can be computed by $T = N^{o(1)}$ many XOR gates (I_{good} is hardwired into the circuit), we can construct $b \cdot N$ $\mathsf{Formula\text{-}XOR}[N^{o(1)}]$ circuits $C_1, C_2, \ldots, C_{b \cdot N}$, such that if $\mathsf{MCSP}[2^{n^{1/3}}](x) = 1$ then $C_i(x) = 1$ for all x, and otherwise $C_i(x) = 0$ for a constant fraction of i's.

By a simple error reduction via random sampling, we construct $m = O(N)$ $\mathsf{Formula\text{-}XOR}[N^{o(1)}]$ circuits $D_1, D_2, \ldots, D_m$, such that if $\mathsf{MCSP}[2^{n^{1/3}}](x) = 1$ then $D_i(x) = 1$ for all x, and otherwise $D_i(x) = 0$ for at least a 0.9 fraction of inputs. Hence, we have $\mathsf{MCSP}[2^{n^{1/3}}] \in \mathsf{GapAND}_{O(N)}\text{-}\mathsf{Formula\text{-}XOR}[N^{o(1)}]$, a contradiction to the assumption. ◄

▶ Remark 26. We note that $\mathsf{GapAND}_{O(N)}\text{-}\mathsf{Formula\text{-}XOR}[N^\varepsilon]$ circuits are a special case of both $\mathsf{Formula\text{-}XOR}[N^{1+\varepsilon}]$ circuits and $\mathsf{GapAND}_{O(N)}\text{-}\mathsf{Formula}[N^{2+\varepsilon}]$ circuits. Therefore, the above proof implies both HM Frontier B1 and HM Frontier D1.

3.3 $\mathsf{NP} \not\subseteq \mathsf{NC}^1$ and Almost-Formula Lower Bounds for MCSP

Recall that near-quadratic *formula* lower bounds are known for $\mathsf{MCSP}[2^{n^{o(1)}}, 2^{n^{o(1)}}]$. On the other hand, a hardness magnification obtained by a super efficient construction of anticheckers established in [39] states that $\mathsf{NP} \subseteq \mathsf{P/poly}$ implies almost linear-size circuits for a worst-case version of parameterized $\mathsf{MCSP}[2^{n^{o(1)}}, 2^{n^{o(1)}}]$. Consequently, if we could make the hardness magnification work for formulas, $\mathsf{NP} \not\subseteq \mathsf{NC}^1$ would follow. We make a step in this direction by showing that $\mathsf{NP} \subseteq \mathsf{NC}^1$ implies the existence of almost-formulas of almost linear size solving the worst-case $\mathsf{MCSP}[2^{n^{o(1)}}, 2^{n^{o(1)}}]$, cf. Theorem 29. This is established by a more detailed analysis of the proof from [39] extended with an application of the Valiant-Vazirani Isolation Lemma (cf. [6, Lemma 17.19]) in the process of selecting anticheckers. We also observe that almost-formulas of sub-quadratic size cannot solve PARITY, cf. Theorem 30. These results yield HM Frontier C1 and HM Frontier C3.

We start the presentation with a lemma needed to derive HM Frontier C1.

▶ **Lemma 27** (Anticheckers). *Assume* $\mathsf{NP} \subseteq \mathsf{NC}^1$. *Then for any* $\lambda \in (0,1)$ *there are circuits* $\{C_{2^n}\}_{n=1}^{\infty}$ *of size* $2^{n+O(n^\lambda)}$ *which given* $\mathsf{tt}(f) \in \{0,1\}^N$, *outputs* $2^{O(n^\lambda)}$ *n-bit strings* $y_1, \ldots, y_{2^{O(n^\lambda)}}$ *together with bits* $f(y_1), \ldots, f(y_{2^{O(n^\lambda)}})$ *forming a set of anticheckers for* f, *i.e. if f is hard for circuits of size* 2^{n^λ} *then every circuit of size* $2^{n^\lambda}/2n$ *fails to compute* f *on one of the inputs* $y_1, \ldots, y_{2^{O(n^\lambda)}}$. *Moreover, each* $y_i, f(y_i)$ *is generated by a subcircuit of* C_{2^n} *with inputs* $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1}), \mathsf{tt}(f)$ *whose only gates with fanout* > 1 *are* $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$.

Proof. This proof follows [39]. Our contribution here is the "moreover" part, but we also give a more succinct self-contained proof. For each Boolean function f the desired set of anticheckers is known to exist, the only problem is to find it with a circuit of the desired size and formula-like form. In order to do so, we will simulate the proof of the existence of anticheckers, but make the involved counting constructive by using linear hash functions and the assumption $\mathsf{NP} \subseteq \mathsf{NC}^1$. Additionally, for the "moreover" part of the lemma, we will employ the Valiant-Vazirani Isolation Lemma (cf. [6, Lemma 17.19]) in the process of selecting good anticheckers.

Let $\lambda \in (0,1)$ and f be a Boolean function with n inputs hard for circuits of size 2^{n^λ}. For j n-bit strings $y_1, \ldots, y_j$ and $s \in [0,1]$, define a predicate

$$P_f(y_1, \ldots, y_j)[s] \text{ iff } \leq s \text{ fraction of all circuits of size } 2^{n^\lambda}/2n \text{ compute } f \text{ on } y_1, \ldots, y_j.$$

Further, let $R_f(y_1, \ldots, y_j)$ be the number of circuits of size $2^{n^\lambda}/2n$ which do not make any error on $y_1, \ldots, y_j$ when computing f. Note that P_f and R_f depend on j values of f, not on the whole $\mathsf{tt}(f)$, but for simplicity we do not display them.

Suppose that given $\mathsf{tt}(f)$ we already generated $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$ such that $P_f(y_1, \ldots, y_{i-1})[(1-1/4n)^{i-1}]$ holds. For $i = 1$ the generated set is empty. We want to find $y_i, f(y_i)$ such that $P_f(y_1, \ldots, y_i)[(1-1/4n)^i]$. In order to do so, we will construct a formula $F(y_1, \ldots, y_i, f(y_1), \ldots, f(y_i))$ of size $2^{O(n^\lambda)}$ (if $i \leq 2^{O(n^\lambda)}$) such that under the assumption $R_f(y_1, \ldots, y_{i-1}) \geq 2n^2$,

$$F(y_1, \ldots, y_i, f(y_1), \ldots, f(y_i)) = 1 \quad \Rightarrow \quad P_f(y_1, \ldots, y_i)[(1-1/4n)^i]$$

$$P_f(y_1, \ldots, y_{i-1})[(1-1/4n)^{i-1}] \quad \Rightarrow \quad \exists y_i, F(y_1, \ldots, y_i, f(y_1), \ldots, f(y_i)) = 1.$$

Assume for now that we already have such a formula F. We firstly show how to find $y_i, f(y_i)$ given F by an exhaustive search through all n-bit strings in combination with Valiant-Vazirani Lemma.

Consider a $2^{O(n^\lambda)}$-size formula $F^{r,h}(y_1, \ldots, y_{i-1}, z, f(y_1), \ldots, f(y_{i-1}), f(z))$ computing the following predicate

$$F(y_1, \ldots, y_{i-1}, z, f(y_1), \ldots, f(y_{i-1}), f(z)) \wedge h(z) = 0^r \tag{1}$$

where $z \in \{0,1\}^n$, $r \leq n+2$ and $h \in \mathcal{H}_{n,r}$ for a pairwise independent efficiently computable hash function collection $\mathcal{H}_{n,r}$ from $\{0,1\}^n$ to $\{0,1\}^r$. Formula $F^{r,h}$ exists since $\mathsf{NP} \subseteq \mathsf{NC}^1$. By Valiant-Vazirani Lemma, for fixed $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$, if h is chosen randomly from $\mathcal{H}_{n,r}$ and r randomly from $\{2, \ldots, n+1\}$, then with probability $\geq 1/8n$, there is a unique z satisfying (1). Therefore, the probability that none of $2^{O(n^\lambda)}$ many randomly chosen tuples r, h guarantees a unique solution is $< (1-1/8n)^{2^{O(n^\lambda)}} \leq 1/2^{2^{O(n^\lambda)}/8n}$. That

is, there exist a set $\mathcal{R}$ of $2^{O(n^\lambda)}$ tuples r, h such that for each $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$, at least one tuple r, h from $\mathcal{R}$ will guarantee a unique solution. Consequently, for each $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$ for at least one $r, h \in \mathcal{R}$ the following $2^{n+O(n^\lambda)}$-size formula

$$\bigvee_{k=1, \ldots, 2^n} (b_j^k \wedge F^{r,h}(y_1, \ldots, y_{i-1}, b^k, f(y_1), \ldots, f(y_{i-1}), f(b^k))),$$

where b_j^k is the jth bit of the kth n-bit string b^k (in the lexicographic order), outputs the jth bit of a good antichecker y_i. Since $\mathsf{NP} \subseteq \mathsf{NC}^1$, we can select the right y_i from the $2^{O(n^\lambda)}$ candidate strings corresponding to tuples r, h from $\mathcal{R}$ by applying a formula of size $2^{O(n^\lambda)}$ on top of them. Having y_i, a formula of size $poly(n)2^n$ with access to $\mathsf{tt}(f)$ can generate $f(y_i)$.

Iteratively, a circuit of size $2^{n+O(n^\lambda)}$ will generate $y_1, \ldots, y_{2^{O(n^\lambda)}}, f(y_1), \ldots, f(y_{2^{O(n^\lambda)}})$ such that $P_f(y_1, \ldots, y_{2^{O(n^\lambda)}})[(1-1/4n)^{2^{O(n^\lambda)}}]$ as long as $R_f(y_1, \ldots, y_{2^{O(n^\lambda)}}) \geq 2n^2$. Deciding whether $R_f(y_1, \ldots, y_i) \geq 2n^2$ is in $\mathsf{NP} \subseteq \mathsf{NC}^1$ (on input $y_1, \ldots, y_i, f(y_1), \ldots, f(y_i), 1^{2^{n^\lambda}}$), so there are formulas of size $2^{O(n^\lambda)}$ for it. Since $(1 - 1/4n)^{2^{O(n^\lambda)}} \leq 1/2^{2^{O(n^\lambda)}/4n}$, we reach $R_f(y_1, \ldots, y_i) < 2n^2$ with $i \leq 2^{O(n^\lambda)}$. When this happens, the remaining $< 2n^2$ circuits of size $2^{n^\lambda}/2n$ can be generated by an $\mathsf{NP}^{\mathsf{coNP}}$ algorithm, and since $\mathsf{NP} \subseteq \mathsf{NC}^1$, by a formula of size $2^{O(n^\lambda)}$. Finally, for each of the remaining circuits we can find an n bit string witnessing its error exhaustively by a formula of size $2^{n+O(n^\lambda)}$. Altogether, the desired anticheckers $y_1, \ldots, y_{2^{O(n^\lambda)}}$ with bits $f(y_1), \ldots, f(y_{2^{O(n^\lambda)}})$ will be generated by a circuit of size $2^{n+O(n^\lambda)}$. Note that this circuit will have the desired formula-like structure because its only gates with fanout bigger than 1 are those computing tuples $y_i, f(y_i)$.

$\triangleright$ **Claim 28.** If $P_f(y_1, \ldots, y_{i-1})[(1 - 1/4n)^{i-1}]$ and $R_f(y_1, \ldots, y_{i-1}) \geq 2n^2$, then for some y_i, $P_f(y_1, \ldots, y_i)[(1 - 1/4n)^{i-1}(1 - 1/2n)]$.

Claim 28 is proved by a standard counting argument, cf. [39, Claim 22]. Observe that with Claim 28 we can construct the desired formula F. Here we employ approximate counting with linear hash functions: if $X \subseteq \{0,1\}^m$ is a set of size s, there are matrices $A_1, \ldots, A_{\log(4s^c)}$ such that each A_j defines a linear function mapping a Cartesian power X^c to $(s(1+\varepsilon))^c/\log(4s^c)$, for $c = 2(\varepsilon^{-1}(\log \log s + \log \varepsilon^{-1}))$. Moreover, for each A_j there is $X_j^c \subseteq X^c$ satisfying $\forall x \in X_j^c \forall x' \in X^c$ $(x \neq x' \rightarrow A_j(x) \neq A_j(x'))$, and $\bigcup_j X_j^c = X^c$. Mapping $x \in X^c$ to $A_j(x)$ in the jth block of size $(s(1+\varepsilon))^c/\log(4s^c)$, for the first A_j with $x \in X_j^c$, thus defines an injection from X^c to $(s(1+\varepsilon))^c$ which witnesses that the size of X is $\leq s(1+\varepsilon)$. See e.g. [28, Section 3, 2nd paragraph] for details.

Therefore, once we have $P_f(y_1, \ldots, y_i)[(1 - 1/4n)^{i-1}(1 - 1/2n)]$ we can conclude that there are matrices $A_1, \ldots, A_{2^{O(n^\lambda)}}$ defining an injective mapping of a Cartesian power (with exponent of rate $poly(n)$) of the set of all circuits of size $2^{n^\lambda}/2n$ that compute f on $y_1, \ldots, y_i$ to the same Cartesian power of $(1 - 1/4n)^{i-1}(1 - 1/2n)(1 + 1/4n) \leq (1 - 1/4n)^i$ fraction of the set of all circuits of size $2^{n^\lambda}/2n$. The existence of such matrices, not only witnesses $P_f(y_1, \ldots, y_i)[(1 - 1/4n)^i]$ but is also an $\mathsf{NP}^{\mathsf{coNP}}$ property, and since $\mathsf{NP} \subseteq \mathsf{NC}^1$, decidable by a formula F of size $2^{O(n^\lambda)}$. $\blacktriangleleft$

▶ **Theorem 29** (Improved magnification via anticheckers). *Assume that* $MCSP[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ *is hard for circuits* C *(with* 2^n *inputs) of size* $2^{n+O(n^{1/2})}$ *with the following form. Given* $\mathsf{tt}(f)$*, subcircuits of* C *generate* $y_1, \ldots, y_{2^{O(n^{1/2})}}, f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$ *so that each* $y_i, f(y_i)$ *is generated by a subcircuit of* C *with inputs* $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1}), \mathsf{tt}(f)$ *whose only gates with fanout* > 1 *are* $y_1, \ldots, y_{i-1}, f(y_1), \ldots, f(y_{i-1})$. *Having* $y_1, \ldots, y_{2^{O(n^{1/2})}}, f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$*,* C *applies a formula of size* $2^{O(n^{1/2})}$ *on top of these gates.*

Then $\mathsf{NP} \not\subseteq \mathsf{NC}^1$.

Proof. If $NP \subseteq NC^1$, then $MCSP[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ can be solved by circuits of size $2^{n+O(n^{1/2})}$ of the required form: given a Boolean function f, apply Lemma 27 to generate a set of its anticheckers $y_1, \ldots, y_{2^{O(n^{1/2})}}$ together with bits $f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$ and using $NP \subseteq NC^1$ decide whether f is hard for circuits of size $2^{n^{1/2}}/2n$ on $y_1, \ldots, y_{2^{O(n^{1/2})}}$.   ◀

Note that circuits from the assumption of hardness magnification via anticheckers, Theorem 29, are $2^{O(n^{1/2})}$-almost formulas of almost linear size which gives us HM Frontier C1. We can now complement it with HM Frontier C3.

Consider an s-almost formula. Each gate G of F with fanout larger than 1 is computed by a formula with inputs being either the original inputs of F or gates of F with fanout larger than 1. We call any maximal formula of this form a *principal* formula of G.

▶ **Theorem 30.** *PARITY* $\notin n^\varepsilon$-almost-*Formula*$[n^{2-9\varepsilon}]$, *if* $\varepsilon < 1$.

Proof Sketch. For the sake of contradiction, assume PARITY has n^ε-almost formulas of size $n^{2-9\varepsilon}$. Since there are only n^ε gates of fanout > 1, we can replace these gates by appropriate constants and obtain formulas F_n of size $n^{2-8\varepsilon}$ computing PARITY with probability $\geq 1/2 + 1/2^{n^\varepsilon}$. In more detail, each formula F_n checks if the principal formulas compute the fixed constants. If this is the case, then F_n outputs the output of the original almost-formula (since gates with fan-out larger than 1 are fixed, the output can be computed by a formula). Otherwise, F_n outputs a fixed constant, whichever is better on the majority of the remaining inputs. This does not increase the size of the resulting formula F_n by more than a constant factor. As pointed out by Komargodski-Raz [32], each boolean function f on n input bits can be approximated by a real polynomial of degree $O(t\sqrt{L(f)}\frac{\log n}{\log \log n})$ up to a point-wise additive error of 2^{-t}, and this can be shown to imply that each formula of size $o((n/t)^2(\log \log n/\log n)^2)$ computes PARITY over n input bits with probability at most $1/2 + 1/2^{t+O(1)}$ (for large enough t). Taking $t = n^{2\varepsilon}$ we get a contradiction.   ◀

3.4 $NP \not\subseteq NC^1$ and AC^0 Lower Bounds for $(n-k)$-Clique

In this Section, we discuss the proofs of some statements claimed in HM Frontier E from Section 1.1. Recall that we consider graphs on n vertices that are described in the adjacency matrix representation. The input graph is therefore represented using $m = \Theta(n^2)$ bits. We begin with the proof of the magnification result in HM Frontier E1.

▶ **Proposition 31.** *Let* $k(n) = (\log n)^C$ *for some constant* C. *If there exists* $\varepsilon > 0$ *such that for every depth* $d \geq 1$, $(n-k)$-Clique $\notin AC^0_d[m^{1+\varepsilon}]$, *then* $NP \not\subseteq NC^1$.

Proof. We use a straightforward reduction to the magnification theorem for k-Vertex-Cover established in [42, Theorem 7]. (We state Proposition 31 in a slightly weaker form just for simplicity.) Indeed, a graph G on n vertices has a vertex cover of size $\leq k$ if and only if G has an independent set of size $\geq n - k$. In turn, the latter is true if and only if the complement graph $\overline{G}$ has a clique of size $\geq n - k$. Therefore, by negating input literals, the complexities of $(n-k)$-Clique and k-Vertex-Cover are equivalent with respect to AC^0 circuits. For this reason, the hardness magnification theorem of [42] immediately implies Proposition 31.   ◀

We state below conditional and unconditional lower bounds on the complexity of detecting very large cliques. The next proposition implies the lower bound claimed in HM Frontier E4.

▶ **Proposition 32** ([4]; see also [29, Section 9.2]). *For* $k(n) \leq n/2$, *every monotone circuit for* $(n-k)$-Clique *requires* $2^{\Omega(k^{1/3})}$ *gates*.

Interestingly, the problem can be solved by (bounded depth) polynomial size monotone circuits if $k \leq \sqrt{\log n}$ [4].

Finally, by the observation employed in the proof of Proposition 31, for non-monotone computations the complexities of detecting large cliques and small vertex covers are equivalent. A consequence of this is that one can show the following result, which implies the statement in HM Frontier E2.

▶ **Proposition 33.** *If ETH for non-uniform circuits holds, then $(n-k)$-Clique $\notin$ P/poly as long as $\omega(\log n) \leq k \leq n/2$.*

Indeed, under ETH the k-Vertex-Cover problem cannot be solved in time $2^{o(k)} \cdot \mathsf{poly}(m)$ (see [26] and [17, Theorem 29.5.9]). Further discussion on the conditional hardness of k-Vertex-Cover that also applies to $(n-k)$-Clique appears in [42].

4 Hardness Magnification and Natural Proofs

4.1 Equivalences

The main contribution of this section is new hardness magnification results showing non-learnability of circuit classes from slightly super-linear lower bounds for the approximate version of MCSP and the gap version of MCSP. We then use these magnification results to establish a series of equivalences.

▶ **Lemma 34** (Hardness Magnification for Learnability from Lower Bounds for Approximate MCSP). *Let $s, t : \mathbb{N} \to \mathbb{N}$ be size functions such that $n \leq s(n) \leq t(n)$ and ε, δ be parameters such that $\varepsilon < 1/2$, $0 \leq \delta \leq 1/9$. If for infinitely many input lengths $N = 2^n$, $\mathsf{MCSP}[(s, 0), (t, \varepsilon)] \notin \mathsf{Circuit}[N \cdot \mathsf{poly}(t(n)/\varepsilon)]$, then for infinitely many inputs n, $\mathsf{Circuit}[s(n)]$ cannot be learnt up to error $\varepsilon/2$ with confidence $1 - \delta$ by $t(n)$-size circuits using non-adaptive membership queries over the uniform distribution.*

We also show a related result which gives lower bounds for learnability of a circuit class $\mathcal{C}$ using $\mathcal{C}$-circuits by starting with a lower bound against worst-case MCSP instead of the average-case.

▶ **Lemma 35** (Hardness Magnification for Learnability from Lower Bounds for Gap MCSP). *Let $c \geq 1$ be an arbitrary constant. If there is $\varepsilon < 1/2$, such that infinitely many input lengths $N = 2^n$, $\mathsf{MCSP}[n^c, 2^n/n^c] \notin \mathsf{Circuit}[N^{1+\varepsilon}]$, then for every $\gamma \in (0, 1)$, for infinitely many inputs n, $\mathsf{Circuit}[n^c]$ cannot be learnt up to error $1/O(n^{2c})$ with confidence $1 - 1/n$ by $\mathsf{Circuit}[2^{O(n^\gamma)}]$-circuits using non-adaptive membership queries over the uniform distribution.*

Proof of Theorem 1. The following implications establish the desired equivalences.

$(a) \Longrightarrow (c)$: For the parameters c, γ, ε given by (a), we apply Lemma 34 for $s(n) = n^c$ and $t(n) = 2^{n^\gamma}$, to see that for some $\gamma' > 0$, $\mathsf{Circuit}[n^c]$ cannot be learned by circuits of size $2^{O(n^{\gamma'})}$ via non-adaptive queries up to an error $O(1/n^c)$.

$(c) \Longrightarrow (d)$: We show the contrapositive of this implication. Suppose that for every $d \geq 1$, there exists a $\mathsf{Circuit}[\mathsf{poly}(n)]$-natural property that is useful against $\mathsf{Circuit}[n^d]$ for all large enough n. By Theorem 5, for every $c \geq 1$, we can learn $\mathsf{Circuit}[n^c]$ by a sequence of oracle $\mathsf{Circuit}[2^{O(n^{1/2})}]$-circuits up to an error of n^{-c}, by choosing $d = 2ac$ for the constant a from Theorem 5.

$(d) \Longrightarrow (a)$, $(d) \Longrightarrow (b)$: Trivial, using the fact that random functions are hard.

$(c) \Longrightarrow (e)$: Follows from the contrapositive of Theorem 10.

$(e) \implies (c)$: Follows from the non-uniform version of Proposition 29 in [41], using essentially the same proof.

$(b) \implies (c)$: For the parameter c given by (b), we apply Lemma 35 to see that $\mathsf{Circuit}[n^c]$ cannot be learned by circuits of size $2^{O(n^\gamma)}$ via non-adaptive queries up to an error $O(1/n^c)$, for any $\gamma \in (0,1)$. ◀

We now complete the proof of Theorem 1 by proving Lemmas 34 and 35.

Proof of Lemma 34. For the promise problem $\mathsf{MCSP}[(s,0),(t,\varepsilon)]$ over N inputs, define

$$\Pi_{yes} = \{y \in \{0,1\}^N \mid \exists \text{ circuit of size} \leq s(n) \text{ that computes } f_y\}$$
$$\Pi_{no} = \{y \in \{0,1\}^N \mid \text{no circuit of size} \leq t(n) \ (1-\varepsilon)\text{-approximates } f_y\}$$

We prove the contrapositive of the statement, by showing a reduction from $\mathsf{MCSP}[(s,0),(t,\varepsilon)]$ to a learning algorithm for $\mathsf{Circuit}[s(n)]$ using non-adaptive member-ship queries over the uniform distribution. For a fixed $\varepsilon < 1/2$ and $0 \leq \delta \leq 1/9$, let $\{D_n\}_{n\geq 1} \in \mathsf{Circuit}[t(n)]$ be the corresponding sequence of oracle circuits which learns $\mathsf{Circuit}[s(n)]$ up to error $\varepsilon/2$, where D_n makes non-adaptive queries to some function $f \in \mathsf{Circuit}[s(n)]$ over n inputs.

Let $q = q(n) = \frac{200}{\varepsilon^2}$. Define $F_N : \{0,1\}^N \times \{0,1\}^{nq(n)} \times \{0,1\}^{t(n)} \to \{0,1\}$ as the sequence of randomized circuits such that :

$$z \in \Pi_{yes} \implies \Pr_{y_1, w} \{F_N(z, y_1, w) = 1\} > 2/3$$
$$z \in \Pi_{no} \implies \Pr_{y_1, w} \{F_N(z, y_1, w) = 1\} < 1/3$$

The reduction F_N does the following. Let $Y = (x_1, \ldots, x_{t(n)})$ be the set of queries made by D_n. F_N runs the learner D_n with input w as its source of internal randomness and answers its oracle queries to f_z by using the other input $z \in \{0,1\}^N$. If the output string of the learner cannot be interpreted as a $t(n)$-sized circuit, then F_N outputs 0. Otherwise, let h be the $t(n)$-sized circuit on n inputs, which can interpret the hypothesis output by the learner as a $t(n)$-sized circuit. F_N then interprets the random input y_1 as a sequence of q random examples $v_1, \ldots, v_q \in \{0,1\}^n$ and computes h on each of these. It then forms a string $u \in \{0,1\}^q$, where for every $i \in [q], u_i = 1$ if and only if $h(v_i) = f_z(v_i)$. Finally, it uses a threshold gate on T on $q(n)$ inputs to check if the Hamming weight of u is at least $((1 - 3\varepsilon/4)q)$.

We now show the correctness of the reduction. If $z \in \Pi_{yes}$, then f_z is computed by some circuit of size at most $s(n)$. Thus, for every random choice of y_1 and w, D_n can learn the function f_z and with probability at least $(1 - \delta)$, output a hypothesis h which has an error of at most $\varepsilon/2$ with respect to f_z. Now, for the q samples given by y_1, by an application of Hoeffding's inequality (Lemma 13), the probability that the Hamming weight of $u \in \{0,1\}^q$ is lesser than $(1 - 0.6\varepsilon)\, q$ is at most $2\exp(-2q\varepsilon^2/100)$ which is at most $1/4$ for our choice of q. When $\delta \leq 1/9$, we see that $T(u) = 1$ with probability at least $(1-\delta)3/4 \geq 2/3$.

On the other hand, if $z \in \Pi_{no}$, then no circuit of size at most $t(n)$ can even $(1-\varepsilon)$-approximate f_z. Thus, for any random choice of y_1 and w, any hypothesis h which D_n outputs is a circuit of size at most $t(n)$ and thus is at least ε-far from f_z. By a similar application of Hoeffding's inequality, we see that the probability that the Hamming weight of $u \in \{0,1\}^q$ is greater than $(1 - 0.9\varepsilon)\, q$ is at most $2\exp(-2q\varepsilon^2/100) \leq 1/4$. Therefore, $T(u) = 0$ with probability $2/3$.

For the next step, we need to derandomize the circuits F_N. Define E_N as

$$E_N : \{0,1\}^N \times \left(\{0,1\}^{n \cdot q + t(n)}\right)^R \to \{0,1\}$$
$$E_N(z, y^{(1)}, \ldots, y^{(R)}) = \mathsf{MAJ}_R(F_N(z, y^{(1)}), \ldots, F_N(z, y^{(R)}))$$

where $R = CN$ and each $y^{(j)} \in \{0,1\}^{n \cdot q + t(n)}$, for each $j \in [R]$.

When, $z \in \Pi_{yes}$, then using Hoeffding's inequality, we see that with probability at most 2^{-2N} (for suitably chosen C), the string $(F_N(z, y^{(1)}), \ldots, F_N(z, y^{(R)}))$ has Hamming weight $\leq 3R/5$. Similarly, when $z \in \Pi_{no}$, with probability at most 2^{-2N}, the string $(F_N(z, y^{(1)}), \ldots, F_N(z, y^{(R)}))$ has Hamming weight $\geq 2R/5$. Thus, the majority gate differentiates between the two cases except with probability at most 2^{-2N}. We use Adleman's trick [6] to fix a string $\alpha \in \{0,1\}^{R \cdot (n \cdot q + t(n))}$ which correctly derandomizes F_N on all inputs in Π_{yes} and Π_{no} and call the resulting circuit as E_N^* which computes the function $E_N^* : \{0,1\}^N \to \{0,1\}$.

We next compute the size of E_N^*. Each $F_N(z, y^{(i)})$ is fixed to $F_N(z, \alpha^{(i)})$, where $\alpha^{(i)} \in \{0,1\}^{(n \cdot q + t(n))}$ is the ith section of the hardwired random string α. Observe that for the set of oracle queries Y made by D_n, it is enough to use appropriate literals from the input z whenever we need to access the truth table of f_z. Indeed, whenever D_n uses a random example, the randomness comes from $\alpha^{(i)}$ which is fixed non-uniformly and whenever it makes a membership query, the set of queries Y is fixed for D_n because of its non-adaptivity. Recall that the size of the circuit D_n is $t(n)$ and the hypothesis h output by the learner can be interpreted as a circuit and efficiently computed by another circuit of size $\mathsf{poly}(t(n))$. Thus, the circuit size to compute $F_N(z, \alpha)$ is at most $\mathsf{poly}(t(n) \cdot q)$ and the total circuit size to construct E_N^* is $O(N \cdot \mathsf{poly}(t(n)/\varepsilon))$. ◄

Proof sketch of Lemma 35. We show a two-sided error randomized reduction from $\mathsf{MCSP}[n^c, 2^n/n^c]$ to $\{D_n\}_{n \geq 1}$. Let $q = q(n) = O\left(n^{3c}\right)$. The reduction is almost the same as that of Lemma 34. Here we use a threshold gate on $q(n)$ inputs which answers 1 whenever the Hamming weight of its input is greater than $(1 - 1/n^{1.5c})q(n)$.

When the input to $\mathsf{MCSP}[n^c, 2^n/n^c]$ is a yes instance, with probability at least $(1 - 1/n)$, D_n outputs a hypothesis $h_n \in \mathsf{Circuit}[2^{n^\gamma}]$ which has error at most $1/O(n^{2c})$. Now for the $q(n)$ samples drawn uniformly at random, the probability that h agrees with the input instance on at least a $(1 - 1/n^{1.5c})q(n)$ samples is at least $(1 - 1/n)2/3$.

When the input to $\mathsf{MCSP}[n^c, 2^n/n^c]$ is a no instance, any hypothesis h which D_n outputs must have error greater than $1/O(n^{c+2})$. Indeed, if the error is less than $O(1/n^{c+2})$, then by hardwiring all the error inputs by using circuits of size at most $O\left(\frac{2^n}{n^{c+2}} \cdot n\right)$ we get a circuit of size at most $2^n/n^c$, which is a contradiction to the promise of the no instance. By Hoeffding's inequality, the probability that h agrees with the input instance on at most a $(1 - 1/n^{1.5c})q(n)$ samples is at least $2/3$.

The derandomization is the same as that of Lemma 34, obtained by repeating the above reduction $R = O(N)$ times and computing the majority over the R outputs of the reduction. The circuit size to compute $\mathsf{MCSP}[n^c, 2^n/n^c]$ is thus $O(N \cdot 2^{O(n^\gamma)} n^{3c}) = O(N^{1+\varepsilon})$, for $\varepsilon = o(1)$. ◄

4.2 Towards a More Robust Theory

The question of non-naturalizability of hardness magnification for $\mathsf{MCSP}[n^c/2n, n^c]$ is connected to the question of basing hardness of learning on the assumption $\mathsf{NP} \not\subseteq \mathsf{Circuit}[2^{O(n^\gamma)}]$.

▶ **Proposition 36.** *Assume that for every $\gamma \in (0,1)$ there is $d \geq 2$ such that $\mathsf{NP} \nsubseteq$ $\mathsf{Circuit}[2^{O(n^\gamma)}]$ implies hardness of learning $\mathsf{Circuit}[n^d]$ by 2^{n^γ}-size circuits with error $1/n^d$. Then, there is a constant e such that for every $\gamma \in (0,1)$ and $c \geq 1$, $\mathsf{MCSP}[n^c/2n, n^c] \notin$ $\mathsf{Circuit}[N^{1+e\gamma c}]$ implies that there is no $\mathsf{P/poly}$-natural property against $\mathsf{P/poly}$.*

Proof. By Theorem 5, $\mathsf{P/poly}$-natural property against $\mathsf{P/poly}$ implies that for every d there is $\gamma < 1/d$ and 2^{n^γ}-size circuits learning $\mathsf{Circuit}[n^d]$ with error $1/n^d$. By our assumption, this implies $\mathsf{NP} \subseteq \mathsf{Circuit}[2^{O(n^\gamma)}]$. We can now use $\mathsf{NP} \subseteq \mathsf{Circuit}[2^{O(n^\gamma)}]$ as the assumption in the proof of Theorem 29 to conclude that there is a constant e independent of γ such that for $c \geq 1$, $\mathsf{MCSP}[n^c/2n, n^c] \in \mathsf{Circuit}[N^{1+e\gamma c}]$. ◀

A form of the opposite implication holds as well if we assume NP-completeness of MCSP. Moreover, instead of the non-naturalizability of hardness magnification, we need to assume a reduction from worst-case MCSP to approximate MCSP.

▶ **Definition 37.** *A p-time algorithm A k-reduces $\mathsf{MCSP}[s,t]$ to $\mathsf{MCSP}[(s,0),(t,\varepsilon)]$ if it maps instances of $\mathsf{MCSP}[s,t]$ to instances of $\mathsf{MCSP}[(s,0),(t,\varepsilon)]$ and*
   1. For $f \in \mathsf{Circuit}[s]$, $A(\mathsf{tt}(f))$ is the truth-table of a Boolean function in $\mathsf{Circuit}[s^k]$.
   2. For $f \notin \mathsf{Circuit}[t]$, $A(\mathsf{tt}(f))$ is not $(1-\varepsilon)$-approximable by circuits of size $t^{1/k}$.

▶ **Proposition 38.** *Assume there is a p-time algorithm k-reducing $\mathsf{MCSP}[s,t]$ to $\mathsf{MCSP}[(s,0),(t,\varepsilon)]$ and that for all $0 < \alpha < \beta < 1$, $\mathsf{MCSP}[2^{\alpha n}, 2^{\beta n}]$ is NP-complete. If for every sufficiently small $\alpha > 0$ there is $\beta < 1/k$ and a $2^{\beta n}$-time algorithm learning $\mathsf{Circuit}[2^{\alpha n}]$ with error ε, then $\mathsf{P} = \mathsf{NP}$.*

Proof. Let A bet the p-time k-reduction from the statement and $\alpha > 0$ be sufficiently small. Assume we can learn in $2^{\beta n}$-time $\mathsf{Circuit}[2^{k\alpha n}]$ with error ε and $k\alpha < \beta < 1/k$. This implies that $\mathsf{MCSP}[(2^{k\alpha n}, 0), (2^{\beta n}, \varepsilon)]$ can be solved in p-time. Since A reduces an NP-complete problem $\mathsf{MCSP}[2^{\alpha n}, 2^{k\beta n}]$ to $\mathsf{MCSP}[(2^{k\alpha n}, 0), (2^{\beta n}, \varepsilon)]$, this shows that $\mathsf{P} = \mathsf{NP}$. ◀

5 The Locality Barrier

5.1 Lower Bounds Above Magnification Threshold

5.1.1 The Razborov-Smolensky Polynomial Approximation Method

In this section, we observe that the lower bound techniques of Razborov and Smolensky [44, 49] can be "localized." The following proposition instantiates the locality barrier for HM Frontier A.

▶ **Proposition 39** (Locality Barrier for HM Frontier A). *The following results hold.*
   ▪ $(\mathrm{A1}^{\mathcal{O}})$ (Oracle Circuits from Magnification) : $\mathsf{MKtP}[n^c, 2n^c] \in \mathsf{AND}\text{-}\mathcal{O}\text{-}\mathsf{XOR}[N^{1.01}]$.
   More precisely, $\mathsf{MKtP}[n^c, 2n^c]$ is computed by circuits with $N^{1.01}$ gates and of the following form: the output gate is an AND gate of fan-in $O(N)$, at the middle layer are oracle gates of fan-in $\mathrm{poly}(n)$, and at the bottom layer are XOR gates.
   ▪ $(\mathrm{A3}^{\mathcal{O}})$ (Extension of Lower Bound Techniques) :   *For a constant d, assume that $O_1, \cdots, O_d \in \mathbb{N}$ satisfy $\prod_{i=1}^d O_i \leq \sqrt{N}/\omega(\log N)^d$. Then $\mathsf{Majority}$ cannot be computed by a depth-d polynomial-size oracle $(\mathsf{AC}^0[\oplus])^{\mathcal{O}}$ circuit whose oracle gates on the i-th level have fan-in at most O_i.*

The first item is immediate from the proof of Theorem 14 in Section 3.1. In what follows, we prove the second item of Proposition 39.

Recall that the proof techniques of Razborov and Smolensky [44, 49] consist of two parts: The first lemma shows that any low degree polynomial cannot approximate Majority. (A simple proof sketch can be found in, e.g., [33].)

▶ **Lemma 40.** *For any polynomial $p \in \mathbb{F}_2[x_1, \cdots, x_N]$ of degree $\leq \sqrt{N}/4$,*

$$\Pr_{x \sim \{0,1\}^N}[p(x) \neq \mathsf{Majority}(x)] \geq \frac{1}{4}.$$

The second lemma shows that $\mathsf{AC}^0[\oplus]$ circuits can be approximated by low degree polynomials. We show that this argument can be localized.

▶ **Lemma 41.** *Let C be a depth-d polynomial-size oracle $\mathsf{AC}^0[\oplus]$ circuit whose oracle gates on the i-th level have fan-in at most O_i. Then there exists a polynomial $p \in \mathbb{F}_2[x_1, \cdots, x_N]$ of degree $\leq O(\log N)^d \cdot \prod_{i=1}^{d} O_i$ such that $\Pr_{x \sim \{0,1\}^N}[p(x) \neq C(x)] < \frac{1}{4}$.*

Proof Sketch. We convert each layer of the circuit C into a low degree probabilistic polynomial p that approximates C.

Consider the i-th level of a circuit C. NOT, OR, AND, and XOR gates can be converted into a probabilistic polynomial of degree $O(\log N)$ and error $1/\mathsf{poly}(N)$ in the standard way [44]. In order to represent an oracle gate $\mathcal{O}$ as a low-degree polynomial, we simply take the multilinear extension of the oracle gate $\mathcal{O}$. Note that, at the i-th level, the fan-in of the oracle gate $\mathcal{O}$ is bounded by O_i; thus the oracle gate at the i-th level can be represented as a polynomial of degree $\leq O_i$. Thus, in either cases, any gate at i-th level can be represented as a probabilistic polynomial of degree $\max\{O(\log N), O_i\}$. Continuing this for $i = 1, \cdots, d$ and composing resulting polynomials, we obtain a probabilistic polynomial of degree $\prod_{i=1}^{d} \max\{O(\log N), O_i\}$ that approximates C. This implies via standard techniques the existence of a (deterministic) polynomial of the same degree that correctly computes the circuit on most inputs. ◀

These two lemmas immediately imply the Majority lower bound for $(\mathsf{AC}^0[\oplus])^{\mathcal{O}}$:

Proof of (A3$^{\mathcal{O}}$) of Proposition 39. Suppose that there exists a depth-d polynomial-size oracle $\mathsf{AC}^0[\oplus]$ circuit that computes Majority and satisfies the condition of Proposition 39. By Lemma 41, there exists a polynomial p of degree at most $O(\log N)^d \cdot \prod_{i=1}^{d} O_i \leq o(\sqrt{N})$ that approximates Majority. However, this contradicts Lemma 40. ◀

Finally, we mention that an incomparable bound can be obtained by using a lower bound for $\mathsf{AC}^0[\oplus]$ interactive compression games.

▶ **Proposition 42** ([40, Corollary 5.3]). *(A3$^{\mathcal{O}}$) Majority $\notin (AC^0[\oplus])^{\mathcal{O}}[\mathsf{poly}(n)]$ if the total number of input wires in the circuit feeding the $\mathcal{O}$-gates is $N/(\log N)^{\omega(1)}$.*

5.1.2 The Formula-XOR Lower Bound of [52]

This section captures an instantiation of the locality barrier for HM Frontier B. Throughout this section we use the $\{-1, 1\}$ realization of the Boolean domain (that is, -1 represents True and 1 represents False). Let Formula-XOR on variables $x_1, \ldots, x_n$ be the class of formulas where the input leaves are labeled by parity functions of arbitrary arity over $x_1, \ldots, x_n$.

▶ **Proposition 43** (Locality Barrier for HM Frontier B). *The following results hold.*

▬ (B1$^{\mathcal{O}}$) (Oracle Circuits from Magnification) : *For any $\varepsilon > 0$, $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}] \in$ Formula-$\mathcal{O}$-XOR$[N^{1.01}]$, where every oracle $\mathcal{O}$ has fan-in at most N^{ε} and appears in the layer right above the XOR leaves.*

▬ (B3$^{\mathcal{O}}$) (Extension of Lower Bound Techniques) : *For any $\delta > 0$, InnerProduct over N input bits cannot be computed by $N^{2-3\delta}$-size Formula-$\mathcal{O}$-XOR circuits with at most $N^{2-3\delta}$ oracle gates of fan-in N^{δ} in the layer right above the XOR leaves, for any oracle $\mathcal{O}$.*

To prove item 2 of Proposition 43, we adapt Tal's [52] lower bound for bipartite formulas[15], for which we need the following results.

▶ **Lemma 44** ([47, 52]). *Let F be a De Morgan formula of size s which computes $f :$ $\{-1,1\}^n \to \{1,1\}$. Then, there exists a multilinear polynomial p over $\mathbb{R}$ of degree $O(\sqrt{s})$, such that for every $x \in \{-1,1\}^n$, $p(x) \in [F(x) - 1/3, F(x) + 1/3]$.*

For any function $f : \{-1,1\}^n \to \{-1,1\}$, f is ε-correlated with a parity $p_S(x) = \prod_{i \in S} x_i$, if $|\mathbb{E}_{x \in \{-1,1\}^n}[f(x) \cdot p_S(x)]| \geq \varepsilon$. We have

▶ **Lemma 45.** *For any $\delta > 0$, let $F(x_1, \ldots, x_n)$ be a Formula-$\mathcal{O}$-XOR formula of size s, where every oracle $\mathcal{O}$ has fan-in at most n^{δ} and appears in the layer right above the XOR leaves. Then the following hold true:*

1. *There exists a multi-linear polynomial $p(x_1, \ldots, x_n)$ over $\mathbb{R}$ with at most $s^{O(\sqrt{s})} \cdot 2^{n^{\delta} \cdot O(\sqrt{s})}$ monomials such that for every $x \in \{-1,1\}^n$, $sign(p(x)) = F(x)$.*

2. *There exists a parity function $f_T(x_1, \ldots, x_n)$ which is at least $\left(\frac{1}{s^{O(\sqrt{s})} \cdot 2^{n^{\delta} O(\sqrt{s})}} \right)$-correlated with F.*

Proof. We assume that the oracle function is a Boolean function on n^{δ} inputs. Let $t \leq s/n^{\delta}$ be the number of oracle gates in F. Let $p_1, \ldots, p_s$ be the leaves of F, where each p_i is an XOR gate over $x_1, \ldots, x_n$ and every oracle gate $g_1, \ldots, g_t$ is such that $g_i(x) = \mathcal{O}(p_{i1}(x), \ldots, p_{i\ell}(x))$, where $\ell = n^{\delta}$ and $p_{ij} \in \{p_1, \ldots, p_t\}$ for every $i \in [t], j \in [\ell]$.

Let F' be a De morgan formula obtained by replacing oracle gates in F with new variables z_i (for notational simplicity we assume that every leaf is an input to some oracle gate), for $i \in [t]$. We now use Lemma 44 on F' to get a degree $d = O(\sqrt{t})$ polynomial $q(z)$ such that for every $z \in \{-1,1\}^t$, $sign(q(z)) = F'(z)$. Expanding $q(z)$ as a multilinear polynomial :

$$q(z) = \sum_{S \subseteq [t], |S| \leq d} \hat{q}(S) \prod_{i \in S} z_i$$

To prove the first item, we replace each z_i by the original leaf and we get that for every $x \in \{-1,1\}^n$,

---

[15] A bipartite formula on variables $x_1, \ldots, x_n, y_1, \ldots, y_n$ is a formula such that each leaf computes an arbitrary function in either $(x_1, \ldots, x_n)$ or $(y_1, \ldots, y_n)$. Formula-XOR circuits are a subset of bipartite formulas as one can always write $\oplus(x_1, \ldots, x_{2n})$ as the parity of $\oplus(x_1, \ldots, x_n)$ and $\oplus(x_{n+1}, \ldots, x_{2n})$.

$$
\begin{aligned}
F(x) &= \text{sign}\left(\sum_{S \subseteq [t], |S| \leq d} \hat{q}(S) \prod_{i \in S} g_i(x) \right) \\
&= \text{sign}\left(\sum_{S \subseteq [t], |S| \leq d} \hat{q}(S) \prod_{i \in S} \left(\sum_{U \subseteq [\ell]} \hat{\mathcal{O}}(U) \prod_{j \in U} p_{ij}(x) \right) \right) \\
&= \text{sign}\left(\sum_{\substack{S \subseteq [t] \\ S = \{i_1, \ldots, i_{|S|}\} \\ |S| \leq d}} \sum_{U_{i_1}, \ldots, U_{i_{|S|}} \subseteq [\ell]} \hat{q}(S) \cdot \left(\prod_{1 \leq k \leq |S|} \hat{\mathcal{O}}(U_{i_k}) \prod_{j \in U_{i_k}} p_{i_k j}(x) \right) \right)
\end{aligned}
$$

where the second equality uses the fact that any Boolean function on ℓ inputs can be represented by a multilinear polynomial of degree at most ℓ where each coefficient is between $[-1, 1]$. Clearly the number of monomials is at most $s^{O(\sqrt{s})} \cdot 2^{n^\delta \cdot O(\sqrt{s})}$.

To prove the second item, firstly observe that for every $z \in \{-1, 1\}^t$, $q(z) \cdot F'(z) \in [2/3, 4/3]$, because $|q(z) - F'(z)| \leq 1/3$ for every z. This also means that for the polynomial $r(x) = q(g_1(x), \ldots, g_t(x))$, $\mathbb{E}_{x \in \{-1,1\}^n}[r(x) \cdot F(x)] \geq 2/3$.

Given that $\hat{q}(S) = \mathbb{E}_{z \in \{-1,1\}^s}\left[q(z) \prod_{i \in S} z_i \right]$, we see that $|\hat{q}(S)| \leq 4/3$. We have

$$
\begin{aligned}
2/3 &\leq \mathbb{E}_{x \in \{-1,1\}^n}[F(x) \cdot r(x)] \\
&= \mathbb{E}_{x \in \{-1,1\}^n}\left[F(x) \cdot \sum_{S \subseteq [t], |S| \leq d} \hat{q}(S) \prod_{i \in S} g_i(x) \right] \\
&\leq \sum_{\substack{S \subseteq [t] \\ S = \{i_1, \ldots, i_{|S|}\} \\ |S| \leq d}} \sum_{U_{i_1}, \ldots, U_{i_{|S|}} \subseteq [\ell]} \hat{q}(S) \prod_{1 \leq k \leq |S|} \hat{\mathcal{O}}(U_{i_k}) \mathbb{E}_{x \in \{-1,1\}^n}\left[F(x) \prod_{1 \leq k \leq |S|} \prod_{j \in U_{i_k}} p_{i_k j}(x) \right]
\end{aligned}
$$

Since, $|\hat{q}(S)| \leq 4/3$ for every $S \subseteq [t]$ and $|\hat{\mathcal{O}}(U)| \leq 1$ for every $U \subseteq [\ell]$, we see that there exists a set S of size at most d and sets $U_{i_1}, \ldots, U_{i_{|S|}}$ such that $\left| \mathbb{E}_{x \in \{-1,1\}^n}\left[F(x) \cdot \prod_{1 \leq k \leq |S|} \prod_{j \in U_{i_k}} p_{i_k j}(x) \right] \right| \geq \frac{1}{t^{O(\sqrt{t})} \cdot 2^{n^\delta O(\sqrt{t})}} \geq \frac{1}{s^{O(\sqrt{s})} \cdot 2^{n^\delta O(\sqrt{s})}}$. Taking p_T be the parity of the parities given by $p_T = \prod_{1 \leq k \leq |S|} \prod_{j \in U_{i_k}} p_{i_k j}(x)$, we see that p_T is $\frac{1}{s^{O(\sqrt{s})} \cdot 2^{n^\delta O(\sqrt{s})}}$-correlated with F. ◄

Define the Inner Product modulo 2 function, $\mathsf{InnerProduct}_n : \{-1, 1\}^n \times \{-1, 1\}^n \to \{-1, 1\}$ as $IP_n(x, y) = (-1)^{\sum_{i=1}^n (1-x_i)(1-y_i)/4}$.

Proof Sketch of Proposition 43. The first item follows from an inspection of the proof of Theorem 25 in Section 3.2. Theorem 24 gives the same oracle circuit construction (with different oracles) under the assumption $\mathsf{QP} \subseteq \mathsf{P/poly}$.

The second item follows from Lemma 45. We observe that three different techniques used to show Formula-XOR lower bounds localize. Firstly, Tal's lower bound based on sign rank shows that the sign rank of any Formula-XOR circuit F is at most the number of monomials in the polynomial p given by the first item of lemma 45. Since this is at most $s^{O(\sqrt{s})} \cdot 2^{n^\delta \cdot O(\sqrt{s})}$ and InnerProduct has a sign rank which is at least $2^{n/2}$ [18], the lower

bound follows. Secondly, Tal's lower bound based on the discrepancy of a function also localizes, as he shows that the discrepancy of F is at least a constant times the correlation of F with the parity f_T given by item 2 of Lemma 45, which is at least $\Omega\left(\frac{1}{s^{O(\sqrt{s})} \cdot 2^{n^{\delta}O(\sqrt{s})}}\right)$, whereas the discrepancy of the inner product is at most $1/2^{n/2}$ (cf. [29, Lemma 14.5]), thus proving the given lower bound for inner product. Finally, we also observe that the lower bound technique of showing high correlation of F with some parity f_T and the fact that inner product has exactly $2^{-n/2}$-correlation with any parity also localizes to give the same lower bound. ◀

5.1.3   Almost-Formula Lower Bounds

This section captures an instantiation of the locality barrier for HM Frontier C. We recall the following definition. Consider an s-almost formula. Each gate G of F with fanout larger than 1 is computed by a formula with inputs being either the original inputs of F or gates of F with fanout larger than 1. We call any maximal formula of this form a *principal* formula of G.

▶ **Theorem 46** (Locality Barrier for HM Frontier C). *The following results hold.*
- (C1$^{\mathcal{O}}$) (Oracle Circuits from Magnification) : *$MCSP[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ is computable by $2^{O(n^{1/2})}$-almost formulas of size $2^{n+O(n^{1/2})}$ with oracles of fanin $2^{O(n^{1/2})}$ at the bottom layer of principal formulas computing gates with fanout larger than 1.*
- (C3$^{\mathcal{O}}$) (Extension of Lower Bound Techniques) : *For every $\varepsilon < 1$, PARITY is not in n^{ε}-almost-Formula$[n^{2-9\varepsilon}]$ even if the almost-formulas are allowed to use arbitrary oracles of fanin $< n^{\varepsilon}$ at the bottom layer of principal formulas computing gates with fanout larger than 1.*

Proof. The first item follows by inspecting the proof of Theorem 29. It is not hard to see that $MCSP[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ is computable by $2^{O(n^{1/2})}$-almost formulas F_N of size $2^{n+O(n^{1/2})}$ with local oracles of fanin $2^{O(n^{1/2})}$. Moreover, the only gates of fanout larger than 1 are the gates computing anticheckers $y_1, \ldots, y_{2^{O(n^{1/2})}}$ with bits $f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$. We want to show that the local oracles are at the bottom of principal formulas generating gates with fanout larger than 1. In order to achieve this we need to modify formulas F_N a bit.

First, note that F_N contains an oracle which is applied on top of anticheckers $y_1, \ldots, y_{2^{O(n^{1/2})}}$ with bits $f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$. In order to ensure that this oracle is at the bottom of a principal formula computing a gate with fanout bigger than 1 we simply add dummy negation gates to the output gate and the gates computing anticheckers $y_1, \ldots, y_{2^{O(n^{1/2})}}$ with bits $f(y_1), \ldots, f(y_{2^{O(n^{1/2})}})$, if necessary.

Second, note that each $y_{i+1}, f(y_{i+1})$ is generated as follows: 1. if $R_f(y_1, \ldots, y_i) \geq 2n^2$ then a subformula F' generates anticheckers $y_{i+1}, f(y_{i+1})$, and 2. if $R_f(y_1, \ldots, y_i) < 2n^2$ then a subformula F'' generates anticheckers $y_{i+1}, f(y_{i+1})$. In both cases we replace predicates $R_f(y_1, \ldots, y_i) < 2n^2$ by oracles. In case 1, subformulas of F' with oracles at the bottom compute predicates $F^{r,h}$ from the proof of Lemma 27. This process generates a set of $2^{O(n^{1/2})}$ potential anticheckers. F' chooses the right antichecker by applying another oracle. In order to ensure that this top oracle is at the bottom of a principal formula, we add dummy negation gates to the gates generating the potential anticheckers. This increases the number of gates with fanout larger than 1 only by $2^{O(n^{1/2})}$. In case 2, $y_{i+1}, f(y_{i+1})$ is generated by oracles outputting circuits which have not been killed yet and evaluating them on all possible inputs. Here we ensure that the oracles are at the bottom by asking them to perform both tasks: choose the next alive circuit and evaluate it on a given input. The oracle selecting the right antichecker from the set of potential anticheckers is treated in the same way as in case 1. All in all, we obtain the desired oracle almost formulas.

The second item is proved analogously to Theorem 30. For the sake of contradiction assume PARITY has n^ε-almost formulas of size $n^{2-9\varepsilon}$ with local oracles at the bottom of principal formulas. Since there are only n^ε gates of fanout > 1, we can replace these gates by constants and obtain formulas F_n of size $n^{2-8\varepsilon}$ with local oracles at the bottom computing PARITY with probability $\geq 1/2 + 1/2^{n^\varepsilon}$. Let $L'(f)$ be the size (i.e. the number of leafs) of the smallest formula with local oracles at the bottom computing f. Since oracles have fanin $< n^\varepsilon$ and are located at the bottom, each function $f : \{-1, 1\}^n \mapsto \{-1, 1\}$ can be approximated by a polynomial of degree $O(t\sqrt{L'(f)}\frac{\log n}{\log\log n}n^\varepsilon)$ up to point-wise error of 2^{-t}. This implies that each formula of size $o((n/t)^2(\log\log n/\log n)^2(1/n^\varepsilon)^2)$ with local oracles at the bottom computes PARITY with probability at most $1/2 + 1/2^{t+O(1)}$ (for large enough t). Taking $t = n^{2\varepsilon}$ we get a contradiction.   ◀

5.1.4   GapAND-Formula Lower Bounds

This section captures an instantiation of the locality barrier for HM Frontier D.

▶ **Theorem 47** (Locality Barrier for HM Frontier D). *The following results hold.*
1. (D1$^\mathcal{O}$) (Oracle Circuits from Magnification) : $MCSP[2^{\sqrt{n}}] \in \mathsf{GapAND}_{O(N)}\text{-}O_{N^{o(1)}}$-Formula$[N^2]$.
2. (D3$^\mathcal{O}$) (Extension of Lower Bound Techniques) :   *For* $0 < \beta < \varepsilon < 1$, $\mathsf{Andreev}_N \notin \mathsf{GapAND}_{O(N)}\text{-}O_{N^\beta}$-Formula$[N^{3-\varepsilon}]$.
3. (D3$^\mathcal{O}$) :   *Furthermore,* $MCSP[2^n/n^4] \notin \mathsf{GapAND}_{O(N)}\text{-}O_{N^\beta}$-Formula$[N^{3-\varepsilon}]$, *for* $0 < \beta < \varepsilon < 1$.

Item 1 of the theorem above follows directly from Theorem 25.

Next we show that the classical $N^{3-o(1)}$ formula size lower bound for the Andreev's function [21, 51] localizes, even in the presence of a GapAND gate of bounded fan-in at the top of the formula.

Proof of Item 2. Let $m = N/2$, recall that $\mathsf{Andreev}_N$ is defined on a $2m$-bit string $z = x \circ y$, where $x, y \in \{0, 1\}^m$. For simplicity, we assume m is a power of 2 in the following.

$\mathsf{Andreev}_N(x, y)$ first partitions x into $\log m$ blocks $x_1, x_2, \ldots, x_{\log m}$, each of length $m/\log m$. After that, it computes $i \in \{0, 1\}^{\log m}$ as $i = \mathsf{PARITY}(x_1) \circ \mathsf{PARITY}(x_2) \circ \ldots \mathsf{PARITY}(x_{\log m})$. It then treats i as an integer from $[m]$, and outputs y_i.

Now, suppose there is a $\mathsf{GapAND}_{O(N)}\text{-}O_{N^\beta}$-Formula$[N^{3-\varepsilon}]$ formula for $\mathsf{Andreev}_N$. Suppose we fix the y variables to a string $w \in \{0, 1\}^m$, and apply a random restriction keeping exactly one variable from each block alive to x variables, then w.p. 0.9, we obtain a $\mathsf{GapAND}_{O(N)}\text{-}O_{N^\beta}$-Formula$[N^{1-\varepsilon} \cdot \mathrm{polylog}(N)]$ formula computing $f_w : \{0, 1\}^{\log m} \to \{0, 1\}$ [51].

That is, for all $w \in \{0, 1\}^m$, there exists an O_{N^β}-Formula$[N^{1-\varepsilon} \cdot \mathrm{polylog}(N)]$ formula 0.8-approximating f_w. Note that there are at most $2^{N^{1-\varepsilon+\beta}\cdot\mathrm{polylog}(N)}$ such O_{N^β}-Formula$[N^{1-\varepsilon} \cdot \mathrm{polylog}(N)]$ formulas, and there are 2^N possible w's (Note that O is a fixed oracle which does not depend on w). Since each O_{N^β}-Formula$[N^{1-\varepsilon} \cdot \mathrm{polylog}(N)]$ formula can only 0.8-approximate $2^{\alpha\cdot N}$ many functions from $\{0, 1\}^{\log m} \to \{0, 1\}$ for a constant $\alpha < 1$, there must exist a w such that f_w cannot be 0.8-approximated by such formulas, contradiction.   ◀

Next, we observe that the $N^{3-o(1)}$ formula lower bound for MCSP [16] also localizes.

Proof of Item 3. We first observe that the PRG construction of [16] also works for oracle formulas. (We omit the details of this proof.)

▷ **Claim 48** ([16]).   For $0 < \beta < \varepsilon < 1$, there is $M = N^{1-\Omega_{\beta,\varepsilon}(1)}$ and a PRG $G : \{0, 1\}^M \to \{0, 1\}^N$ such that the following hold.

1. For each fixed $z \in \{0,1\}^M$, $G(z)$, when interpreted as a function from $\{0,1\}^{\log N} \to \{0,1\}$, can be computed by a circuit of size $N^{1-\Omega(1)}$.
2. For all O_{N^β}-Formula$[N^{3-\varepsilon}]$ formulas C, we have

$$\left| \Pr_{z \in \{0,1\}^N}[C(z) = 1] - \Pr_{z \in \{0,1\}^M}[C(G(z)) = 1] \right| \leq 0.01.$$

Now, suppose MCSP$[2^n/n^4]$ on $N = 2^n$ bits can be computed by a GapAND$_{O(N)}$-O_{N^β}-Formula$[N^{3-\varepsilon}]$ formula C. Let $C_1, C_2, \ldots, C_{b \cdot N}$ be the O_{N^β}-Formula$[N^{3-\varepsilon}]$ sub-formulas of C under the top GapAND gate, where b is a constant.

We know that

$$\Pr_{z \in \{0,1\}^N}[\mathsf{MCSP}[2^n/n^4](z) = 1] = o(1).$$

Since C computes MCSP$[2^n/n^4]$, and $C(x) = 0$ implies $C_i(x) = 0$ for at least a 0.9 fraction of $i \in [b \cdot N]$. We have that

$$\Pr_{i \in [b \cdot N],\, z \in \{0,1\}^N}[C_i(z) = 1] \leq 0.2.$$

On the other side, by the definition of MCSP$[2^n/n^4]$, and the Item (1) of Claim 48, it follows that

$$\Pr_{z \in \{0,1\}^M}[\mathsf{MCSP}[2^n/n^4](G(z)) = 1] = 1.$$

Again, since C computes MCSP$[2^n/n^4]$, and $C(x) = 1$ implies $C_i(x) = 1$ for all $i \in [b \cdot N]$. We have that

$$\Pr_{i \in [b \cdot N],\, z \in \{0,1\}^M}[C_i(G(z)) = 1] = 1.$$

Therefore, there must exist an i such that

$$\left| \Pr_{z \in \{0,1\}^N}[C_i(z) = 1] - \Pr_{z \in \{0,1\}^M}[C_i(G(z)) = 1] \right| \geq 0.5,$$

which is a contradiction to Item (2) of Claim 48. ◄

Finally, we show that there is a language in E which cannot be computed by GapAND$_{O(N)}$-Formula$[N^{3-\varepsilon}]$ formulas, but it *can* be computed by an $O_{N^{o(1)}}$-Formula$[N^2]$ formula. Therefore, this lower bound does not localize in the sense of Theorem 47.

▶ **Theorem 49.** *There is a language $L \in$ E, such that $L \notin$ GapAND$_{O(N)}$-Formula$[N^{3-\varepsilon}]$ for all constants $\varepsilon > 0$, but $L \in O_{N^{o(1)}}$-Formula$[N^2]$.*

Proof. The function L is very similar to the Andreev's function. On an input x of length N, let $m = \log N$ (we assume N is a power of 2 for simplicity). To avoid the second input to Andreev$_N$, we want to find a function $f_{\mathsf{hard}} : \{0,1\}^m \to \{0,1\}$ which cannot be 0.8-computed by $N^{1-\varepsilon/2}$ formulas in $2^{O(N)}$ time (such a function exists by a simple counting argument). To find f_{hard}, we simply enumerate all possible functions $f : \{0,1\}^m \to \{0,1\}$, and check whether it can be 0.8-approximated by an $N^{1-\varepsilon/2}$-size formula.

There are $2^{2^m} = 2^N$ possible functions on m bits, and $(N^{1-\varepsilon/2})^{O(N^{1-\varepsilon/2})} = 2^{N^{1-\varepsilon/2} \cdot \mathrm{polylog}(N)}$ many formulas of $N^{1-\varepsilon/2}$ size. Hence, a straightforward implementation of the algorithm runs in $2^{O(N)}$ time.

Next, L partitions x into m blocks $x_1, x_2, \ldots, x_m$, each of length N/m. After that, it computes $i \in \{0,1\}^m$ as $i = \mathsf{PARITY}(x_1) \circ \mathsf{PARITY}(x_2) \circ \ldots \mathsf{PARITY}(x_m)$. It then outputs $f_{\mathsf{hard}}(i)$.

Now, suppose there is a $\mathsf{GapAND}_{O(N)}$-$\mathsf{Formula}[N^{3-\varepsilon}]$ for L. We apply a random restriction keeping exactly one variable from each block alive, then w.p. 0.9, we obtain a $\mathsf{GapAND}_{O(N)}$-$\mathsf{Formula}[N^{1-\varepsilon} \cdot \mathrm{polylog}(N)]$ formula for f_{hard} [51], which implies that there is an $N^{1-\varepsilon} \cdot \mathrm{polylog}(N)$-size formula 0.8-approximating f_{hard}, contradiction.

Finally, it is easy to verify that $L \in \mathsf{E}$ and $L \in O_{N^{o(1)}}$-$\mathsf{Formula}[N^2]$.   ◂

5.1.5 AC^0 Lower Bounds via Random Restrictions

This section states and proves a result capturing an instantiation of the locality barrier for HM Frontier E.

▶ **Proposition 50** (Locality Barrier for HM Frontier E). *The following results hold.*
- (E1$^{\mathcal{O}}$) (Oracle Circuits from Magnification) :   *For each* $k = (\log n)^C$ *and every large enough depth* d, $(n-k)$-$\mathsf{Clique} \in (\mathsf{AC}^0_d)^{\mathcal{O}}[m^{1+\varepsilon_d}]$, *where* $\varepsilon_d \to 0$ *as* $d \to \infty$, *and the corresponding circuit employs a single oracle gate* $\mathcal{O}$ *of fan-in at most* $O((\log n)^{4C})$.
- (E3$^{\mathcal{O}}$) (Extension of Lower Bound Techniques) : $\mathsf{Parity} \notin (\mathsf{AC}^0)^{\mathcal{O}}[\mathrm{poly}(n)]$ *if the total number of input wires in the circuit feeding the* $\mathcal{O}$-*gates is* $n/(\log n)^{\omega(1)}$.

Proof. The first item is established by inspection of the proof of Proposition 31, which relies on the circuit construction from [42] and a straightforward translation between vertex cover and clique detection. Recall that the circuit in [42] simulates a well-known kernelization algorithm for k-Vertex-Cover. This algorithm produces a graph H containing $O(k^2)$ vertices and a new parameter $k_H \leq k$. This graph can be described by a string of length $O(k^4)$, and the pair (H, k_H) becomes the input string to the single oracle $\mathcal{O}$ that is necessary in the oracle circuit construction. (If $\mathcal{O}$ solves vertex cover, the resulting oracle circuit correctly solves $(n-k)$-Clique.)

The second item easily follows by simulating oracle circuits via interactive compression games (see e.g. [40, Section 5]). In other words, one can view a circuit with oracles as an interactive protocol between two parties, where one of them has unbounded computational power, and the other is restricted to computations in a fixed circuit class. The total number of wires feeding the oracle gates corresponds to the number of bits sent to the unbounded party. The desired lower bound for oracle circuits then follows immediately from the main result from [9], which shows that the random restriction method can be extended to establish limitations on circuits with oracle gates of large fan-in.   ◂

Informally, the main difficulty with the use of random restrictions in connection to HM Frontier E is that as soon as one simplifies a boolean circuit so that the oracle gate $\mathcal{O}$ is directly fed by input literals, one can fix just $(\log n)^{O(C)}$ input variables and eliminate this gate. Sacrificing such a small number of coordinates won't affect a typical worst-case lower bound based on the random restriction method.

5.1.6   Lower Bounds Through Reductions

Consider a reduction of PARITY to $\mathsf{MCSP}[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ by subquadratic-size n^ε-almost formulas with $n^{\varepsilon'}$ MCSP (possibly non-local) oracles at the bottom of each principal formula computing a gate with fanout > 1. By Theorem 46, such a reduction would imply $\mathsf{MCSP}[2^{n^{1/2}}/2n, 2^{n^{1/2}}] \notin n^\varepsilon$-$\mathsf{almost}$-$\mathsf{Formula}[N^{1.1}]$ assuming that after replacing all oracles by

n^ε-almost formulas of size $N^{1.1}$ the total size of the resulting circuit remains $< N^{2-9(\varepsilon+\varepsilon')}$. In combination with hardness magnification, this would give us $\mathsf{NP} \not\subseteq \mathsf{NC}^1$. Unfortunately, Theorem 46 rules this possibility out.

▶ **Corollary 51.** *PARITY is not computable by subquadratic-size n^ε-almost formulas with $n^{\varepsilon'}$ oracle gates computing $\mathsf{MCSP}[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$, assuming that after replacing all oracles by n^ε-almost formulas of size $N^{1.1}$ the total size of the resulting circuit remains $< N^{2-9(\varepsilon+\varepsilon')}$ for $\varepsilon + \varepsilon' < 1$.*

Proof. Assume the reduction in question exists. By Theorem 29, for every $\varepsilon > 0$ and all sufficiently big n, $\mathsf{MCSP}[2^{n^{1/2}}/2n, 2^{n^{1/2}}]$ is computable by $N^{1.1}$-size n^ε-almost formulas with local oracles at the bottom of principal formulas computing gates with fanout > 1. By the assumption, if we replace the MCSP oracles in the reduction by almost-formulas with local oracles, the resulting circuit is an $n^{\varepsilon+\varepsilon'}$-almost formula of size $N^{2-9(\varepsilon+\varepsilon')}$ with oracles of bounded fan-in. This contradicts the second item of Theorem 46.   ◀

Analogous arguments rule out the possibility of establishing strong lower bounds via reductions also in other HM frontiers.

5.2   Lower Bounds Below Magnification Threshold

The localizations presented in this section show that one cannot obtain strong circuit lower bounds by "lowering the threshold" in certain hardness magnification proofs. As a consequence of one of our results (Theorem 59 in Section 5.2.2), we also refute the Anti-Checker Hypothesis from [39].

5.2.1 AC^0 Lower Bounds via Pseudorandom Restrictions

In this section we show that the AC^0 lower bounds proved for MCSP (MKtP) via pseudorandom restrictions [16] (see also Section 3.1) localize in a very strong sense.

We use $\mathsf{AC}_d^0[O_1, O_2, \ldots, O_d]$ to denote AC_d^0 circuits extended with arbitrary oracles, such that oracle gates on the i-th level (the gates whose distance from the inputs is i) have fan-in at most O_i.

▶ **Theorem 52.** *There is a constant c such that for all $\varepsilon > 0$, constants d, and $O_1, O_2, \ldots, O_d$ such that $\prod_{i=1}^d O_i \le N/(\log N)^{\omega(1)}$, $\mathsf{MCSP}[n^c, n^{2c}] \notin \mathsf{AC}_d^0[O_1, O_2, \ldots, O_d][\mathsf{poly}(N)]$.*

▶ Remark 53. We remark that the constraint on oracles in the above theorem is incomparable to the second item of Proposition 50. Here we focus on the maximum oracle fan-in at each level, while there the focus is on the total fan-in of all oracles. A lower bound result for an explicit problem with parameters similar to Theorem 52 is not known for AC^0 oracle circuits extended with parity gates *(see [40] for results in this direction)*.

We are going to apply Lemma 17, together with the following well-known results on k-wise independence fooling CNFs.

▶ **Lemma 54** ([7, 53]). *$k = O(\log(M/\varepsilon) \cdot \log(M))$-wise independent distribution ε-fools M-clauses CNFs.*

Combining Lemma 17 and Lemma 54, we have the following lemma.

▶ **Lemma 55.** *Let φ be a t-width M-clause CNF formula over N inputs, $p = 2^{-q}$ for some $q \in \mathbb{N}$, and $\varepsilon_0 > 0$ be a real. There is a p-regular,*

$$k = \Theta(\log(M \cdot 2^{t(q+1)}/\varepsilon_0) \cdot \log(M \cdot 2^{t(q+1)}) \cdot q^{-1})\text{-wise}$$

independent random restriction $\boldsymbol{\rho}$ such that

$$\Pr_{\rho \sim \boldsymbol{\rho}}[\mathsf{DT}(\varphi{\restriction}_\rho) > s] \leq 2^{s+t+1}(5pt)^s + \varepsilon_0 \cdot 2^{(s+1)(2t+\log M)}.$$

Moreover, $\boldsymbol{\rho}$ is samplable with $O(t \cdot q \cdot \mathrm{polylog}(M, N) \cdot \log(1/\varepsilon_0))$ bits, and each output coordinate of the random restriction can be computed in time polynomial in the number of random bits.

The moreover part follows from standard construction of k-wise independent distributions (see e.g. [55]).

We also need the following lemma which states that an arbitrary oracle with inputs being small-size decision trees shrinks to a small-size decision tree with high probability, under suitable pseudorandom restrictions.

▶ **Lemma 56.** *Let $O : \{0,1\}^T \to \{0,1\}$ be an arbitrary function, and $D_1, D_2, \ldots, D_T$ be T k-query decision trees on variables $x_1, x_2, \ldots, x_N$. Let $F := O \circ (D_1, D_2, \ldots, D_T)$ be their compositions. For $s \in \mathbb{N}$, and all $k(s+1)$-wise independent $1/(T \cdot k^2)$-regular random restriction $\boldsymbol{\rho}$, we have*

$$\Pr_{\rho \sim \boldsymbol{\rho}}[\mathsf{DT}(F{\restriction}_\rho) > s] \leq \left(\frac{k(s+1)}{2e^2}\right)^{-(s+1)}.$$

Proof. We focus on the following particular decision tree for evaluating $\{D_1, D_2, \ldots, D_T\}$ with respect to a restriction $\rho : [N] \to \{0, 1, *\}$:

🟨 **Algorithm 1** $\mathsf{Eval}(\rho, D_1, D_2, \ldots, D_T)$.

- For i from 1 to T:
  - Simulate decision tree D_i with restriction ρ. That is, when D_i queries an index j, we feed ρ_j to D_i if $\rho_j \in \{0, 1\}$, and query the j-th bit otherwise.
- Let α_i be the output of the i-th decision tree, we output $\alpha = (\alpha_1, \alpha_2, \ldots, \alpha_T)$.

To obtain a decision tree for $F{\restriction}_\rho$, we can run $\mathsf{Eval}(\rho, D_1, D_2, \ldots, D_T)$ to obtain α first and output $F(\alpha)$ at the end.

Let $\widetilde{\mathsf{DT}}(F{\restriction}_\rho)$ be the query complexity of the above decision tree. Since $\mathsf{DT}(F{\restriction}_\rho) \leq \widetilde{\mathsf{DT}}(F{\restriction}_\rho)$ ($\mathsf{DT}(F{\restriction}_\rho)$ is the minimum complexity among all decision trees computing $F{\restriction}_\rho$), it suffices to bound

$$\Pr_{\rho \sim \boldsymbol{\rho}}[\widetilde{\mathsf{DT}}(F{\restriction}_\rho) > s].$$

Consider the event that $\widetilde{\mathsf{DT}}(F{\restriction}_\rho) > s$, it is equivalent to that there exists a string $w \in \{0,1\}^s$, such that if we fix the first s queried unrestricted bits in ρ according to w, Eval ends up querying $> s$ bits. (Note that since we only care about whether $\widetilde{\mathsf{DT}}(F{\restriction}_\rho) > s$, we can force the algorithm to abort if it tries to make the $(s+1)$-th query.)

Now, suppose we fix the string w, then the number of queries made by Eval only depends on ρ. Suppose the algorithm has queried at least $s+1$ bits, we let $D'_1, D'_2, \ldots, D'_t$ ($t \le s+1$) be the decision trees in which the algorithm made queries during the first $s+1$ queries. This implies that if we run $\mathsf{Eval}(\rho, D'_1, D'_2, \ldots, D'_t)$ with respect to the same string w, the algorithm also makes at least $s+1$ queries.

Now, since $\boldsymbol{\rho}$ is $k(s+1)$-wise independent. The probability that $\mathsf{Eval}(\rho, D'_1, D'_2, \ldots, D'_t)$ makes at least $s+1$ queries with respect to the fixed string w is bounded by

$$(T \cdot k^2)^{-(s+1)} \cdot \binom{t \cdot k}{s+1}$$

$$\le (T \cdot k^2)^{-(s+1)} \cdot \left(\frac{t \cdot k \cdot e}{s+1}\right)^{s+1}$$

$$\le \left(\frac{T \cdot k \cdot (s+1)}{t \cdot e}\right)^{-(s+1)} \le \left(\frac{T \cdot k}{e}\right)^{-(s+1)}.$$

Putting everything together, we have

$$\Pr_{\rho \sim \boldsymbol{\rho}}[\widetilde{\mathsf{DT}}(F\!\restriction_\rho) > s]$$

$$\le 2^s \cdot \left(\frac{T \cdot k}{e}\right)^{-(s+1)} \cdot \sum_{t=0}^{s+1} \binom{T}{t}$$

$$\le 2^s \cdot \left(\frac{T \cdot k}{e}\right)^{-(s+1)} \cdot \left(\frac{T \cdot e}{s+1}\right)^{s+1}$$

$$\le \left(\frac{k \cdot (s+1)}{2e^2}\right)^{-(s+1)}.$$

◀

▶ **Remark 57.** Clearly, Lemma 56 also holds when $\boldsymbol{\rho}$ is $k(s+1)$-wise independent and p-regular, for $p \le \frac{1}{T \cdot k^2}$.

Now we are ready to prove Theorem 52.

Proof of Theorem 52. We assume N and $\log N$ are both powers of 2 for simplicity. Let $p = 1/\log^5 N$, $\varepsilon_0 = 2^{-\log^6 N}$, $s = t = 10\log^2 N$, $M = 2^s \cdot N^{\log N}$, and $\boldsymbol{\rho}$ be the k-wise independent p-regular random restriction guaranteed by Lemma 55. Note that we have $k = \omega(\log^6 N)$ and $k = \log^{O(1)} N$.

Let $C \in \mathsf{AC}^0_d[O_1, O_2, \ldots, O_d]$ be a circuit with S gates computing $\mathsf{MCSP}[n^c, n^{2c}]$. For each $i \in [d]$, let S_i be the number of gates at level i (i.e., the gates whose distance from the input gates is i). Recall that O_i is the maximum oracle fan-in at level i. We are going to prove the stronger claim that $S = \Omega(N^{\log N})$. Now, suppose for the sake of contradiction that $S \le N^{\log N}/8$.

Now we proceed in d iterations. We will ensure that at the end of the i-th iteration, all gates at level i become s-query decision trees with high probability. At the i-th iteration, we apply ρ

$$\tau_i = \lceil \log_{1/p} O_i \rceil + 1$$

times. It is straightforward to see that the composition of τ_i independent restrictions from $\boldsymbol{\rho}$ is a k-wise independent p_i-regular random restriction for $p_i = p^{\tau_i} \le \frac{1}{O_i \cdot \log^5 N}$.

Note that each oracle gate at original level i has inputs computed by s-query decision trees (at the first step, one can treat the input variables as 1-query decision trees). By Lemma 56 and noting that $k \geq s(s+1)$ and $O_i \cdot \log^5 N \geq O_i \cdot s^2$, with probability at least

$$1 - S_i \cdot \left(\frac{s(s+1)}{2e^2} \right)^{-(s+1)} \geq 1 - S_i \cdot N^{-\log N},$$

all oracle gates at level i become s-query decision trees after these τ_i restrictions.

Similarly, note that each AND / OR gate at level i are equivalent to a CNF or DNF with width-s and size at most $2^s \cdot S$. By Lemma 55, again with probability at least

$$
\begin{aligned}
1 - S_i \cdot & \left(2^{s+t+1}(5pt)^s + \varepsilon_0 \cdot 2^{(s+1)(2t+\log M)} \right) \\
\geq 1 - S_i \cdot & \Big(2^{20\log^2 N+1}(5 \cdot (1/\log^5 N) \cdot 10\log^2 N)^{10\log^2 N} \\
& + 2^{-\log^6 N} \cdot 2^{(10\log^2 N+1)(20\log^2 N+\log(N^{\log N} \cdot 2^{10\log^2 N}))} \Big) \\
\geq 1 - S_i \cdot & N^{-\log N},
\end{aligned}
$$

all AND / OR gates at level i become s-query decision tree after these τ_i restrictions.

Finally, note that in total we have applied $\boldsymbol{\rho}$ at most

$$\tau_{\mathsf{total}} = 2d + \log_{1/p}\left(\prod_{i=1}^{d} O_i \right) = \log_{1/p} N - \omega(1)$$

times, and the final output gate shrinks to an s-query decision tree with probability at least

$$1 - 2 \cdot S \cdot N^{-\log N}.$$

Since $S \leq N^{\log N}/8$, with probability at least $3/4$, after all these restrictions, C is equivalent to an s-query decision tree.

Now let $p_{\mathsf{end}} = p^{\tau_{\mathsf{total}}} = N^{-1} \cdot p^{-\omega(1)}$. By Chebyshev's inequality, the number of unrestricted variables at the end of the restriction is at least $N_{\mathsf{remain}} = \frac{1}{2} \cdot p_{\mathsf{end}} \cdot N = (\log N)^{\omega(1)}$ with probability at least $1/2$. Therefore, with probability at least $1/4$, at the end of the restrictions, it holds that the remaining circuit C is equivalent to an s-query decision tree D, and the number of unrestricted variables is at least N_{remain}.

Suppose we fix all these remaining unrestricted variables to be 0 to get an input x^*, since each restriction from $\boldsymbol{\rho}$ can be computed by a $\mathsf{poly}(n)$-size circuit, x^* has a circuit of $\mathsf{poly}(n) \cdot \log N = \mathsf{poly}(n) \leq n^c$ size (now we set c). Let S be the set of input variables that D queries on the input x^*. Note that there are at least $2^{N_{\mathsf{remain}} - |S|}$ ways of assigning values to unrestricted variables while keeping variables in S all 0. And we can see that F's output on x^* is the same as its output on all of these assignments. But there must exist at least one assignment such the MCSP value is at least $(\log N)^{2c} = n^{2c}$ $(2^{N_{\mathsf{remain}} - |S|} = 2^{n^{\omega(1)}})$, contradiction to the assumption that C computes $\mathsf{MCSP}[n^c, n^{2c}]$. ◀

5.2.2 The Nearly Quadratic Formula Lower Bound of [23]

In this section, we prove that the nearly quadratic formula lower bound of [23] localizes, and thereby proving the third item of Theorem 2. This localization indeed refutes a family of possible approaches to establish circuit lower bounds through hardness magnification via "lowering the threshold".

More concretely, consider the following hypothesized approach. Suppose we can compute $\mathsf{MCSP}[2^{\sqrt{n}}]$ by a formula F with NP oracles, such that when we replace every oracle O with fan-in β in F by a formula of size β^k which reads all its inputs exactly β^{k-1} times, the size of the new formula is less than $N^{1.99}$. Then we know that NP cannot be computed by formulas of size n^k which reads all its inputs exactly n^{k-1} times, as otherwise we get an $N^{1.99}$-size formula for $\mathsf{MCSP}[2^{\sqrt{n}}]$, which is a contradiction to the lower bound in [23]. If this holds for all $k > 0$, then we would have $\mathsf{NP} \not\subseteq \mathsf{Formula}[n^k]$ for all k.

In the following, by localizing [23], we show that there is no such oracle formula construction for MCSP even if the oracles can be arbitrary. This excludes magnification theorems obtained by approaches that unconditionally produce circuits with oracles, and essentially addresses a question from [39]. It also suggests that the consideration of almost-formulas in HM Frontier C is unavoidable.

A Size Measure on Oracle Formulas and A Potential Approach to Formula Size Lower Bound

We first introduce a size measure Size_t on oracle formulas to formalize the previous discussion.

For a parameter t and an oracle formula F, we define $\mathsf{Size}_t(F)$ as the size of the formula, if we replace every oracle O with fan-in β in F by a formula of size β^t which reads all its inputs exactly β^{t-1} times.

More formally,

$$\mathsf{SIZE}_t(F) := \begin{cases} \mathsf{SIZE}_t(F_1) + \mathsf{SIZE}_t(F_2) & F = F_1 \wedge F_2 \text{ or } F = F_1 \vee F_2, \\ \beta^{t-1} \cdot \left(\sum_{i=1}^{\beta} \mathsf{SIZE}_t(F_i) \right) & F = O(F_1, F_2, \ldots, F_\beta). \end{cases}$$

▶ **Proposition 58.** *For a constant $k > 0$, if there is an NP oracle formula F (all oracles are languages in NP) for $\mathsf{MCSP}[2^{\sqrt{n}}]$ such that $\mathsf{SIZE}_{k+1}(F) \leq N^{2-\varepsilon}$ for a constant $\varepsilon > 0$, then $\mathsf{NP} \not\subseteq \mathsf{Formula}[n^k]$.*

Proof. Suppose $\mathsf{NP} \subseteq \mathsf{Formula}[n^k]$ for the sake of contradiction. Then in particular each NP language can be computed by a size-n^{k+1} formula which reads all its inputs exactly n^k times by adding some dummy nodes in the formula. Therefore, by replacing all NP oracles in F by such formulas, we have an $N^{2-\varepsilon}$-size formula for $\mathsf{MCSP}[2^{\sqrt{n}}]$, in contradiction to the lower bound in [23]. ◀

Localization of [23]

Our following theorem shows that the above approach is not viable even with $k = 3$ by localizing [23], with a moderate constraint on the adaptivity of the oracle circuits.

▶ **Theorem 59.** *There is a universal constant c such that for all constants $\varepsilon > 0$ and $\alpha > 2$, $\mathsf{MCSP}[n^c, 2^{(\varepsilon/\alpha) \cdot n}]$ cannot be computed by oracle formulas F with $\mathsf{SIZE}_3(F) \leq N^{2-\varepsilon}$ and adaptivity $o(\log N / \log \log N)$ (that is, on any path from root to a leaf, there are at most $o(\log N / \log \log N)$ oracles).*

▶ Remark 60. It is not hard to see that the adaptivity can be at most $O(\log N)$ given the condition $\mathsf{SIZE}_3(F) \leq N^{2-\varepsilon}$.

Before proving Theorem 59, we first show it refutes the Anti-Checker Hypothesis (restated below) from [39].

The Anti-Checker Hypothesis. *For every $\lambda \in (0,1)$, there are $\varepsilon > 0$ and a collection $\mathcal{Y} = \{Y_1, \ldots, Y_\ell\}$ of sets $Y_i \subseteq \{0,1\}^n$, where $\ell = 2^{(2-\varepsilon)n}$ and each $|Y_i| = 2^{n^{1-\varepsilon}}$, for which the following holds.*

If $f : \{0,1\}^n \mapsto \{0,1\}$ and $f \notin \mathsf{Circuit}[2^{n^\lambda}]$, then some set $Y \in \mathcal{Y}$ forms an anti-checker for f: For each circuit C of size $2^{n^\lambda}/10n$, there is an input $y \in Y$ such that $C(y) \neq f(y)$.

▶ **Corollary 61.** *The Anti-Checker Hypothesis is false.*

Proof. It is easy to see that, assuming the Anti-Checker Hypothesis, we can solve $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ with a formula F of $N^{2-\varepsilon}$ size which uses $N^{2-\varepsilon}$ oracles of fan-in $\mathsf{poly}(n)2^{n^{1-\varepsilon}} = \mathsf{polylog}(N) \cdot 2^{(\log N)^{1-\varepsilon}} = N^{o(1)}$ only at the layer above the inputs, for some $\varepsilon > 0$. However, since $\mathsf{SIZE}_3(F) \leq N^{2-\varepsilon+o(1)}$, F cannot compute $\mathsf{MCSP}[2^{n^{1/3}}, 2^{n^{2/3}}]$ by Theorem 59, contradiction. ◀

Now we are ready to prove Theorem 59.

Proof of Theorem 59. Let $k = \log^3 N$, and $\boldsymbol{\rho}$ be the k-wise independent $(1/\sqrt{k})$-regular random restriction guaranteed by Lemma 12.

For an oracle formula F and a sub-formula G of it, we say G is a maximal sub-formula if G is an entire subtree rooted at either the root, an oracle gate, or a gate whose father is an oracle.

We are going to apply $t = \Theta(\log_k N)$ independent pseudorandom restrictions $\boldsymbol{\rho}_1, \boldsymbol{\rho}_2, \ldots, \boldsymbol{\rho}_t$, each distributed identically to $\boldsymbol{\rho}$, where t will be set precisely later.

The Overall Proof Structure

To analyze the size of the oracle formula under the random restriction sequence $\boldsymbol{\rho}_1, \boldsymbol{\rho}_2, \ldots, \boldsymbol{\rho}_t$, we define a potential function Φ inductively for all maximal sub-formulas of the given formula F. As it will be clear from the definition, Φ is not only a function of the structure of the oracle formula, but also depends on the history of the pseudorandom restrictions.

Formally, for each maximal sub-formula G of the given formula F, and for each integer $0 \leq i \leq t$, we define a random variable $\Phi_{G,i}$, which denotes the potential function of G after the first i pseudorandom restrictions and only depends on $\boldsymbol{\rho}_1, \boldsymbol{\rho}_2, \ldots, \boldsymbol{\rho}_i$.

Definition of Tiny formulas and Blow up

For an oracle formula, if the top gate is an oracle, we say it is tiny if it depends on at most $\log N$ variables. Otherwise, we say it is tiny if it depends on at most $c_{\mathsf{tiny}} \cdot k$ variables, for a constant c_{tiny} to be specified later.

After each pseudorandom restriction, for a formula with an oracle gate at the top, when it depends on at most $b = 20$ variables, we blow it up to a formula of size $B = 2^b$ (note that if there are two oracle gates u, v such that u and v both depend on at most b variables and u is an ancestor of v, then it suffices to only blow up u).

The above two definitions (tiny formulas and the process of blowing up) may not seem easy to understand at first. Let us explain the motivation behind them. The key difficulty of the proof is to handle the oracle gates properly. The process of blowing up ensures that whenever an oracle becomes too small, we just replace it with a constant size normal formula, so it becomes easier to deal with.

The definition of tiny formulas is more subtle. As it will be clearly in Case II and Case III of the inductive definition of Φ, setting the threshold of being tiny to $\log N$ for oracle formulas with top oracle gates ensures that the corresponding event of becoming tiny happens with high probability, which is indeed crucial in our proof.

The properties of Φ

We require the following properties on Φ.

1. For an oracle formula F, Φ is multiplied by a factor of $\frac{c_F}{k}$ under $\boldsymbol{\rho}$ in expectation, where c_F depends on F but it is upper bounded by a universal constant.
2. With probability $1 - p_F$, for all stages, and all maximal sub-formulas G of F, $\Phi = 0$ for G implies that G is tiny, where p_F depends on F but it is upper bounded by N^{-2}.
3. It holds that either $\Phi = 0$ or $\Phi \geq 1$. Together with the second item, it implies that if the oracle formula is not tiny then $\Phi \geq 1$.

   With these carefully designed properties of Φ, the overall proof is straightforward. We first show that Φ of F is closely related to $\mathsf{SIZE}_3(F)$, and our conditions on the oracle formula imply that Φ of the whole oracle formula is bounded by $N^{2-\varepsilon+o(1)}$ at the beginning. Then after roughly $t \approx \log_k(N^{2-\varepsilon+o(1)})$ rounds of restrictions from $\boldsymbol{\rho}$, Φ becomes 0 with a good probability, which also implies the whole oracle formula becomes tiny (only depend on $\mathrm{polylog}(N)$ bits).

   But then we argue that after t rounds of restrictions from $\boldsymbol{\rho}$, with high probability the number of unrestricted variables is still at least $N^{\Omega(1)}$. Using a similar argument as from [23, 42, 39], we show that the tiny oracle formula left behind cannot compute $\mathsf{MCSP}[n^c, 2^{\varepsilon/\alpha \cdot n}]$ on the remaining variables, which concludes the proof.

The Inductive Definition of the Potential Function Φ

In the following, we gradually develop the definition of the potential function Φ. We remark that Case I and Case II below are actually special cases of Case III and Case IV respectively. We discuss them first in the hope that they provide some intuitions and make it easier to understand the more complicated Case III and Case IV.

Case I: Φ for a Pure Formula

We begin with the simplest case of pure formulas F (formulas with no oracles) of size S. We define

$$\Phi = \begin{cases} S & S \geq 100 \cdot k, \\ 0 & \text{otherwise.} \end{cases}$$

   It follows from the shrinkage lemma [21], formula decomposition [51, Claim 6.2], and the k-wise independence of $\boldsymbol{\rho}$ that, when $S \geq 100 \cdot k$, the *expected size* of S drops by a factor of at least k/c_{Tal}, for a universal constant c_{Tal} (we can set $c_F = c_{\mathsf{Tal}}$). Otherwise, the formula is tiny. It is straightforward to verify that all three properties of Φ are satisfied (we can set $p_F = 0$ in this case).

Case II: Φ for a Pure Oracle

Next we consider the case that F is a pure oracle O with fan-in T (pure oracle means each input to O is just a variable). We set

$$\Phi = T^2 \cdot k^3$$

at the beginning. And set $\Phi \leftarrow \Phi/k$ after each $\boldsymbol{\rho}$. Whenever it happens $\Phi < 1$, we set $\Phi = 0$ afterwards. Here, we can simply set $c_F = 1$.

Now we argue that with probability at least $1 - N^{-5}$ (that is, we set $p_F = N^{-5}$), when $\Phi = 0$, O only depends on at most $\log N$ variables and therefore becomes tiny.

Note that $\Phi = 0$ means at least $\log_k T^2$ rounds of random restrictions have been applied.[16] Their composition is a k-wise independent restriction which keeps a variable unrestricted with probability at most T^{-1}. Therefore, the probability that the number of alive variable is larger than $\log N$ is smaller than

$$\binom{T}{\log N} \cdot T^{-\log N} \leq \left(\frac{e \cdot T}{\log N}\right)^{\log N} \cdot T^{-\log N} \leq \left(\frac{e}{\log N}\right)^{\log N} \leq N^{-5}.$$

Note that in the above inequalities we can safely assume $T > \log N$.

Case III: Φ for an Oracle Formula with an Oracle Top Gate

Then we move to the case of a maximal sub-formula F with an oracle top gate O with fan-in T. Let Φ_i be the corresponding potential function of the maximal sub-formula with root being the i-th input to O. We set

$$\Phi = \max\left(\sum_{i=1}^{T} \Phi_i, 1/k\right) \cdot T^2 \cdot k^4,$$

at the beginning.

When $\sum_{i=1}^{T} \Phi_i > 0$, we still let $\Phi = \left(\sum_{i=1}^{T} \Phi_i\right) \cdot T^2 \cdot k^4$. When $\sum_{i=1}^{T} \Phi_i$ first becomes 0 (this could happen before the first restriction, if $\sum_{i=1}^{T} \Phi_i = 0$ at the beginning), we set $\Phi = T^2 \cdot k^3$ and decrease it by a factor of k during each later restriction, and set it to 0 if it becomes < 1.

Here, we set c_F to be the maximum of $c_{F'}$ for all maximal sub-formulas F' whose root is an input to the top oracle gate O in F.

First let us argue that Φ is multiplied by a factor of $\frac{c_F}{k}$ after each ρ in expectation. When $\sum_{i=1}^{T} \Phi_i = 0$, it is evident from the way we set Φ (note that $c_F \geq 1$). When $\sum_{i=1}^{T} \Phi_i > 0$, it follows from the induction as each Φ_i is multiplied by a factor of $\frac{c_F}{k}$ after each ρ in expectation. In the borderline case when $\sum_{i=1}^{T} \Phi_i > 0$ before ρ and becomes 0 afterwards. One can see Φ drops from at least $T^2 \cdot k^4$ to at most $T^2 \cdot k^3$.

Moreover, when $\sum_{i=1}^{T} \Phi_i = 0$, with probability at least $1 - \sum_{i=1}^{T} p_{F_i}$ (F_i is the i-th sub-formula whose root is an input to the top oracle gate O in F) all the sub-formulas are tiny, so at this time the oracle depends on at most $O(T \cdot k)$ variables.

Therefore, when Φ drops to 0, with probability at least $1 - \sum_{i=1}^{T} p_{F_i} - N^{-5}$ the whole oracle formula becomes tiny, by a calculation similar to the pure oracle case. Therefore, we can set $p_F = \sum_{i=1}^{T} p_{F_i} + N^{-5}$.

Case IV: Φ for a Formula with Oracle Leaves

Finally, we deal with the most complicated case when the maximal sub-formula F is a formula with oracle leaves. Suppose F is a formula of size S with m oracle leaves. Let Φ_i be the potential function of the sub-formula corresponding to the i-th oracle leaf. Also, let c_{drop} be the maximum of the c_F's of all the sub-formulas corresponding to the oracle leaves.

---

[16] Note that for this argument, a potential function of $T \cdot k$ already suffices. We use $T^2 \cdot k^3$ here to make it consistent with Case III.

The difficulty in analyzing this case is that there could be many oracles which are tiny but have not blown up yet, and we have to keep track of the number of such oracles. Let N_{active} be the number of remaining active tiny oracles (oracles which are tiny but have not blown up). Clearly, $N_{\mathsf{active}} \leq S$ at the beginning.

We set

$$\Phi = S + N_{\mathsf{active}} \cdot k^2 + \sum_{i=1}^{m} \Phi_i \cdot k^4,$$

at the beginning. When $S \leq 100 \cdot k$ happens, we change Φ to be

$$N_{\mathsf{active}} \cdot k^2 + \sum_{i=1}^{m} \Phi_i \cdot k^4$$

afterwards.

After each $\boldsymbol{\rho}$, if $S \geq 100 \cdot k$, the expected size of S becomes at most

$$c_1 \cdot S/k + c_2 \cdot k \cdot \left(\sum_{i=1}^{m} \Phi_i + N_{\mathsf{active}} \right),$$

for two universal constants c_1 and c_2. This bound holds because, by Claim 4.4 of [24], a formula of size S can be decomposed into $6S/k$ sub-formulas, each of size at most k, and each formula has at most 2 sub-formula children.

The number of active oracle leaves (which are not blown up) is at most $\sum_{i=1}^{m} \Phi_i + N_{\mathsf{active}}$. Hence, at least $6 \cdot S/k - \sum_{i=1}^{m} \Phi_i - N_{\mathsf{active}}$ sub-formulas do not contain an active oracle leaf, and their total expected size is $O(S/k)$ after $\boldsymbol{\rho}$ (by Lemma 4.1 and Lemma 4.3 of [24], and [51]). For those sub-formulas containing active oracle leaves, their total size is at most $(\sum_{i=1}^{m} \Phi_i + N_{\mathsf{active}}) \cdot O(k)$ after $\boldsymbol{\rho}$ (this takes account of the worst case situation that all these active oracle leaves blow up).

Also, we can see that after $\boldsymbol{\rho}$, N_{active} becomes at most

$$N_{\mathsf{active}}/k^2 + \sum_{i=1}^{m} \Phi_i$$

in expectation. This is because for a tiny active oracle depending on at most $\log N$ variables, the probability that it does not blow up after $\boldsymbol{\rho}$ is at most

$$\binom{\log N}{b} \cdot k^{-b/2} \leq (\log N)^{b - 1.5 \cdot b} = (\log N)^{-10} \leq 1/k^2.$$

By induction, we also have that $\sum_{i=1}^{m} \Phi_i$ is multiplied by a factor of $\frac{c_{\mathsf{drop}}}{k}$ in expectation as well after each $\boldsymbol{\rho}$. Therefore, after $\boldsymbol{\rho}$, the expectation of Φ can be bounded by

$$c_1 \cdot S/k + c_2 k \left(\sum_{i=1}^{m} \Phi_i + N_{\mathsf{active}} \right) + \left(N_{\mathsf{active}}/k^2 + \sum_{i=1}^{m} \Phi_i \right) \cdot k^2 + \left(\sum_{i=1}^{m} \Phi_i \right) \cdot \frac{c_{\mathsf{drop}}}{k} \cdot k^4$$

$$\leq S \cdot \frac{c_1}{k} + N_{\mathsf{active}} \cdot k^2 \cdot \frac{c_2 + 1/k}{k} + \sum_{i=1}^{m} \Phi_i \cdot k^4 \cdot \left(\frac{c_{\mathsf{drop}} + c_2/k^2 + 1/k}{k} \right).$$

We can set

$$c_F = \max(c_1, c_2 + 1/k, c_{\mathsf{drop}} + c_2/k^2 + 1/k).$$

Recall that when $S \leq 100 \cdot k$ happens, we change Φ to be

$$N_{\mathsf{active}} \cdot k^2 + \sum_{i=1}^{m} \Phi_i \cdot k^4$$

afterwards.

By the previous discussion, after this Φ still drops by a factor of k/c_F in expectation after each $\boldsymbol{\rho}$. Note that when $\Phi = 0$, we can see the size of the whole formula is smaller than $B \cdot 100 \cdot k = O(k)$, therefore it is tiny (here we set $c_{\mathsf{tiny}} = B \cdot 100$). This is because $\Phi = 0$ implies $S \leq 100 \cdot k$ happened at some point, and also $N_{\mathsf{active}} = \sum_{i=1}^{m} \Phi_i = 0$. They together imply that all oracles have blown up, and the size bound follows since each oracle adds at most B leaves.

Let F_i be the sub-formula with root being the i-th oracle leaf. In this case, we can set $p_F = \sum_{i=1}^{m} p_{F_i}$.

The MCSP Lower Bound

Let F be an oracle formula with $\mathsf{SIZE}_3(F) \leq N^{2-\varepsilon}$ and adaptivity $\tau = o(\log N / \log \log N)$. We first need to verify that c_F is upper bounded by a universal constant. One can upper bound

$$c_F \leq \max(c_1, c_2 + 1/k, c_{\mathsf{Tal}}) + \tau \cdot (c_2/k^2 + 1/k) \leq \max(c_1, c_2 + 1/k, c_{\mathsf{Tal}}) + o(1) = O(1).$$

We can also upper bound p_F by $p_F \leq N^{-5} \cdot N^2 = N^{-3}$.

By the inductive definition of the potential function Ψ on maximal sub-formulas, it is not hard to show that

$$\Phi \leq \mathsf{SIZE}_3(F) \cdot k^{O(\tau)} \leq N^{2-\varepsilon+o(1)}.$$

Note that this inequality crucially employs the definition of $\mathsf{SIZE}_3(\cdot)$.

After each $\boldsymbol{\rho}$, Φ is reduced by a factor of k/c_F. After

$$t = \lceil \log_{k/c_F} \Phi \rceil + 2$$

rounds of $\boldsymbol{\rho}$, the expected Φ of the overall formula becomes $< 1/10$, which means with probability $0.9 - p_F \geq 0.8$ it is tiny and only depends on at most $O(k) = O(\log^3 N)$ variables.

Note that by definition

$$(k/c_F)^t \leq \Phi \cdot k^3,$$

and therefore

$$k^t \leq \Phi \cdot k^3 \cdot (c_F)^t \leq N^{2-\varepsilon+o(1)},$$

as $(c_F)^t = (c_F)^{O(\log N / \log \log N)} = N^{o(1)}$.

The composition of t independent $\boldsymbol{\rho}$ keeps a variable unrestricted with probability $k^{-t/2} \geq N^{-1+\varepsilon/2-o(1)}$, and is clearly pairwise independent. By Chebyshev's inequality, after t restrictions from $\boldsymbol{\rho}$, with probability 0.5, at least

$$1/2 \cdot N \cdot N^{-1+\varepsilon/2-o(1)} \geq N^{\varepsilon/2-o(1)}$$

variables remain active. So with probability at least 0.3, after t restrictions from $\boldsymbol{\rho}$, the remaining formula F only depends on $O(\log^3 N)$ variables, and the number of remaining unrestricted variables is at least $N^{\varepsilon/2-o(1)}$.

Suppose we fix all these remaining unrestricted variables to be 0 to get an input x^*. Since each restriction from $\boldsymbol{\rho}$ can be computed by a $\mathsf{poly}(n)$-size circuit, x^* has a circuit of $\mathsf{poly}(n) \cdot t = \mathsf{poly}(n) \leq n^c$ size (here we set c). Let S be the set of input variables that F depends on. Note that there are at least $2^{N^{\varepsilon/2 - o(1)} - |S|}$ ways of assigning values to unrestricted variables while keeping variables in S all 0. Since F only depends on S, F's output on x^* is the same as its output on all of these assignments. But there must exist at least one assignment such the MCSP value is at least $N^{\varepsilon/\alpha} = 2^{(\varepsilon/\alpha) \cdot n}$ as $\alpha > 2$. Therefore, F cannot compute $\mathsf{MCSP}[n^c, 2^{(\varepsilon/\alpha) \cdot n}]$. ◀

References

1   Miklós Ajtai. Σ_1^1-formulae on finite structures. *Annals of Pure and Applied Logic*, 24(1):1–48, 1983.

2   Eric Allender, Lisa Hellerstein, Paul McCabe, Toniann Pitassi, and Michael E. Saks. Minimizing Disjunctive Normal Form Formulas and AC^0 Circuits Given a Truth Table. *SIAM J. Comput.*, 38(1):63–84, 2008. `doi:10.1137/060664537`.

3   Eric Allender and Michal Koucký. Amplifying lower bounds by means of self-reducibility. *J. ACM*, 57(3):14:1–14:36, 2010.

4   Alexander E. Andreev and Stasys Jukna. Very large cliques are easy to detect. *Discrete Mathematics*, 308(16):3717–3721, 2008. `doi:10.1016/j.disc.2007.07.036`.

5   Benny Applebaum, Boaz Barak, and David Xiao. On Basing Lower-Bounds for Learning on Worst-Case Assumptions. In *Symposium on Foundations of Computer Science* (FOCS), pages 211–220, 2008.

6   Sanjeev Arora and Boaz Barak. *Computational complexity: a modern approach.* Cambridge University Press, 2009.

7   Louay M. J. Bazzi. Polylogarithmic Independence Can Fool DNF Formulas. *SIAM J. Comput.*, 38(6):2220–2272, 2009. `doi:10.1137/070691954`.

8   Marco L. Carmosino, Russell Impagliazzo, Valentine Kabanets, and Antonina Kolokolova. Learning Algorithms from Natural Proofs. In *Conference on Computational Complexity* (CCC), pages 10:1–10:24, 2016. `doi:10.4230/LIPIcs.CCC.2016.10`.

9   Arkadev Chattopadhyay and Rahul Santhanam. Lower Bounds on Interactive Compressibility by Constant-Depth Circuits. In *Symposium on Foundations of Computer Science* (FOCS), pages 619–628, 2012.

10 Lijie Chen, Ce Jin, and Ryan Williams. Hardness Magnification for all Sparse NP Languages. In *Symposium on Foundations of Computer Science* (FOCS), 2019.

11 Lijie Chen, Ce Jin, and Ryan Williams. Sharp Threshold Results for Computational Complexity. In *Unpublished manuscript*, 2019.

12 Lijie Chen, Dylan M. McKay, Cody D. Murray, and R. Ryan Williams. Relations and Equivalences Between Circuit Lower Bounds and Karp-Lipton Theorems. In *Computational Complexity Conference* (CCC), 2019.

13 Lijie Chen and Roei Tell. Bootstrapping Results for Threshold Circuits "Just Beyond" Known Lower Bounds. In *Symposium on Theory of Computing* (STOC), 2019.

14 Xi Chen, Igor Carboni Oliveira, and Rocco A. Servedio. Addition is exponentially harder than counting for shallow monotone circuits. In *Symposium on Theory of Computing* (STOC), pages 1232–1245, 2017.

15 Mahdi Cheraghchi, Elena Grigorescu, Brendan Juba, Karl Wimmer, and Ning Xie. AC$^0 \circ$ MOD$_2$ lower bounds for the Boolean Inner Product. *J. Comput. Syst. Sci.*, 97:45–59, 2018.

16 Mahdi Cheraghchi, Valentine Kabanets, Zhenjian Lu, and Dimitrios Myrisiotis. Circuit Lower Bounds for MCSP from Local Pseudorandom Generators. In *International Colloquium on Automata, Languages, and Programming* (ICALP), pages 39:1–39:14, 2019. `doi:10.4230/LIPIcs.ICALP.2019.39`.

17   Rodney G. Downey and Michael R. Fellows. *Fundamentals of Parameterized Complexity*. Texts in Computer Science. Springer, 2013. `doi:10.1007/978-1-4471-5559-1`.

18   Jürgen Forster. A linear lower bound on the unbounded error probabilistic communication complexity. *J. Comput. Syst. Sci.*, 65(4):612–625, 2002. `doi:10.1016/S0022-0000(02)00019-3`.

19   Merrick L. Furst, James B. Saxe, and Michael Sipser. Parity, Circuits, and the Polynomial-Time Hierarchy. *Mathematical Systems Theory*, 17(1):13–27, 1984. `doi:10.1007/BF01744431`.

20   Mika Göös, Pritish Kamath, Robert Robere, and Dmitry Sokolov. Adventures in Monotone Complexity and TFNP. In *Innovations in Theoretical Computer Science Conference* (ITCS), pages 38:1–38:19, 2019.

21   Johan Håstad. The Shrinkage Exponent of de Morgan Formulas is 2. *SIAM J. Comput.*, 27(1):48–64, 1998. `doi:10.1137/S0097539794261556`.

22   Shuichi Hirahara. Non-Black-Box Worst-Case to Average-Case Reductions within NP. In *Symposium on Foundations of Computer Science* (FOCS), pages 247–258, 2018.

23   Shuichi Hirahara and Rahul Santhanam. On the Average-Case Complexity of MCSP and Its Variants. In *Computational Complexity Conference* (CCC), pages 7:1–7:20, 2017. `doi:10.4230/LIPIcs.CCC.2017.7`.

24   Russell Impagliazzo, Raghu Meka, and David Zuckerman. Pseudorandomness from Shrinkage. *J. ACM*, 66(2):11:1–11:16, 2019. `doi:10.1145/3230630`.

25   Russell Impagliazzo, Ramamohan Paturi, and Michael E. Saks. Size-Depth Tradeoffs for Threshold Circuits. *SIAM J. Comput.*, 26(3):693–707, 1997.

26   Russell Impagliazzo, Ramamohan Paturi, and Francis Zane. Which Problems Have Strongly Exponential Complexity? *J. Comput. Syst. Sci.*, 63(4):512–530, 2001. `doi:10.1006/jcss.2001.1774`.

27   Russell Impagliazzo and Avi Wigderson. Randomness vs Time: Derandomization under a Uniform Assumption. *J. Comput. Syst. Sci.*, 63(4):672–688, 2001. `doi:10.1006/jcss.2001.1780`.

28   Emil Jeřábek. Approximate counting by hashing in bounded arithmetic. *J. Symb. Log.*, 74(3):829–860, 2009. `doi:10.2178/jsl/1245158087`.

29   Stasys Jukna. *Boolean Function Complexity - Advances and Frontiers*. Springer, 2012.

30   Jørn Justesen. Class of constructive asymptotically good algebraic codes. *IEEE Trans. Information Theory*, 18(5):652–656, 1972. `doi:10.1109/TIT.1972.1054893`.

31   Mauricio Karchmer and Avi Wigderson. Monotone Circuits for Connectivity Require Super-Logarithmic Depth. *SIAM J. Discrete Math.*, 3(2):255–265, 1990. `doi:10.1137/0403021`.

32   Ilan Komargodski and Raz Ran. Average-case lower bounds for formula size. In *Symposium on Theory of Computing* (STOC), 2013.

33   Swastik Kopparty. On the complexity of powering in finite fields. In *Proceedings of the Symposium on Theory of Computing* (STOC), pages 489–498, 2011. `doi:10.1145/1993636.1993702`.

34   Jan Krajíček. *Forcing with random variables and proof complexity*. Cambridge University Press, 2011.

35   Richard J. Lipton and Ryan Williams. Amplifying circuit lower bounds against polynomial time, with applications. *Computational Complexity*, 22(2):311–343, 2013.

36   Dylan M. McKay, Cody D. Murray, and R. Ryan Williams. Weak Lower Bounds on Resource-Bounded Compression Imply Strong Separations of Complexity Classes. In *Symposium on Theory of Computing* (STOC), 2019.

37   Moritz Müller and Ján Pich. Feasibly constructive proofs of succinct weak circuit lower bounds. *Electronic Colloquium on Computational Complexity* (ECCC), 24:144, 2017. URL: `https://eccc.weizmann.ac.il/report/2017/144`.

38   Igor Carboni Oliveira. Randomness and Intractability in Kolmogorov Complexity. In *International Colloquium on Automata, Languages, and Programming* (ICALP), pages 32:1–32:14, 2019.

39   Igor Carboni Oliveira, Ján Pich, and Rahul Santhanam. Hardness magnification near state-of-the-art lower bounds. In *Computational Complexity Conference* (CCC), 2019.

40   Igor Carboni Oliveira and Rahul Santhanam. Majority is Incompressible by $\text{AC}^0[p]$ Circuits. In *Conference on Computational Complexity* (CCC), pages 124–157, 2015.

41   Igor Carboni Oliveira and Rahul Santhanam. Conspiracies Between Learning Algorithms, Circuit Lower Bounds, and Pseudorandomness. In *Computational Complexity Conference* (CCC), pages 18:1–18:49, 2017. `doi:10.4230/LIPIcs.CCC.2017.18`.

42   Igor Carboni Oliveira and Rahul Santhanam. Hardness Magnification for Natural Problems. In *Symposium on Foundations of Computer Science* (FOCS), pages 65–76, 2018. `doi:10.1109/FOCS.2018.00016`.

43   Alexander A. Razborov. Lower bounds on the monotone complexity of some Boolean functions. *Doklady Akademii Nauk SSSR*, 281:798–801, 1985. English translation in: Soviet Mathematics Doklady 31:354–357, 1985.

44   Alexander A. Razborov. Lower bounds on the size of constant-depth networks over a complete basis with logical addition. *Mathematicheskie Zametki*, 41(4):598–607, 1987.

45   Alexander A. Razborov. Pseudorandom Generators Hard for k-DNF Resolution and Polynomial Calculus Resolution. *Annals of Mathematics*, 181(2):415–472, 2015.

46   Alexander A. Razborov and Steven Rudich. Natural Proofs. *J. Comput. Syst. Sci.*, 55(1):24–35, 1997. `doi:10.1006/jcss.1997.1494`.

47   Ben W. Reichardt. Reflections for quantum query algorithms. In *Symposium on Discrete Algorithms* (SODA), pages 560–569, 2011.

48   Michael Sipser and Daniel A. Spielman. Expander codes. *IEEE Trans. Information Theory*, 42(6):1710–1722, 1996. `doi:10.1109/18.556667`.

49   Roman Smolensky. Algebraic Methods in the Theory of Lower Bounds for Boolean Circuit Complexity. In *Symposium on Theory of Computing* (STOC), pages 77–82, 1987.

50   Aravind Srinivasan. On the approximability of clique and related maximization problems. *J. Comput. Syst. Sci.*, 67(3):633–651, 2003.

51   Avishay Tal. Shrinkage of De Morgan Formulae by Spectral Techniques. In *Symposium on Foundations of Computer Science* (FOCS), pages 551–560, 2014. `doi:10.1109/FOCS.2014.65`.

52   Avishay Tal. Formula lower bounds via the quantum method. In *Symposium on Theory of Computing* (STOC), pages 1256–1268, 2017. `doi:10.1145/3055399.3055472`.

53   Avishay Tal. Tight Bounds on the Fourier Spectrum of AC^0. In *Computational Complexity Conference* (CCC), pages 15:1–15:31, 2017. `doi:10.4230/LIPIcs.CCC.2017.15`.

54   Luca Trevisan and Tongke Xue. A Derandomized Switching Lemma and an Improved Derandomization of AC^0. In *Conference on Computational Complexity* (CCC), pages 242–247, 2013. `doi:10.1109/CCC.2013.32`.

55   Salil P. Vadhan. Pseudorandomness. *Foundations and Trends in Theoretical Computer Science*, 7(1-3):1–336, 2012. `doi:10.1561/0400000010`.

56   Andrew Chi-Chih Yao. Circuits and Local Computation. In *Symposium on Theory of Computing* (STOC), pages 186–196, 1989. `doi:10.1145/73007.73025`.

A   Review of Hardness Magnification in Circuit Complexity

A.1   Previous Work

We focus on some representative examples. For definitions and more details, check Section 2 or consult the original papers.

Srinivasan [50] (Informal).   If there exists $\varepsilon > 0$ such that $n^{1-o(1)}$-approximating MAX-CLIQUE requires boolean circuits of size at least $m^{1+\varepsilon}$ (where $m = \Theta(n^2)$), then $\mathsf{NP} \not\subseteq \mathsf{Circuit[poly]}$.

Allender-Koucký [3] and Chen-Tell [13]. The following results hold.

- Let $\Pi \in \{\mathsf{BFE}, \mathsf{W}_{\mathsf{S}_5}, \mathsf{W5\text{-}STCONN}\}$. Suppose that for each $c > 1$ there exist infinitely many $d \in \mathbb{N}$ such that TC^0 circuits of depth d require more than $n^{1+c^{-d}}$ wires to solve Π. Then, $\mathsf{NC}^1 \not\subseteq \mathsf{TC}^0$.
- Suppose that for each $c > 1$ there exist infinitely many $d \in \mathbb{N}$ such that MAJ cannot be computed by ACC^0 circuits of depth d with $n^{1+c^{-d}}$ wires. Then $\mathsf{MAJ} \notin \mathsf{ACC}^0$, and consequently $\mathsf{TC}^0 \not\subseteq \mathsf{ACC}^0$.

Lipton-Williams [35]. If there is $\varepsilon > 0$ such that for every $\delta > 0$ we have $\mathsf{CircEval} \notin \mathsf{Size\text{-}Depth}[n^{1+\varepsilon}, n^{1-\delta}]$, then for every $k \geq 1$ and $\gamma > 0$ we have $\mathsf{CircEval} \notin \mathsf{Size\text{-}Depth}[n^k, n^{1-\gamma}]$ (in particular $\mathsf{P} \not\subseteq \mathsf{NC}$).

Oliveira-Santhanam [42]. The following results hold.

- Let $s(n) = n^k$ and $\delta(n) = n^{-k}$, where $k \in \mathbb{N}$. If $\mathsf{MCSP}[(s, 0), (s, \delta)] \notin \mathsf{Formula}[N^{1+\varepsilon}]$ for some $\varepsilon > 0$, then there is $L \in \mathsf{NP}$ over m-bit inputs and $\delta > 0$ such that $L \notin \mathsf{Formula}[2^{m^\delta}]$.
- Suppose there exists $k \geq 1$ such that for every $d \geq 1$ there is $\varepsilon_d > 0$ such that $\mathsf{MCSP}[(s, 0), (s, \delta)] \notin \mathsf{AC}_d^0[N^{1+\varepsilon_d}]$, where $s(n) = n^k$ and $\delta(n) = n^{-k}$. Then $\mathsf{NP} \not\subseteq \mathsf{NC}^1$.
- Let $k(n) = n^{o(1)}$. If there exists $\varepsilon > 0$ such that $k\text{-Vertex-Cover} \notin \mathsf{DTISP}[m^{1+\varepsilon}, m^{o(1)}]$, where the input is an n-vertex graph represented by an adjacency matrix of bit length $m = \Theta(n^2)$, then $\mathsf{P} \neq \mathsf{NP}$.
- Let $k(n) = (\log n)^C$, where $C \in \mathbb{N}$ is arbitrary. If for every $d \geq 1$ there exists $\varepsilon > 0$ such that $k\text{-Vertex-Cover} \notin \mathsf{AC}_d^0[m^{1+\varepsilon}]$, then $\mathsf{NP} \not\subseteq \mathsf{NC}^1$.

Oliveira-Pich-Santhanam [39] and McKay-Murray-Williams [36] (Informal). If there exists $\varepsilon > 0$ such that for every small enough $\beta > 0$,

- $\mathsf{MCSP}[2^{\beta n}] \notin \mathsf{Circuit}[N^{1+\varepsilon}]$, then $\mathsf{NP} \not\subseteq \mathsf{Circuit}[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin \mathsf{TC}^0[N^{1+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{TC}^0[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin U_2\text{-Formula}[N^{3+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{Formula}[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin B_2\text{-Formula}[N^{2+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{Formula}[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin \mathsf{Formula\text{-}XOR}[N^{1+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{Formula}[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin \mathsf{BP}[N^{2+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{BP}[\mathsf{poly}]$.
- $\mathsf{MKtP}[2^{\beta n}] \notin (\mathsf{AC}^0[6])[N^{1+\varepsilon}]$, then $\mathsf{EXP} \not\subseteq \mathsf{AC}^0[6]$.

Many results for MKtP admit analogues for MrKtP, which considers a randomized version of Kt complexity introduced by [38]. An advantage of MrKtP is that strong unconditional lower bounds against uniform computations are known, while the hardness of problems such as MCSP and MKtP currently relies on cryptographic assumptions.

Chen-McKay-Murray-Williams [12]. The following results hold.

- If there is $\varepsilon > 0$, $c \geq 1$, and an n^c-sparse language $L \in \mathsf{NP}$ such that $L \notin \mathsf{Circuit}[n^{1+\varepsilon}]$, then $\mathsf{NE} \not\subseteq \mathsf{Circuit}[2^{\delta \cdot n}]$ for some $\delta > 0$.
- If there is $\varepsilon > 0$ such that for every $\beta > 0$ there is a 2^{n^β}-sparse language $L \in \mathsf{NTIME}[2^{n^\beta}]$ such that $L \notin \mathsf{Circuit}[n^{1+\varepsilon}]$, then $\mathsf{NEXP} \not\subseteq \mathsf{Circuit}[\mathsf{poly}]$.

More recently, [10] established that many hardness magnification theorems for problems such as MCSP and MKtP hold in fact under the assumption that a *sufficiently sparse and explicit language* admits weak lower bounds. We refer to their work for more details.

A.2   Hardness Magnification Through the Lens of Oracle Circuits

We can view the results from Appendix A.1 as *unconditional* upper bounds on the size of small fan-in oracle circuits solving the corresponding problems, for a certain choice of oracle gates. In a magnification theorem, it is important to upper bound the uniform complexity of the oracle gates. For our discussion, this is not going to be relevant.

We repeat here a definition from Section 2, for convenience of the reader.

▶ **Definition 62** (Local circuit classes). *Let $\mathcal{C}$ be a circuit class (such as $\mathsf{AC}^0[s]$, $\mathsf{TC}^0_d[s]$, $\mathsf{Circuit}[s]$, etc). For functions $q, \ell, a \colon \mathbb{N} \to \mathbb{N}$, we say that a language L is in $[q, \ell, a]-\mathcal{C}$ if there exists a sequence $\{E_n\}$ of oracle circuits for which the following holds:*

  (i) *Each oracle circuit E_n is a circuit from $\mathcal{C}$.*
 (ii) *There are at most $q(n)$ oracle gates in E_n, each of fan-in at most $\ell(n)$, and any path from an input gate to an output gate encounters at most $a(n)$ oracle gates.*
(iii) *There exists a language $\mathcal{O} \subseteq \{0,1\}^*$ such that the sequence $\{E_n^{\mathcal{O}}\}$ (E_n with its oracle gates set to $\mathcal{O}$) computes L.*

In the definition above, q stands for *quantity*, ℓ for *locality*, and a for *adaptivity* of the corresponding oracle gates.

The fact that existing magnification theorems produce such circuits is a consequence of the algorithmic nature of the underlying proofs, which show how to reduce an instance of a problem to shorter instances of another related problem. By inspection of each proof, it is possible to establish a variety of upper bounds. We explicitly state some of them below.

▶ **Proposition 63.** *The following results hold.*

- [3] *For every* $\Pi \in \{\mathsf{BFE}, \mathsf{W}_{\mathsf{S}_5}, \mathsf{W5\text{-}STCONN}\}$ *and every* $\beta > 0$, $\Pi_n \in \left[O\left(n^{1-\beta}\right), n^\beta, O\left(\frac{1}{\beta}\right)\right]-\mathsf{TC}^0[O(n)]$.
- [35] *For every* $\delta > 0$, $\mathsf{CircEval}_n \in \left[n \cdot \mathsf{poly}(\log n), n^\delta, n^{1-\delta}\right]-\mathit{Circuit}[n \cdot \mathsf{poly}(\log n)]$.
- [42] *For every constructive function* $n \leq s(n) \leq 2^n/\mathsf{poly}(n)$ *and parameter* $0 < \delta(n) < 1/2$, $\mathsf{MCSP}[(s, 0), (s, \delta)] \in [N, \mathsf{poly}(s/\delta), 1]-\mathsf{Formula}[N \cdot \mathsf{poly}(s/\delta)]$.
- [42] *Let* $k = (\log n)^C$, *where* $C \in \mathbb{N}$. *Then* $k\text{-}\mathsf{Vertex\text{-}Cover} \in [1, (\log n)^{4C}, 1]-\mathsf{AC}^0_d[m^{1+\varepsilon}]$, *where* $\varepsilon_d \to 0$ *as* $d \to \infty$.
- [39] *For every* $\beta > 0$ *and for every constructive function* $s(n) \leq 2^{\beta n}$, $\mathsf{Gap\text{-}MKtP} \in [N, \mathsf{poly}(s), 1]-\mathsf{Formula\text{-}XOR}[N \cdot \mathsf{poly}(s)]$.
- [39] *For every constructive function* $s(n) \leq 2^n/\mathsf{poly}(n)$, *it follows that* $\mathsf{Gap\text{-}MCSP} \in [N \cdot \mathsf{poly}(s), \mathsf{poly}(s), \mathsf{poly}(s)]-\mathsf{Circuit}[N \cdot \mathsf{poly}(s)]$.
- [36] *For every constructive function* $s(n) \leq 2^n/\mathsf{poly}(n)$, *we have* $\mathit{MCSP}[s(n)] \in [O(N/\mathsf{poly}(s)), \mathsf{poly}(s), O(n/\log(s))]-\mathit{Circuit}[N/\mathsf{poly}(s)]$.

We stress however that not every hardness magnification theorem needs to lead to an unconditional construction of efficient oracle circuits. (All the proofs that we know of produce such circuits though.)